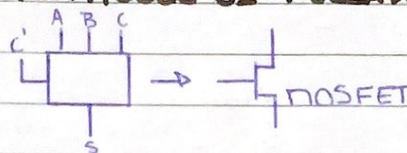


I. Sistemas Computacionais - Atividade 01: FULLADDER

NOME: Julia Pravato

Nº USP: 14615054

DATA: 30/03/2023



→ Tabela da verdade

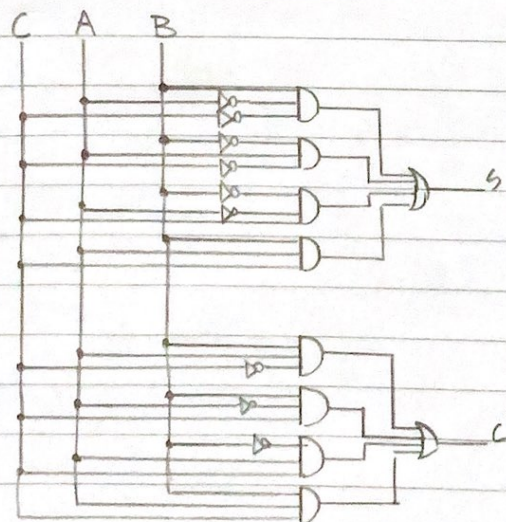
C	A	B	S	C'
0	0	0	0	0
0	0	1	1	0
0	1	0	1	0
0	1	1	0	1
1	0	0	1	0
1	0	1	0	1
1	1	0	0	1
1	1	1	1	1

- Full Adder 3 bits na soma (carry)

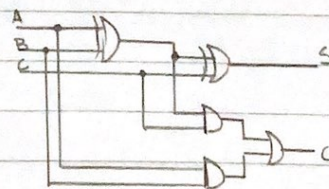
↳ Somador Completo de 1 bit

C | cin
C' | cout

→ Soma e Produto



75 transistores mosfet



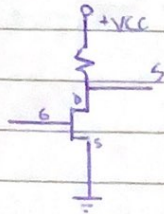
→ Fulladder: portas lógicas em simplificação

___/___/___

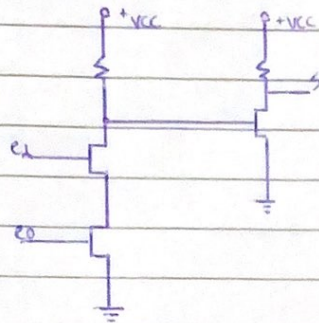
S T Q Q S S D

→ Portas Lógicas transistorizadas

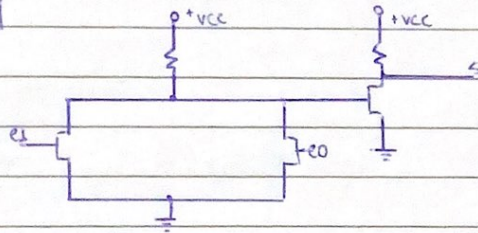
NOT

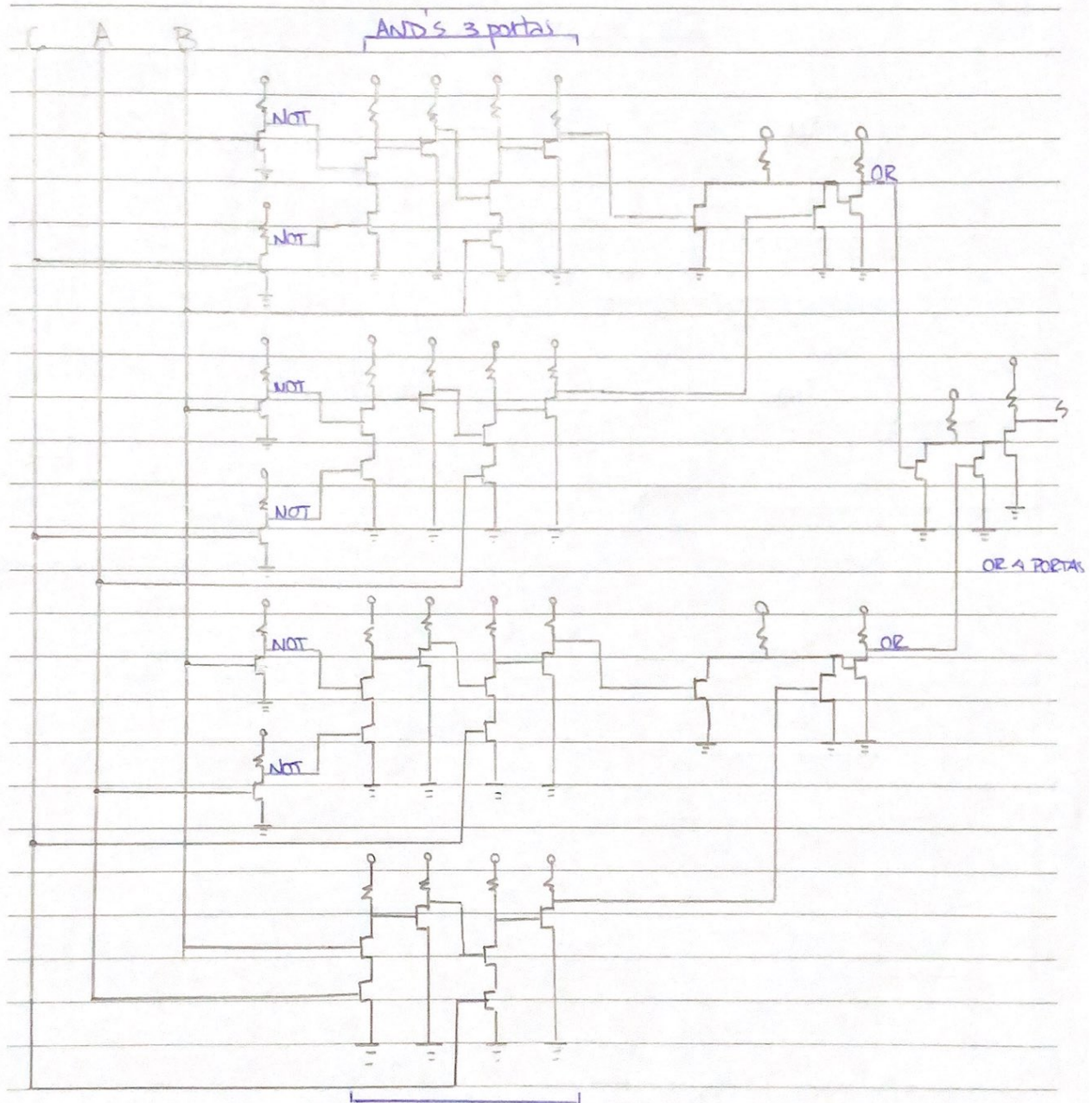


AND



OR

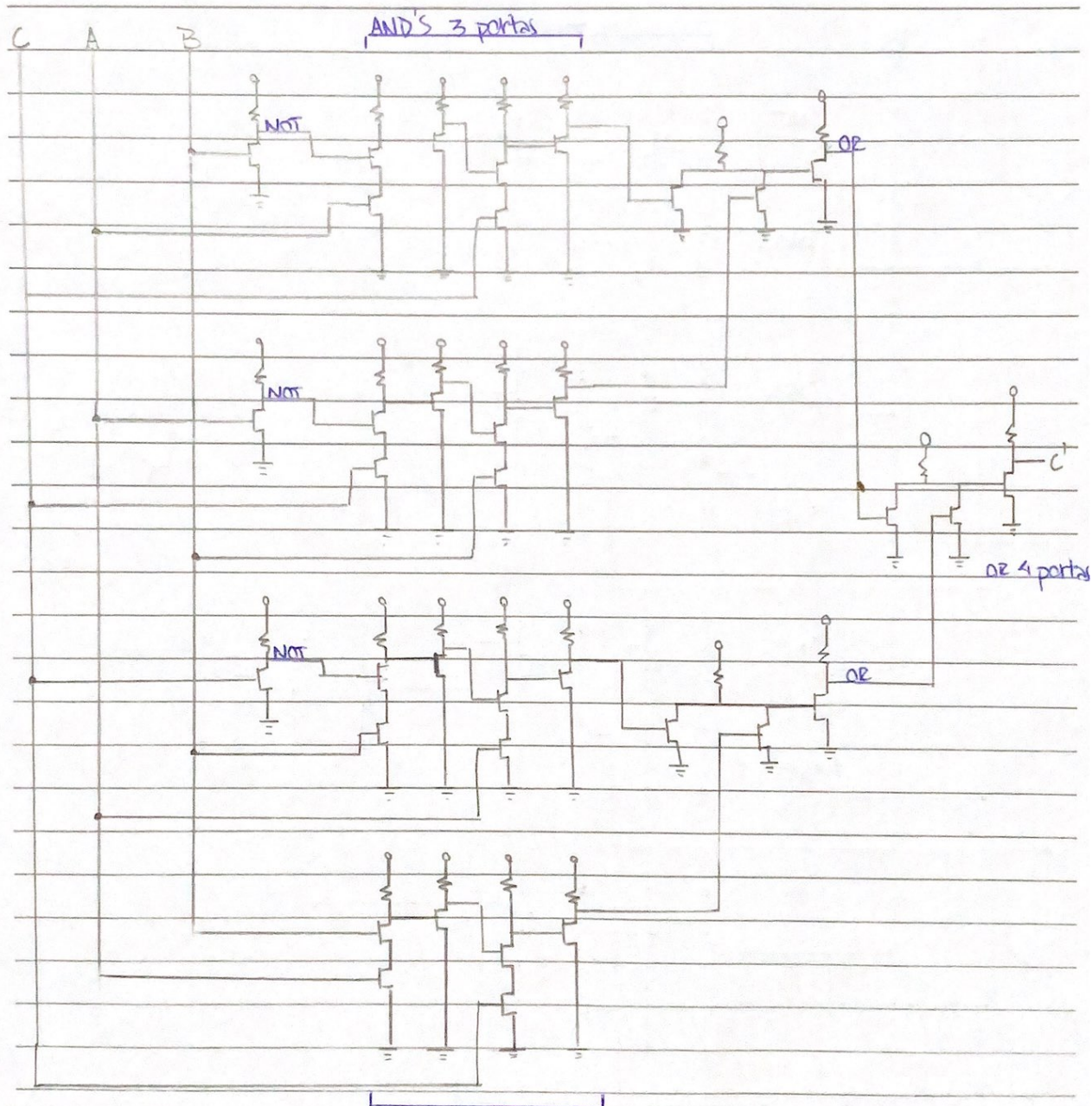




Saída S

__/__/__

S T Q Q S S D



Saída C'