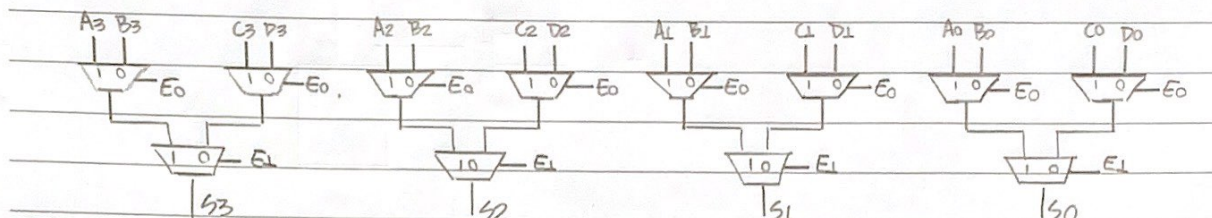
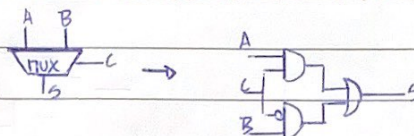


I. Sistemas Computacionais - Atividade 02: Multiplexador

NOME: Julia Prayato

Nº USP: 14615054

DATA: 15/04/2023

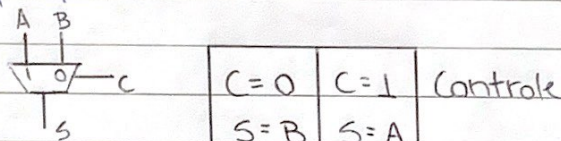


Entradas de 4 bits: A, B, C, D

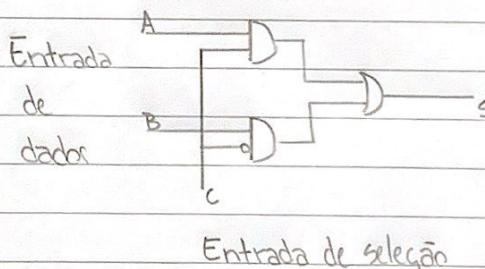
Saída de 4 bits: S

Pinos de controle de seleção: E0, E1

→ Operação



E1	E0	S
0	0	D
0	1	C
1	0	B
1	1	A



$$S = AC + B\bar{C}$$

Função: combinar múltiplas entradas em um único fluxo de dados.