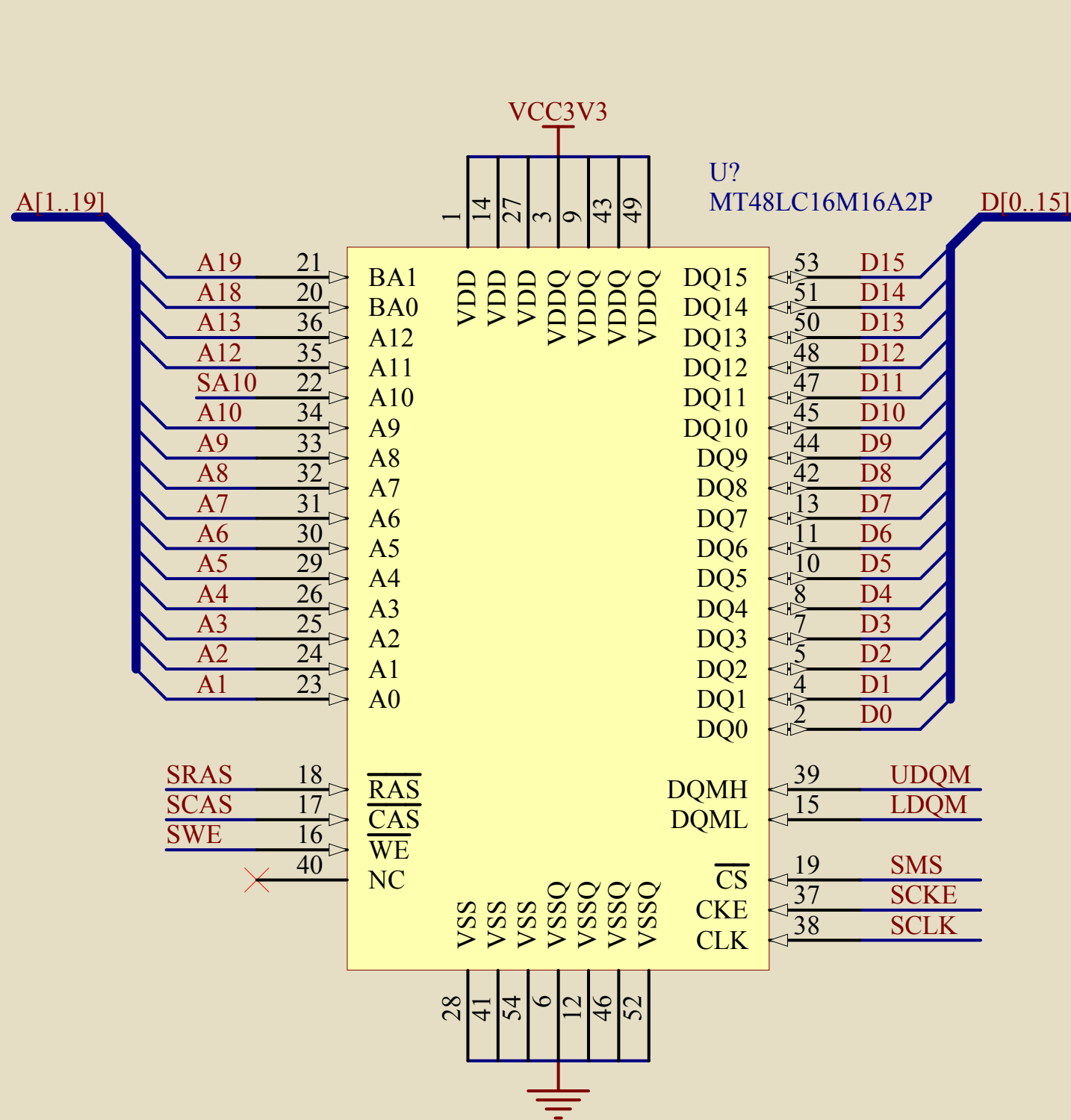
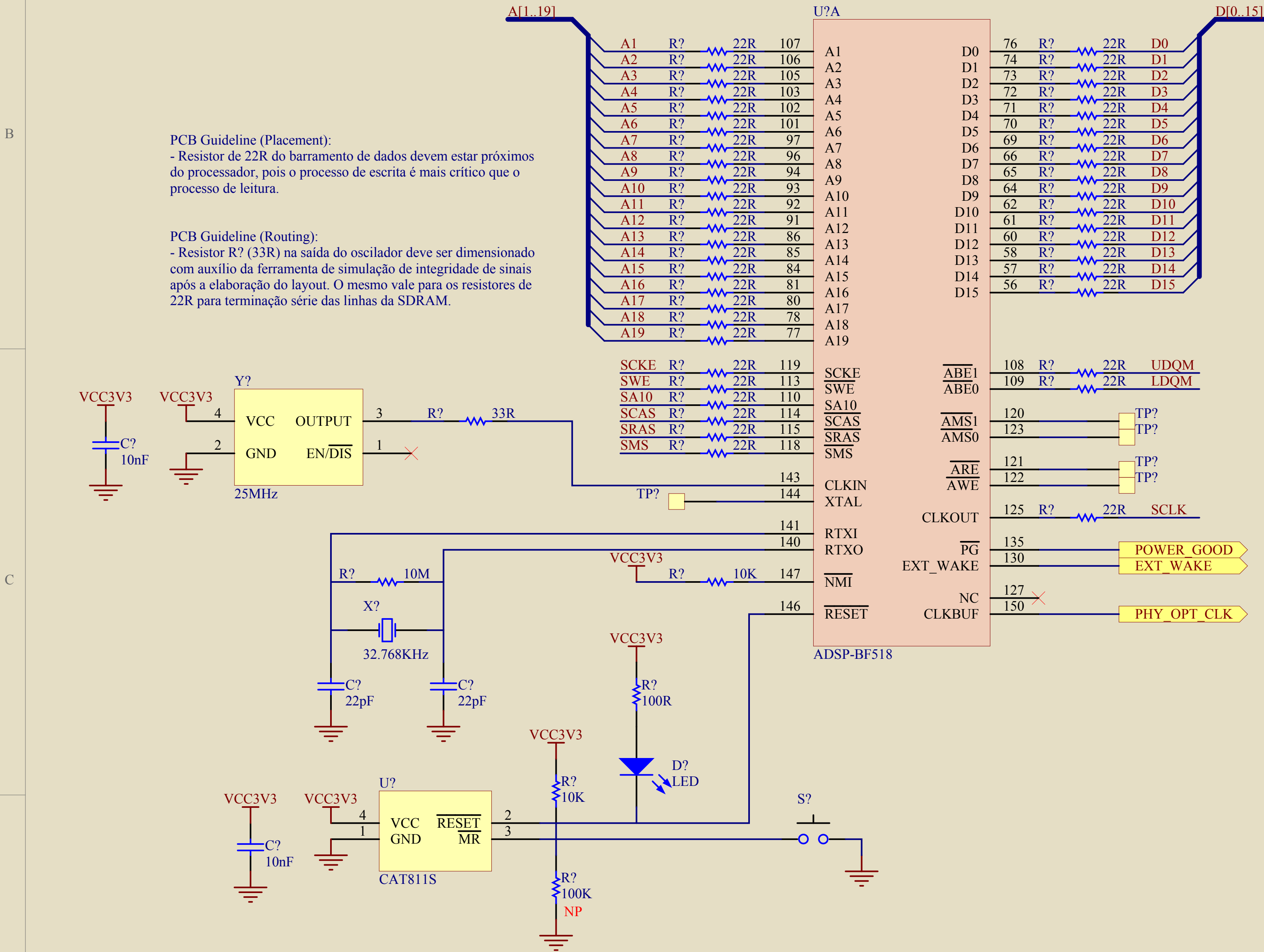


A

B

C

D

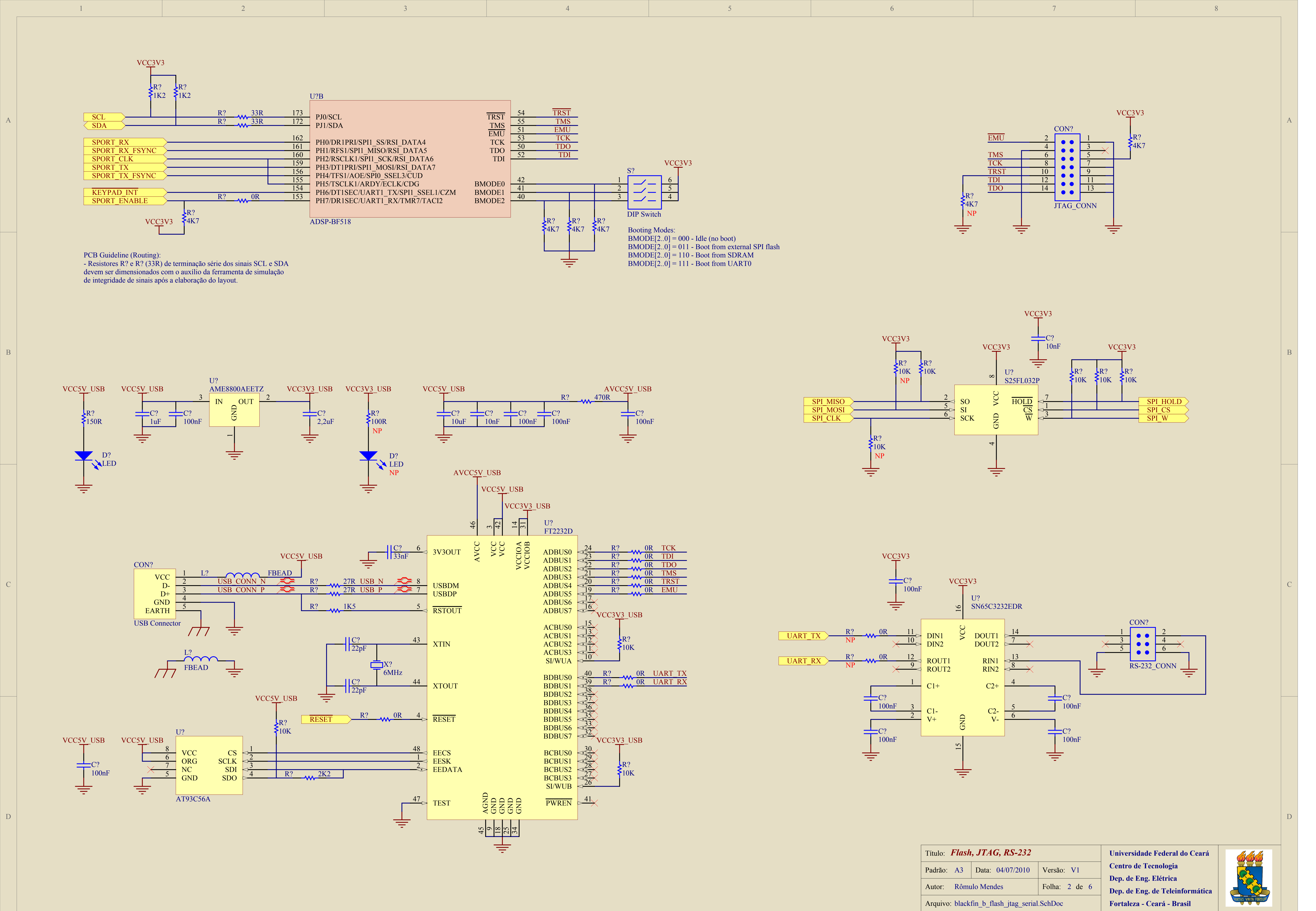


A

B

C

D





A

PCB Guideline (Routing):

- O roteamento dos sinais MII deve manter uma diferença de comprimento entre as linhas de no máximo 5,08 cm. Além disso, o roteamento da trilha mais extensa não deve ultrapassar 15,24 cm.
- A ligação entre PFBIN1/2 e PFBOUT deve ser feita com trilhas espessas.
- Evitar descontinuidade de planos no roteamento dos sinais UTP

Atentar para a descontinuidade envolvendo GND e EARTH.

- Resistor R? (33R) na saída do oscilador deve ser dimensionado com auxílio da ferramenta de simulação de integridade de sinais após a elaboração do layout.
- Todos os resistores de terminação série do projeto devem ter seus valores validados através da ferramenta de simulação de integridade de sinais do Altium Designer.

B

Strap Options (PHY Ethernet):

PHY Address [PHYAD4..PHYAD0] - [00001] (default setting)

Auto-Negotiation Enable [AN\_EN] - [1] (default setting)

Auto-Negotiation [AN1..AN0] - [11] (default setting)

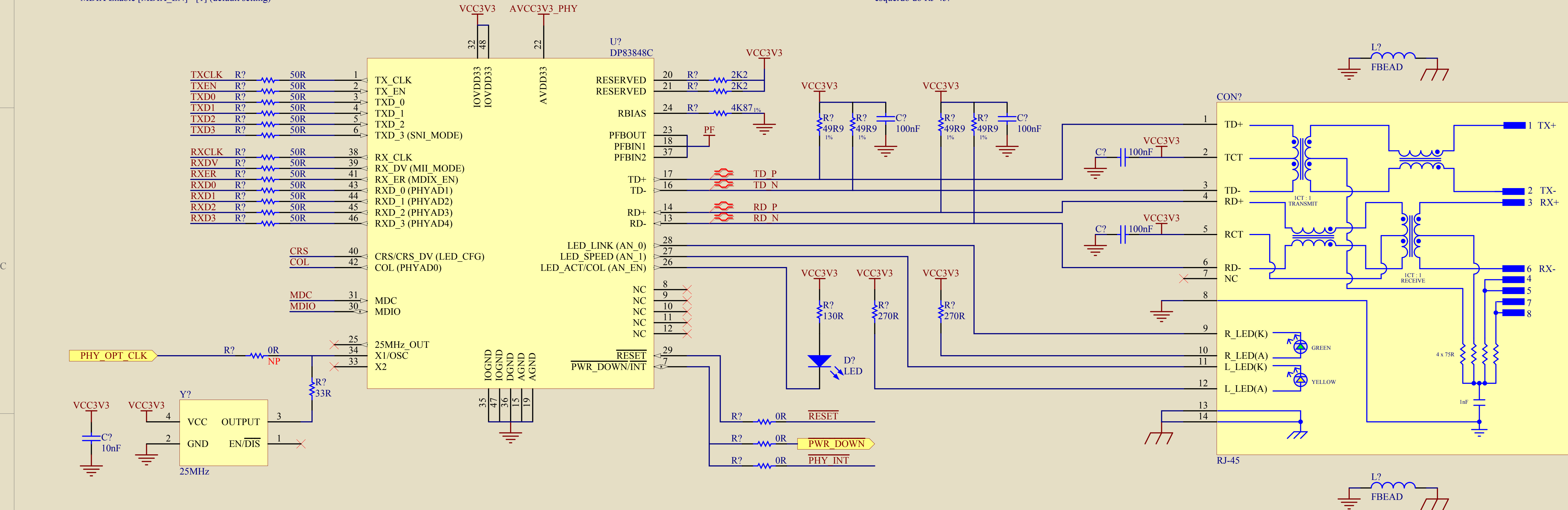
MAC Data Interface [MII\_MODE] - [0] (default setting)

SNI Interface [SNI\_MODE] - [X]

LED Interface [LED\_CFG] - [1] (default setting)

MDIX Enable [MDIX\_EN] - [1] (default setting)

C



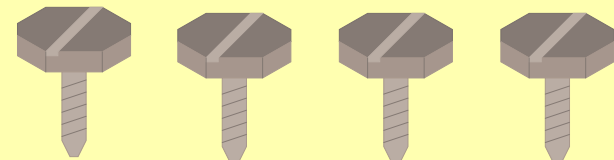
D



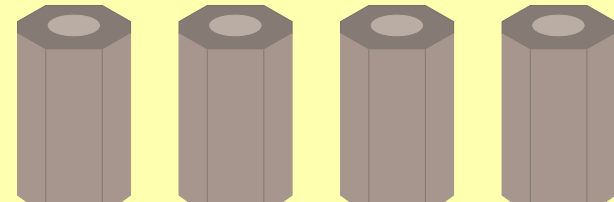
## LCD Character Module



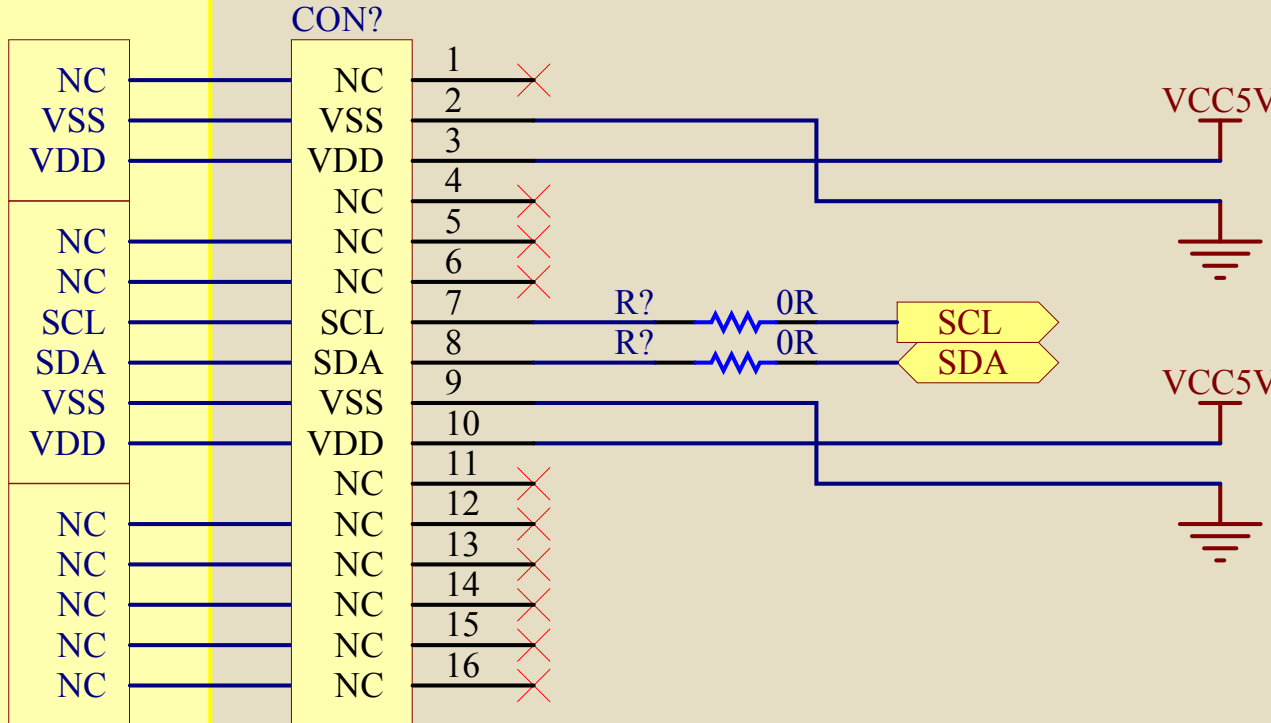
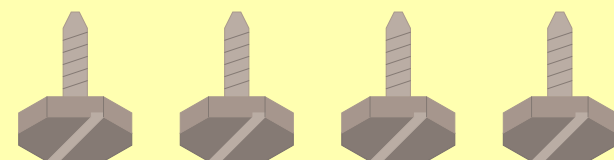
SCREW? SCREW? SCREW? SCREW?



SPACER? SPACER? SPACER? SPACER?



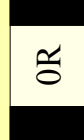
SCREW? SCREW? SCREW? SCREW?



R1

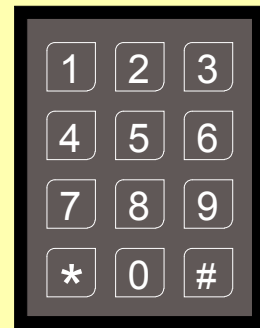


R2

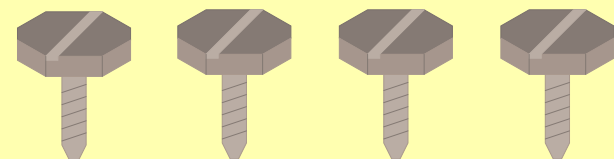


NP

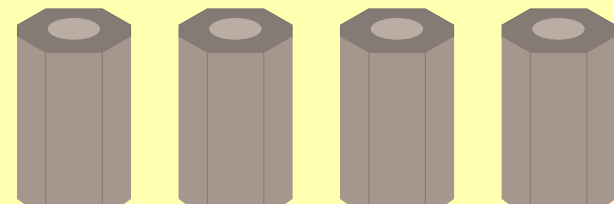
## Keypad



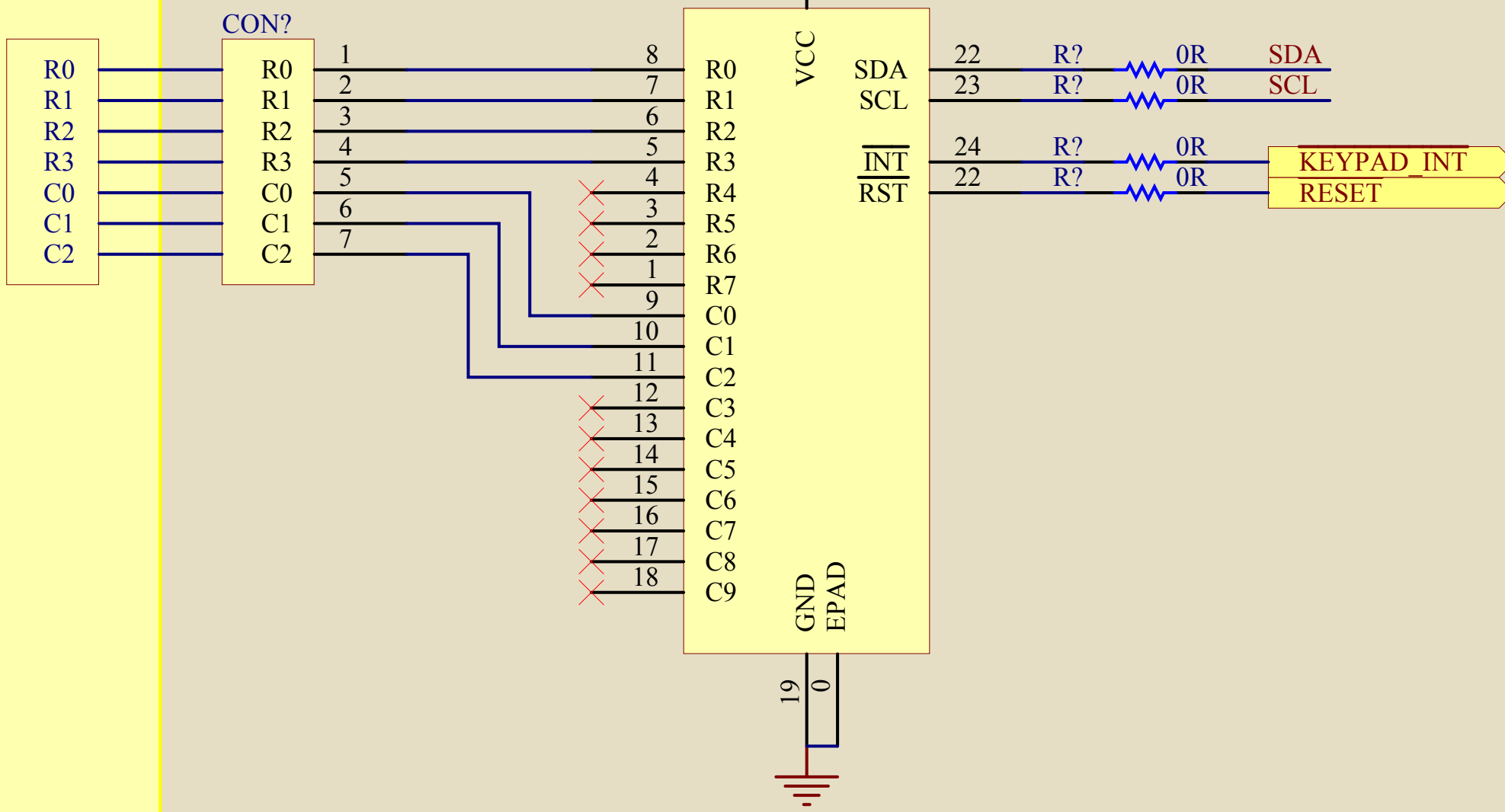
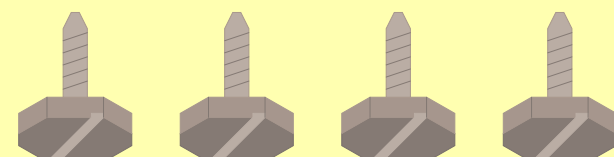
SCREW? SCREW? SCREW? SCREW?



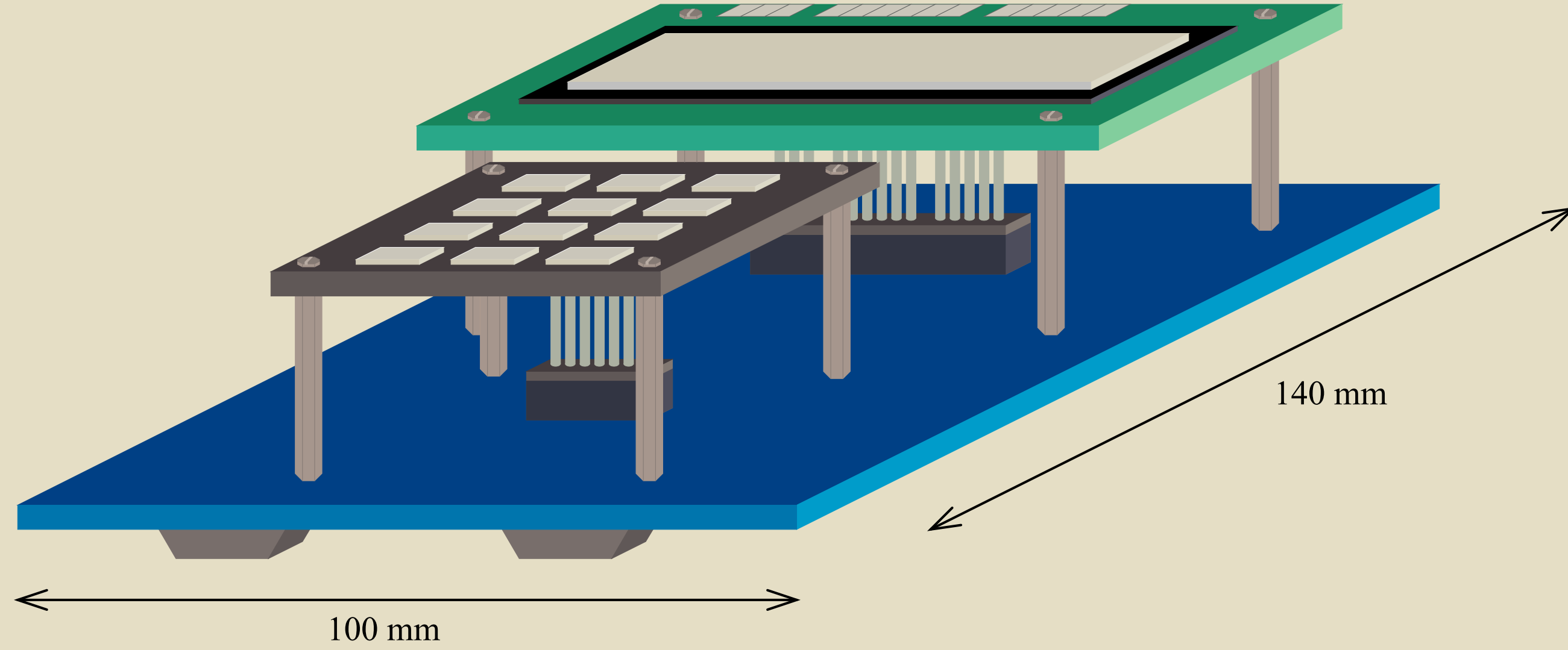
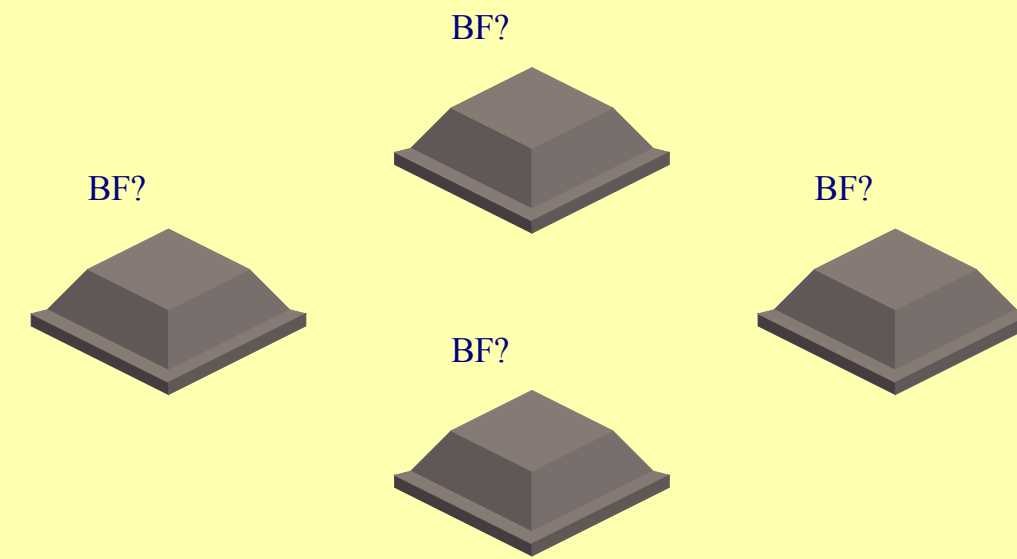
SPACER? SPACER? SPACER? SPACER?



SCREW? SCREW? SCREW? SCREW?



## Board Feet





\*a ser removido posteriormente  
PORTS POWER\_GOOD E EXT\_WAKE DEVEM  
SER USADOS NOS CIRCUITOS DA FONTE.  
PORTS PRESENTES TAMBEM NO SHEET A

POWER\_GOOD

