CSE 331L - 1 Introduction to Assembly Language

Introduction

In this session you will be introduced to assembly language programming and to the emusoss emolator software emososs will be used as both an editor and as an assemblen for all youn assembly language programming.

steps required to run an assembly program:

1. write the necessary assembly some code.

2. Save the, assembly sounce code.

3. Complete / Assemble source code to create machine code.

4. Emulate / Pun machine code

Microcontrollers us. Microprocessors

· A miero processor is a 2PU on a single chip-

· If a microprocessor, it's associated supported cincuitry memory, and peripheral 1/0 components are implemented on a single chip, It is a microcontroller.

Features of 3033

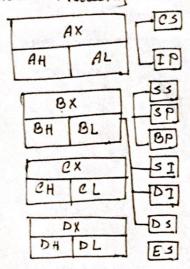
- · 8086 is a 16 bit processor. It's ALU, inhernal negisters work with 10-67. binary word.
- · 3086 has a 16 bit data bus. It can nead or write data to a memory/port either 16 bits on 8 bits at a time.
- . 3036 has a 20 bit address bus which means, it can address up to 220 = IMB memory beation.

Registrar - Register - Resistor

- · Both ALU & FPU have a very small amount of super-fast private memory placed right next to them for their exclusive use. These are negisters.
- The ALOU & FPU stone intermediate and final results from their calculations in these registers.
- · Processed data goes back to the data cacle and then to the main memory from these registers.

Inside the epu: bet to know the various registers

Central Processing Unit (or epu)



seneral Peurpose Registers (GPR)

- · Ax The Accumulator register
- · BX The Base Address negister
- · CX The count negister.
- · Dx The Data negister
- ·SI Source Indere register
- · BI Base Index negister
- · BP Base Pointer
- · Sp Stack Pointer

Despite the name of a negister, It's the programmer who determines the usage for each general-purpose register. The main purpose of a negister is to keep a numbber. The size of the above negister is libits.

4 general-purpose registers are made of Luo separates 8-bit negisters.

Since registers are located inside epu, they are much fasters than memory. Accessing a memory location requires the use of a system bus, so it takes much longer. Accessing data in a register usually takes no time.

Segment Registers

cs-points at the segment containing the current program.

DS-generally points at the segment where variables are defined.

Es-extra segment negister.

SS- points at the segment containing the stack

Special Purpose Rogister

- ·IP-The Instruction Ponter.
- · flag Register Determines the current state of the microprocessor.

Driting Your First Assembly Code

The following table shows the instruction name, the syntax of it's use, and it's description.

- · REG: Any valid register
- · Memory: Referring to a memory location in RAM
- · Immediate: Using direct values.

Instruction	Operation	Description
MOV	RESE, memory. memory, RESE RESE. RESE memory, Imanediate RESE. imaediate	· cannot set the value of 25
ADD	memory REG REG	Add two number. Algorithm: operand: operand: + operand 2.

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Variables, 1/0, Array

Creating Variable

solutax for a variable declaration

name DB value.

DB- stands for Define Byte Dw. stands for Define Word.

• name: can be any letter or digit combination, though it- should start with letter.

system or 9" symbol for variables that are not initialized.

Creating Constants

Constanto are just like variables, but they exist only until the program is composted. To define constants EQU directinais used.

for example:

K EQU 5

MOV AX, K

Creating Armays

Arrosps can be seen as chains of variables. A text string is an example of a bett array.

Here are some array definition examples!

a DB 48h, 65h, 6ch, 6fh, ooh

6 DB 'Hello', D

· You can access the value of my element in array using square brackets.

MOV AL, 9[3]

· You can also use any of the memory index registers BX.SI. DI, BP.

MOV SI. 3 MOV AL, a[SI]

· If you need to declare a large array you can use

The syntax for DUP

number DUP (value (s))

number - number of duplicates to make any constant value. value - expression that DUP will duplicate.

e DB 5 Dup(0)
is an alternating way of declaring
e DB 9,9,9,9,9

Memory Access

To access memory, we can use these four registers: BX, SI, DI, BP.

[8x+31] [8x+D1] [8p+31] [8p+ D1]	[01] [01] diccorriable About only) [0x]	[8x + 51 + 48] [8x + 51 + 48] [8p + 51 + 48]
[0x + 98]	[8x + 3] + d16]	[31 + d16]
[0b1 98]	[8x + DI + d16]	[DI + d16]
[01 + 98]	[8P+3] + d16]	[OP + d16]
[2] + 98]	[8P+DI + d16]	[OX+d16]

- · Displacement can be an immediate value or offset of a pariable or even both.
- Displacement can be inside or actside of the [] symbols, assembler generates the beame machine code for both ways.
- · Displacement is a signed value.

Instruction

Instructions	Operands	Description
INC	rem Mem	Increament Algorithm opperand: operand+1 Example: MOV AL, 4 INC AL; AL: 5 RET
DEC	REG MEM	Decreament Algorith operand = operan-1 Example; MOV AL, 86 DEC AL; AL: 85 RET
LEA	REG., HEM	Load Effective Address Algorithm: REG: address of memory (offset) Exeample: HOV BX, 954 MOV DI, 12h LEA SI, [BX+DI]

Offset

offset is an assembler direction in X86 assembly language. It actually means 'address' and is a way of handling the over leading of move instructions

1- mov si, offset variables

2. move, si, gariables

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Print: Hello world in Assembly Language

DATA SEGMENT

MESSAGE DB "HELLO WORLD!!!\$"

ENDS

CODE SEGMENT

ASSUME DS: DATA CS: CODE

START:

MOV AX, DATA

MOV DS. AX

LEA DX, MESSAGE

MOV AH, 9

INT 21H

MOV AH, 4CH

INT 21H

ENDS

First Line - DATA SEGMENT

END START

DATA SEGMENT is the starting point of the Data Segment and Program and DATA is the name given to this segment we can declare over variables.

Nant Line - MESSAGE DB "HELLO WORLD!!! \$"

HESSAGE is the variable name given to a Data Type (Size) that is DB. DB stands for Define Byte and is of One byte (8 bits). In Assembly language programs, variables are defined by Data Size not it's Type. Character need one By So to store character or string we need DB only that don't mean DB ean't hold number or numerical value.

Next Line-DATA ENDS

DATA ENDS is the End point of the Data segment in a program.

Next Line- CODE SEGMENT

CODE SEGMENT is the starting point of the Code segment in a Program and CODE is the name given to this segment and SEGMENT is the togodord for defining segments.

Next line - ASSUME DS: DATA CS; CODE

In this Assembly Longuage programming, there are Different Register present for Different Purpose so we have to assume DATA is name to Code Segment Register and CODE is the Next Line-START

START is the lebel used to show the storting point of the code in C programming.

Next Line - MOV AX, DATA

MOV DS , AX

After assuming DATA and CODE Segment, still it is the compulsory to mitialize DATA Segment to DS Register. MOV is a regressed to move the second element into the First element. But we cannot use move DATA Directly to DS due to MOV commands restriction, hence the most important register in ALU.

Next Line - MOV AH, 4CH

TAT 21H

The above two line code is used to exit to dos or exit to operating system. Standard input and Standard output melated Interrupt are found in INT 21 H which is also evalued Next line-CODE ENDS

CODE ENDS is the time END points of the Code Sogment in a Program

END START is the end of the lebel used to show the ending point of the code which is written in code Segment.