Elements	Description	Params	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17
I1	Inverter	S	1	1	1	1	1	1	1	1	1	1	1						
I2	Txgate	S	1	1	1	1	1	1	1	1	1	1	1						
I3	Inverter	S	1	1	1	1	1	1	1	1	1	1	1						
I4	Txgate	S	1	1	1	1	1	1	1	1	1	1	1						
I5	Txgate	S	1	1	1	1	1	1	2	2	2	2	2						
I6	Inverter	S	1	3	3	3	3	3	8	8	8	8	8						
I7	Txgate	S	1	1	1	1	1	1	1	1	1	1	1						
I8	Inverter	S	1	3	3	3	3	3	4	4	4	4	4						
I9	2Phase	S_TX	1	1	1	1	1	1	1	1	1	4	4						
		S_INV1	1	1	1	1	1	1	1	1	1	6	6						
		S_INV2	1	1	1	1	1	1	1	1	1	6	6						
		S_INV3	1	1	1	1	1	1	1	1	1	4	4						
I10	Inverter	S	1	1	1	1	1	1	1	1	1	20	20						
I12	Inverter	S	1	1	1	1	1	1	1	1	1	1	1						
I18	Nand	S	1	3	3	3	3	3	3	3	3	3	3						
I19	Inverter	S	1	1	1	1	1	1	1	1	1	1	1						
I27	2Mux	S	1	6.5	6.5	6.5	6.5	6.5	6.5	1	1	1	1						
		S_INV	1	1	1	1	1	1	1	1	1	1	1						
		S_TX	1	1	1	1	1	1	1	1	1	1	1						
I28	Linebuff	INV1	1	8	1	1	1	1	1	1	1	1	1						
		INV2	3.15	17	3.15	3.15	3.15	3.15	3.15	3.15	2	2	2						
		INV3	9.9225	38	9.9225	9.9225	9.9225	9.9225	9.9225	9.9225	3	3	3						
		INV4	31.25	60	31.25	31.25	31.25	31.25	31.25	31.25	8	8	8						
I29	Xor_2Fork	S	1	9	9	1	1	3	1	1	1	1	1						
		S_FORK_1	1	1	1	1	1	1	1	1	1	1	2						
		S_FORK_2a	1	2	2	1	2	2	2	2	2	2	2						
		S_FORK_2b	1	2	2	1	2	2	2	2	2	2	3						
		S_XOR	1	2	2	1	3	3	3	3	3	3	3						
		Explanation of changes:	Default	Eyeballed values	Revert changes to line buffer	Revert changes to XOR	Apply only internal telescoping to XOR	Apply small scale factor to XOR	Revert XOR scale factor and apply telescoping from CLK->QNOT path	Revert Mux Scaling	Try less steep line buffer telescoping	Try higher sizing on 2-phase clock to remove initial delay on CLK->!Q	Try resizing XOR telescoping for better CLK->OUT						
		Measurements																	
Delays (ps)		CLK -> !Q	161.67	248.697	248.697	150.792	154.995	179.352	149.591	143.901	143.901	131.226	134.678						
		CLK -> B_OUT	128.873	225.71	225.71	132.717	136.507	158.82	136.012	130.515	130.515	102.046	105						
		CLK -> G_OUT	311.362	324.235	324.235	312.672	257.994	261.519	257.698	250.485	250.485	221.118	201.13						
		CLK -> OUT	411.755	525.31	525.31	481.147	452.518	462.605	452.291	425.128	425.128	395.912	383.621						
		CLK -> CONT1	167.01	271.7	271.7	176.546	180.687	204.63	180.501	174.724	174.724	145.62	148.766						