

FAST-NATIONAL UNIVERSITY OF COMPUTER AND EMERGING SCIENCES PESHAWAR CAMPUS

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ROLL NO: 22P-9200

SECTION: BS(CS)-3D

COURSE: COAL

ASSIGNMENT 2

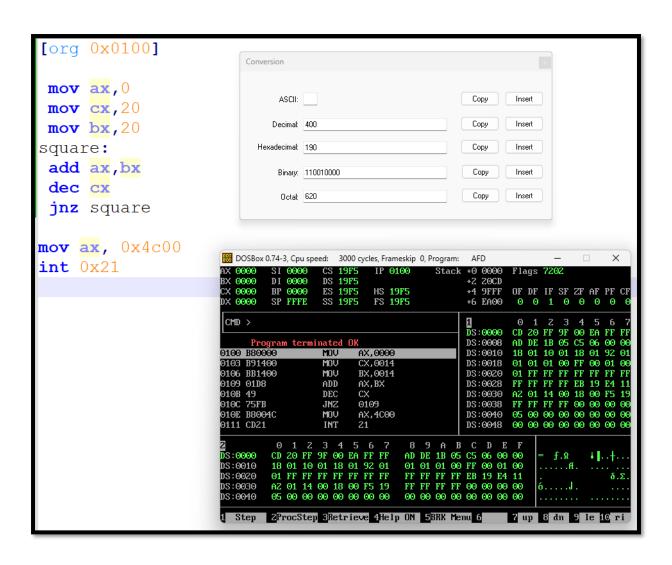
SUBMITTED TO: Sir Usman Abassi

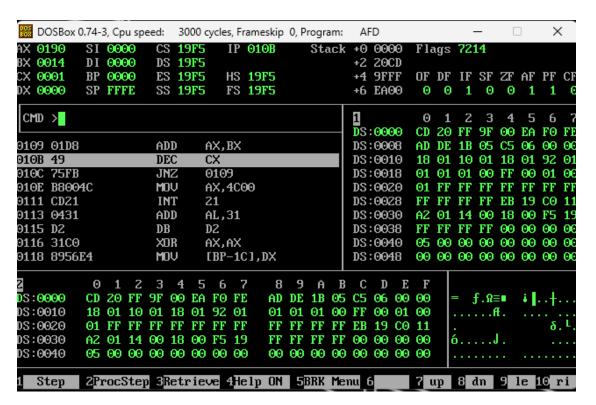
QUESTION #1

```
[org 0x0100]

mov ax,0
mov cx,20
mov bx,20
square:
  add ax,bx
  dec cx
  jnz square

mov ax, 0x4c00
int 0x21
```





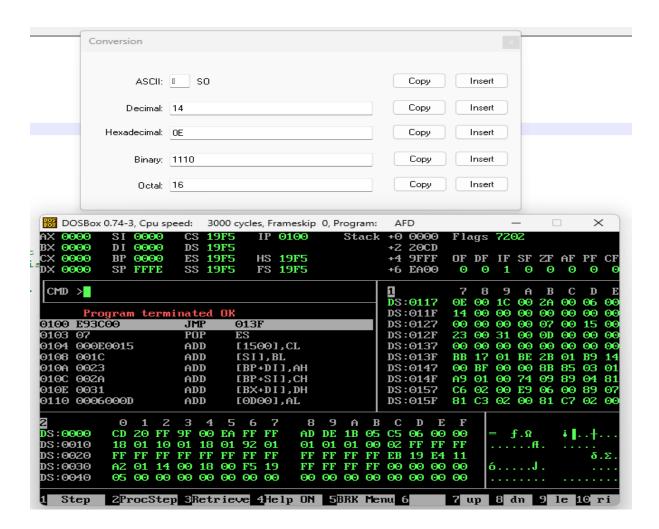
Question#2

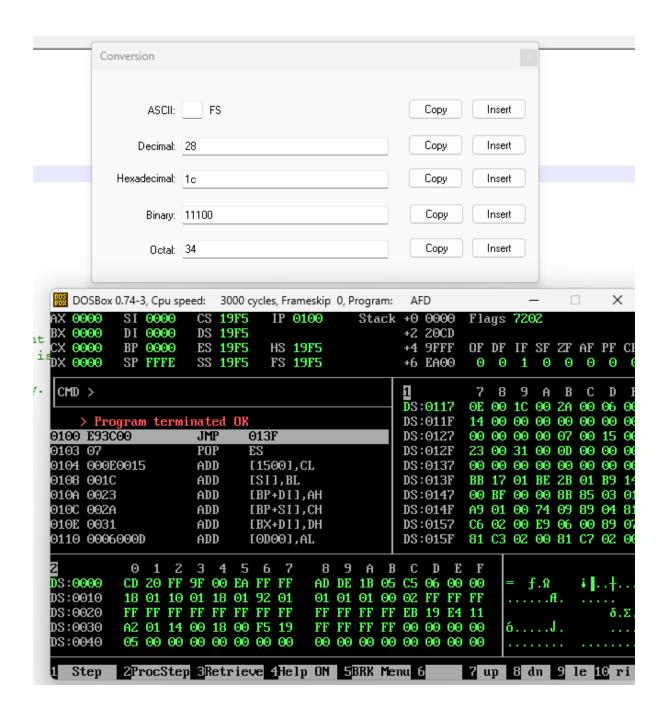
```
; Explanation in the code
[org 0x0100]
imp start
array:dw 7, 14, 21, 28, 35, 42, 49, 6, 13, 20
evenn:dw 0, 0, 0, 0, 0, 0, 0, 0, 0, 0 odd :dw 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0, 0
start:
 mov bx, evenn
 mov si, odd
 mov ex, 20
 mov di, 0
 mov ax, [array + di]
 test ax, 1 ; checking the reason organization iz even 1 ; Jump to even label if the number is even
 mov [si], ax
                             ; Storing odd number in odd array.
 add si, 2
 jmp next
even1:
 mov [bx], ax
                    ;storing even number in even array
 add bx, 2
next:
  add di, 2
  cmp di, ex
  inz check
    mov ax, 0x4c00
    int 0x21
```

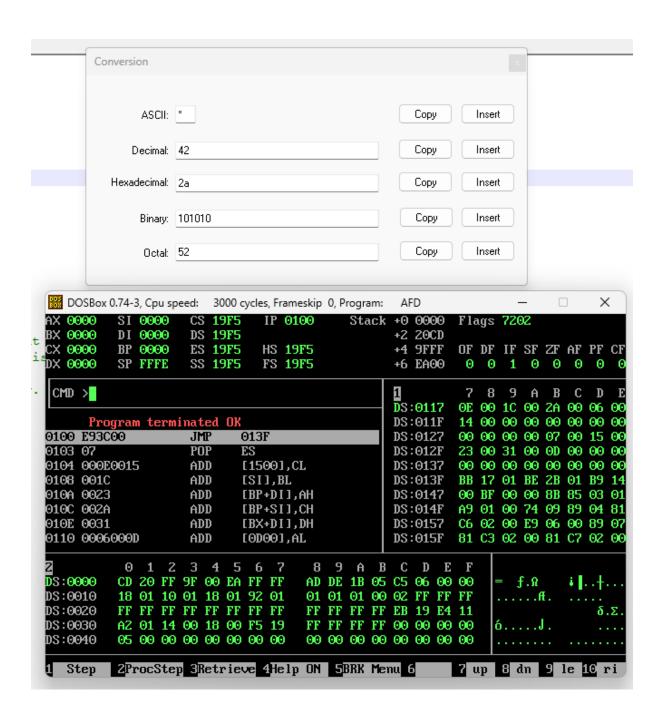
Explanation:

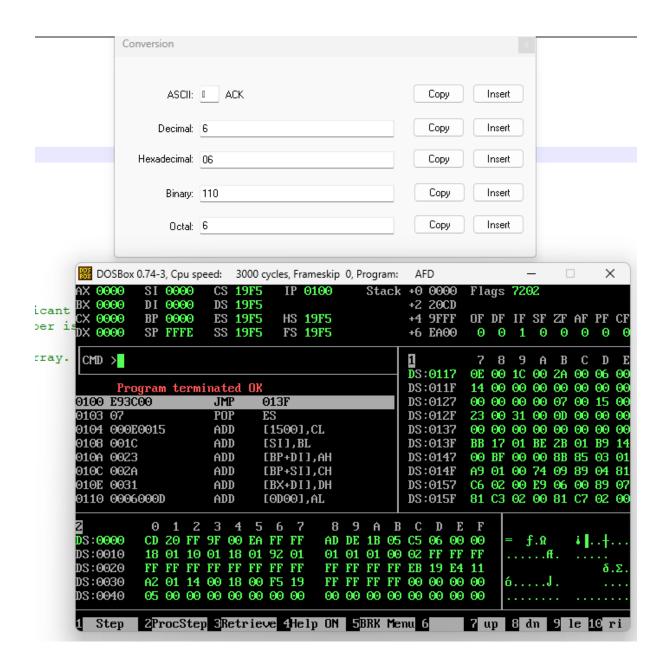
In this code to find out the even and odd from the array of numbers .first of all I am storing 0's in evenn and odd array that will be later updated by the even and odd numbers . so to find this in used test wich will check the least significant bit and it will jump accordingly to the even and odd ones.

```
[org 0x0100]
jmp start
array:dw 8, 15, 22, 29, 36, 43, 50, 7, 14, 21
evenn:dw 0, 0, 0, 0, 0, 0, 0, 0, 0, 0
odd :dw 0, 0, 0, 0, 0, 0, 0, 0, 0
start:
  mov bx, evenn
 mov si, odd
 mov cx, 20
 mov di, 0
check:
  mov ax, [array + di]
                                       ; checking the least significant bit .
  test ax, 1
                               ; Jump to even label if the number is even
 jz even1
  mov [si], ax
                                    ; Storing odd number in odd array.
  add si, 2
 jmp next
even1:
 mov [bx], ax
                         storing even number in even array;
  add bx, 2
                                    DOSBox 0.74-3, Cpu speed: 3000 cycles, Frameskip 0, Program: AFD
                                               SI 0000 CS 19F5 IP 0100
DI 0000 DS 19F5
BP 0000 ES 19F5 HS 19F5
SP FFFE SS 19F5 FS 19F5
                                                                                         Stack +0 0000 Flags 7202
+2 20CD
                                                                         IP 0100
next:
                                   BX 0000
                                                                                                           OF DF IF SF ZF AF PF CF
0 0 1 0 0 0 0
                                   CX 0000
DX 0000
                                                                                                 +4 9FFF
   add di, 2
                                                                                                 +6 EA00
   cmp di, cx
                                                                                                CMD >
   jnz check
                                   > Program terminated OK
0100 E93C00 JMP 6
      mov ax, 0x4c00
                                                                     013F
      int 0x21
                                   0103 0800
0105 0F0016001D
                                                             OR
LLDT
                                                                     [BX+SI],AL
W/[1D00]
                                                                      [SI],AH
[BP+DI],CH
                                   010A 0024
                                                             ADD
                                   010C 00ZB
                                                             ADD
                                    010E 0032
                                                             ADD
                                                                      [BP+SI],DH
                                                                                                DS:0040 05 00 00 00 00 00 00 00 00 DS:0048 00 00 00 00 00 00 00 00 00
                                                                      [BX],AL
[1500],CL
                                    0110 0007
                                                             ADD
                                    0112 000E0015
                                                            ADD
                                               0 1 2 3 4 5 6 7
CD 20 FF 9F 00 EA FF FF
18 01 10 01 18 01 92 01
FF FF FF FF FF FF FF
A2 01 14 00 18 00 F5 19
                                                                                 8 9 A B C D E F
AD DE 1B 05 C5 06 00 00
01 01 01 00 02 FF FF FF
FF FF FF FF EB 19 E4 11
FF FF FF FF 60 00 00 00
                                    DS:0000
                                                                                                                   = f.n
                                                                                                                               4 ...
                                                                                                                  = j.n
.....ft. .....
δ.Σ.
                                   DS:0010
                                    DS:0020
                                                                                                                  ó....J.
                                    DS:0030
                                    DS:0040
                                                05 00 00 00 00 00 00 00
                                                                                 00 00 00 00 00 00 00 00
                                   1 Step 2ProcStep 3Retrieve 4Help ON 5BRK Menu 6 7 up 8 dn 9 le 10 ri
```









Conversion		
		x
ASCII:	□ BEL	Copy Insert
	_	
Decimal:	7	Copy Insert
Decimal.		Сору
U avada da da al	07	Com Invest
Hexadecimal:	U7	Copy Insert
Binary:	111	Copy Insert
Octal:	7	Copy Insert
BOSBox 0.74-3, Cpu s	peed: 3000 cycles, Frameskip 0, Program:	: AFD − □ ×
X 0000 SI 0000	CS 19F5 IP 0142 Stack	k +0 0000 Flags 7200
X 0117 DI 0000	DS 19F5	+2 20CD
X 0000 BP 0000	ES 19F5 HS 19F5	+4 9FFF OF DF IF SF ZF AF PF (
X 0000 SP FFFE	SS 19F5 FS 19F5	+6 EA00 0 0 1 0 0 0 0
		-
CMD >		B C D E F 0 1
	MOU PU OLAF	DS:012B 07 00 15 00 23 00 31 (
013F BB1701	MOU BX,0117	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00 00 00 00 00 00 00
013F BB1701 0142 BE2B01	MOV SI,012B	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00 00 00 00 00 00 0 DS:013B 00 00 00 00 BB 17 01 B
013F BB1701 0142 BE2B01 0145 B91400	MOV SI,012B MOV CX,0014	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00 00 00 00 00 00 00 DS:013B 00 00 00 00 BB 17 01 B DS:0143 2B 01 B9 14 00 BF 00 0
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000	MDV SI,012B MDV CX,0014 MDV DI,0000	DS:012B 07 00 15 00 23 00 31 00 DS:0133 0D 00 00 00 00 00 00 00 00 00 00 00 00
013F BB1701 0142 BE2B01 0145 B91400	MDV SI,012B MDV CX,0014 MDV DI,0000 MDV AX,[0103+DI]	DS:012B 07 00 15 00 23 00 31 0 DS:013B 0D
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 0148 8B850301	MDV SI,012B MDV CX,0014 MDV DI,0000	DS:012B 07 00 15 00 23 00 31 0 DS:013B 0D
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100	MDV SI,012B MDV CX,0014 MDV DI,0000 MDV AX,[0103+DI] TEST AX,0001 JZ 015D	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409	MDV SI,012B MDV CX,0014 MDV DI,0000 MDV AX,[0103+DI] TEST AX,0001 JZ 015D	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B BB850301 014F A90100 0152 7409 0154 8904	MDU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU ISI],AX ADD SI,0002	DS:012B 07 00 15 00 23 00 31 0 DS:013B 0D 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200	MDU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200	MDU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002 Z 3 4 5 6 7 8 9 A E F 9F 00 EA FF FF AD DE 1B 05	DS:012B 07 00 15 00 23 00 31 0 DS:0133 0D 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200 0 1 1 08:0000 CD 20 F	MIU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002 2 3 4 5 6 7 8 9 A E F 9F 00 EA FF FF AD DE 1B 05 0 01 18 01 92 01 01 01 01 00	DS:012B 07 00 15 00 23 00 31 0 DS:013B 00 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200 0 1 1 015:0000 CD 20 F 015:0010 18 01 10 015:0020 FF FF F	MIU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002 2 3 4 5 6 7 8 9 A E F 9F 00 EA FF FF AD DE 1B 05 9 01 18 01 92 01 01 01 01 00 F FF FF FF FF FF FF FF	DS:012B 07 00 15 00 23 00 31 0 DS:013B 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200 0 1 1 08:0000 CD 20 F 08:0010 18 01 10 08:0020 FF FF F 08:0030 A2 01 1	MIU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002 2 3 4 5 6 7 8 9 A E F 9F 00 EA FF FF AD DE 1B 05 9 01 18 01 92 01 01 01 01 00 F FF FF FF FF FF FF FF FF 4 00 18 00 F5 19 FF FF FF FF	DS:012B 07 00 15 00 23 00 31 0 DS:013B 00
013F BB1701 0142 BE2B01 0145 B91400 0148 BF0000 014B 8B850301 014F A90100 0152 7409 0154 8904 0156 81C60200 0 1 3 08:0000 CD 20 F 08:0010 18 01 10 08:0020 FF FF F 08:0030 A2 01 10	MIU SI,012B MDU CX,0014 MDU DI,0000 MDU AX,[0103+DI] TEST AX,0001 JZ 015D MDU [SI],AX ADD SI,0002 2 3 4 5 6 7 8 9 A E F 9F 00 EA FF FF AD DE 1B 05 9 01 18 01 92 01 01 01 01 00 F FF FF FF FF FF FF FF FF 4 00 18 00 F5 19 FF FF FF FF	DS:012B 07 00 15 00 23 00 31 0 DS:013B 00

Thanks