

Computer Organization And Assembly Language

Assignment # 1

Name : Junaid Saeed

Roll No : 22p-9200

Section : BCS-3D

Question :-> 1

→ Write a program in assembly language for each of the below separately that sets ?

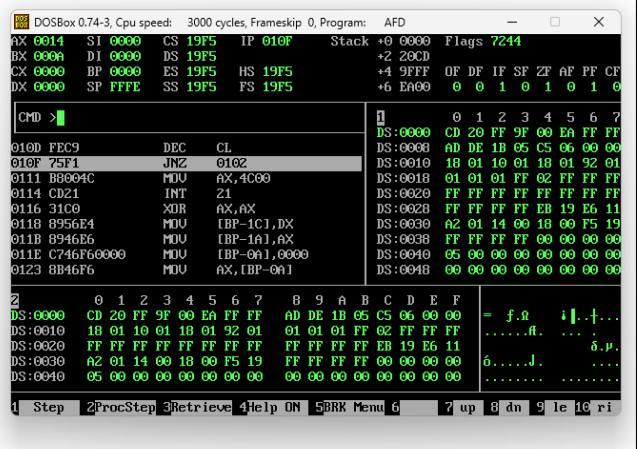
→ Answer :

a) ZERO FLAG(ZF) :

```
[org 0x0100]

mov cl, 2
start:
mov ax, 10
mov bx, 10
add ax, bx
cmp ax, 0
dec cl
jnz start

mov ax, 0x4c00
int 0x21
```



b) CARRY FLAG(CF) :

```
//Carry flag.
```

```
[org 0x0100]
```

```
mov ax, [num1]
```

```
start:
```

```
add ax, [num2]
```

```
jnc start
```

```
mov ax, 0x4c00
```

```
int 0x21
```

```
num1: dw 78
```

```
num2: dw 12
```

AX	SI	DI	BP	SP	CS	DS	ES	SS	FS	IP	Stack	Flags
89E0	8C26	8C77	0E5F	04AE	8EDF	0E8B	0A44	19F5	19F5	926E	+0 7E83	0001
89DE	8C77	0E5F	0A44	19F5	0E8B	0A44	19F5	19F5	19F5	926E	+2 00E8	0000
468B	0E5F	0A44	19F5	19F5	0E8B	0A44	19F5	19F5	19F5	926E	+4 3275	0F DF IF SF ZF AF PF CF
0A44	04AE	19F5	19F5	19F5	0E8B	0A44	19F5	19F5	19F5	926E	+6 5EC5	0 0 0 1 0 0 1

CMD	Address	Instruction	Comment
>	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	

c) PARITY FLAG(PF) :

```
//Parity flag.
```

```
[org 0x0100]
```

```
mov dx, 40
```

```
mov bx, 80
```

```
add dx, bx
```

```
mov ax, 0x4c00
```

```
int 0x21
```

AX	SI	DI	BP	SP	CS	DS	ES	SS	FS	IP	Stack	Flags
89E0	8C26	8C77	0E5F	04AE	8EDF	0E8B	0A44	19F5	19F5	928A	+0 7E83	0005
89DE	8C77	0E5F	0A44	19F5	0E8B	0A44	19F5	19F5	19F5	928A	+2 00E8	0000
468B	0E5F	0A44	19F5	19F5	0E8B	0A44	19F5	19F5	19F5	928A	+4 3275	0F DF IF SF ZF AF PF CF
0A44	04AE	19F5	19F5	19F5	0E8B	0A44	19F5	19F5	19F5	928A	+6 5EC5	0 0 0 1 0 0 1

CMD	Address	Instruction	Comment
>	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	
	0000	ADD [BX+SI],AL	

d) AUXILLIARY FLAG(AF) :

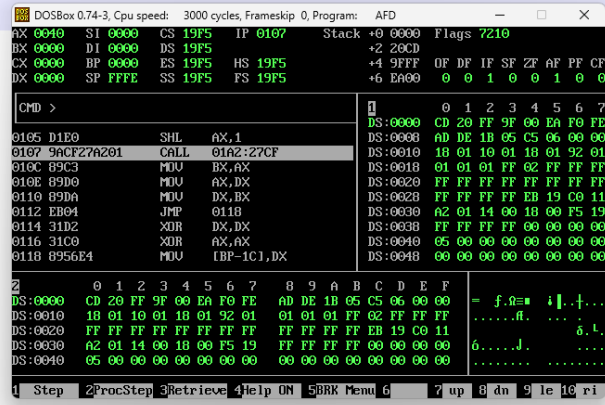
```
//auxiliary flag.

[org 0x0100]

    mov al,20
    mov bl,30

start:
    add al,bl
    jnc start

    mov ax,0x4c00
    int 0x21
```



Question :-> 2

➔ What will be the size of the following assembly language program in bytes?

Answer :

1		[org 0x0100]
2	00000000	B80500
3	00000003	BB0A00
4	00000006	01D8
5	00000008	BB0F00
6	0000000B	01D8
7	0000000D	B804C
8	00000010	CD21

Explanation ::

1. mov ax, 5 = 3 bytes.

2. mov bx, 10 = 3 bytes.
3. add ax, bx = 2 bytes.
4. mov bx, 15 = 3 bytes.
5. add ax, bx = 2 bytes.
6. mov ax, 0x4c00 = 3 bytes.
7. int 0x21 = 2 bytes.

→ Now, add up the sizes of each instruction:

→ $3 + 3 + 2 + 3 + 2 + 3 + 2 = 18$ bytes.

→ (The size is 18 bytes.)

Question :-> 3

→ Calculate the physical memory address generated by the following segment - offset pairs ?

→ Answers :

a) 1DDD:0436

Physical address = 1E206h

b) 1234:7920

Physical address = 19C60h

c) 74F0:2123

Physical address = 77023h

d) 0000:6727

Physical address = 06727h

e) FFFF:4336

Physical address = 04326h

f) **1080:0100**

Physical adress = 10900h

➔ **Solutions :**

Physical address = segment + offset.

→ add (0) at the end of segment

→ add (0) at the start of segment.

{ Solution }

(a) 1DDD : 0436

segment :- 1DDDO

A : 10

offset :- 00436

B : 11

C : 12

D : 13

E : 14

F : 15

11 11 11 11
0001 1101 1101 1101 0000
+ 0000 0000 0100 0011 0110
0001 1110 0010 0000 0110
↓ ↓ ↓ ↓ ↓
1 E 2 0 6

⇒ Physical address = 1E206h

(b) 1234 : 7920

segment : 12340

offset : 07920

11 11
0001 0010 0011 0100 0000
+ 0000 0111 1001 0010 0000
0001 1001 1100 0110 0000
↓ ↓ ↓ ↓ ↓
1 9 C 6 0

⇒ Physical address = 19C60h

(c) 74F0 : 2123

segment : 74F00
offset : 02123

0111	0100 ¹	1111	0000	0000
4 0000	0010	0001	0010	0011
0111	0111	0000	0010	0011
↓	↓	↓	↓	↓
7	7	0	2	3

Physical address = 77023h.

(d) 0000 : 6727

segment : 00000
offset : 06727

0000	0000	0000	0000	0000
+ 0000	0110	0111	0010	0111
0000	0110	0111	0010	0111
↓	↓	↓	↓	↓
0	6	7	2	7

physical address = 06727h

(e) FFFF : 4336

segment : FFFF0

offset : 04336

1111	1111	1111	111	0000
1111	1111	1111	1111	
+ 0000	0100	0011	0011	0110
(1) 0000	0100	0011	0010	0110
0	4	3	2	6

physical address = 04326h.

(f) 1080 : 0100

segment : 10800

offset : 00100

0001	0000	1000	0000	0000
+ 0000	0000	0001	0000	0000
0001	0000	1001	0000	0000
1	0	9	0	0

physical address = 10900h.