<u>Lab 3</u> Execution Of Tasks Lab 3

Task 1

```
D:\Assembly\labwork3.asm - Notepad++
File Edit Search View Encoding Language Settings Tools Macro Run Plugins Window ?
3 🙆 🖯 🖺 🤚 😘 🥱 🖟 🖟 😘 🖍 😘 🖍 🗩 😭 😅 😭 🎕 🍇 🌂 😭 🖼 🚍 🗔 🖫 🌃 🌃 🎊 🚞 🐠 🗨 🗷 🗩 🗩
labwork3.asm
        [org 0x0100]
  3
        : start of code
        mov ax, 5
                            ; move the constant 5 into register ax
        inc ax,
  6
  8
        mov bx, ax
                             ; move the value of ax into the value of bx
 10
        dec bx.
                             ; decrement the value of bx
 11
 13
        mov ax, 0x4c00
                               ; exit ..
                             ; .. is what the OS should do for me
 14
        int 0x21
```

Explanation:

Here in this code, we first add 5 to register ax, and then using **inc** command we increment the value stored in ax by 1, after that we are moving the value from ax to bx register and then using **dec**, we decrement the value in bx by 1 as per question requirement.

Following are the screenshots of execution of code:

```
Welcome to DOSBox v0.74-3

For a short introduction for new users type: INTRO
For supported shell commands type: HELP

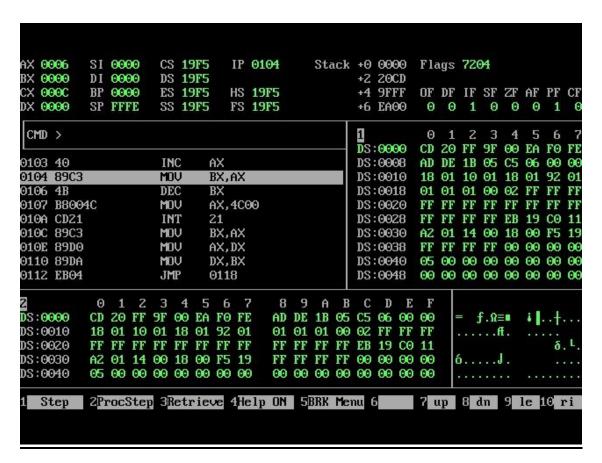
To adjust the emulated CPU speed, use ctrl-F11 and ctrl-F12.
To activate the keymapper ctrl-F1.
For more information read the README file in the DOSBox directory.

HAVE FUN!
The DOSBox Team http://www.dosbox.com

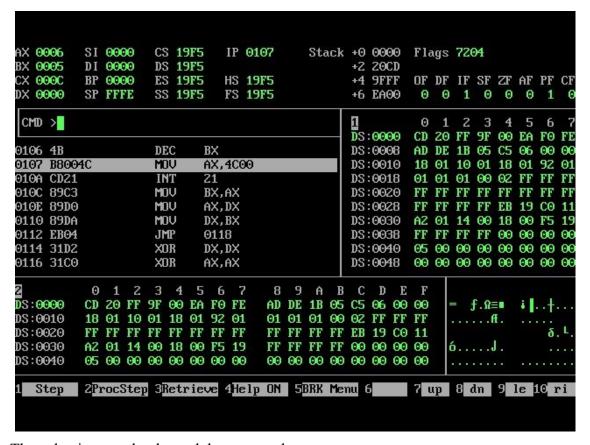
Z:\>SET BLASTER=A220 I7 D1 H5 T6

Z:\>mount x: D:\Assembly
Drive X is mounted as local directory D:\Assembly\\
Z:\>X:\>nasm labwork3.asm -o labwork.com

X:\>afd labwork3.com_
```



The value is added and incremented in ax



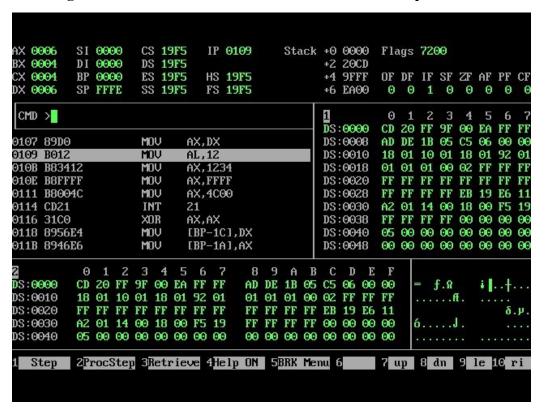
The value is moved to bx and decremented.

Task 2 Following

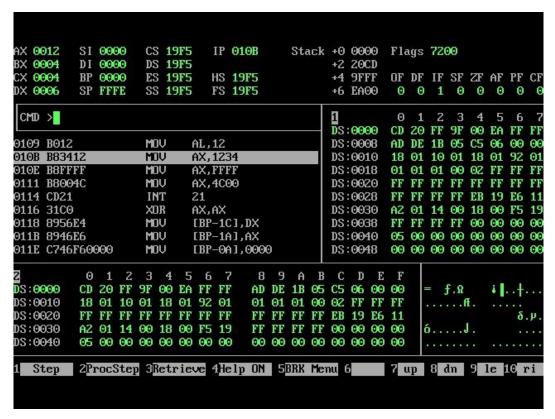
is the code of the task 2:

```
[org 0x0100]
       ; start of code
 4
 5
       mov bl, 4
                             ; move the constant 5 into register bl
 6
       mov dx, 6
                              ; move the constant 6 to dx
 7
       mov cl,bl
                              ; move dl to cl
9
       mov ax, dx
                              ; move dx to ax
10
11
       mov al, 0x12
                       ; move 0x12 to al
       mov ax, 0x1234 ; move 0x1234 to ax
12
13
       mov ax, 0xffff ; move 0xffff to ax
14
15
       mov ax, 0x4c00
16
       int 0x21
```

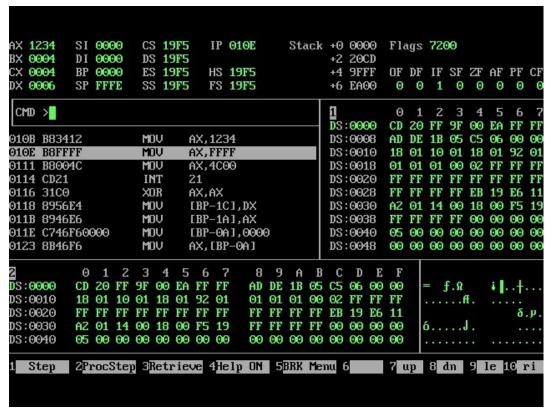
Following are the screenshots of code execution and their explanation:



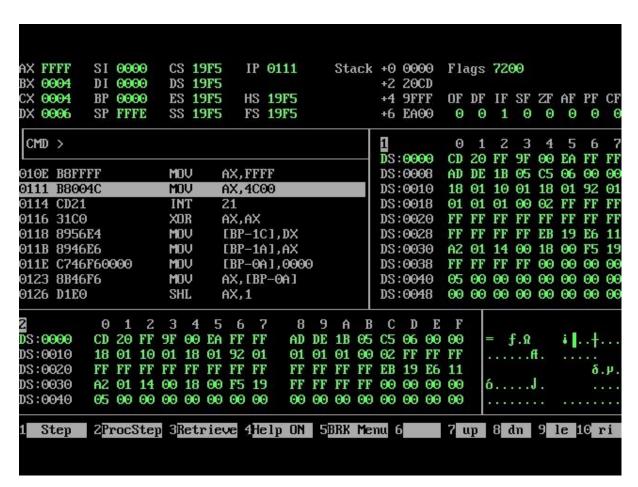
- The value is added to bl, 4 (bl has half of memory size as of bx
- The value is added to bx, 6
- The value is moved from bl to cl (same as bl, cl also is a register containing half of memory as cx)
- The value is moved from dx to ax



The value 0x12 is moved to al.



The value 0x1234 is moved to al.

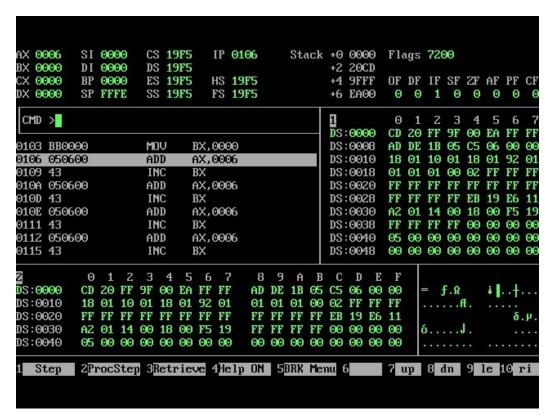


The value 0xffff is moved to al register.

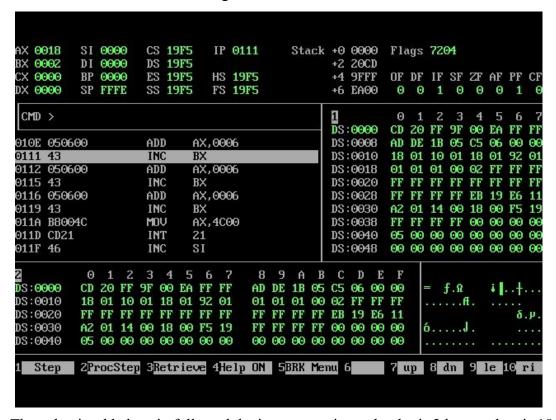
Task 3

```
D:\Assembly\labwork3.asm - Notepad++
File Edit Search View Encoding Language Settings Tools Macro Run Plugins Window
 3 글 등 한 3 D 속 보 한 D 그 C A 첫 역 역 대표 그 1 분호 및 [
labwork3.asm 🚨
         [org 0x0100]
         ; start of code
             ax, 6 ; move the constant 6 into register ax
        mov
        mov
             bx, 0
         add ax, 6 ; add 6 to ax
  8
         inc bx
         add
             ax, 6 ;add 6 to ax
         inc
             bx
 11
         add
             ax, 6 ;add 6 to ax
 12
         inc bx
         add
             ax, 6 ;add 6 to ax
 14
         inc
             bx
 15
         add
             ax, 6 ; add 6 to ax
 16
         inc
 17
 19
        mov ax, 0x4c00
 20
        int 0x21
```

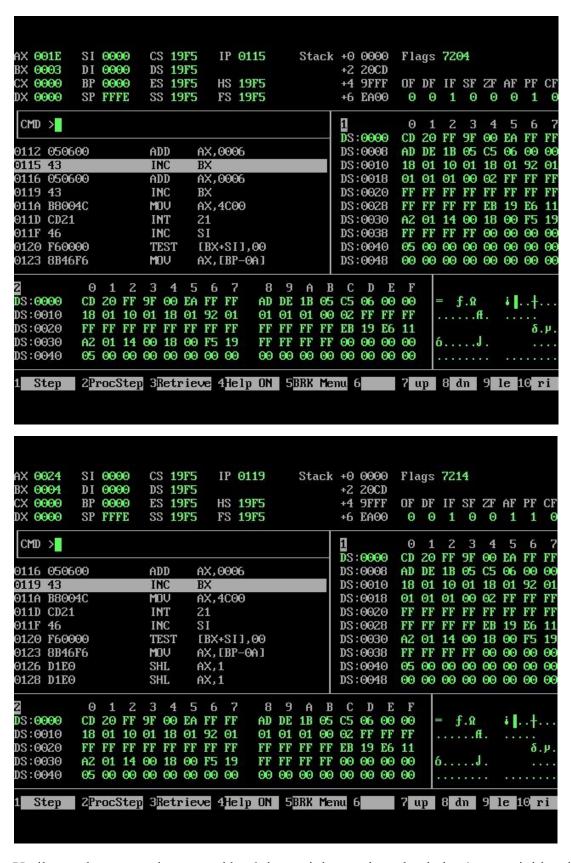
Following are the screenshots of execution of code followed by explanation.



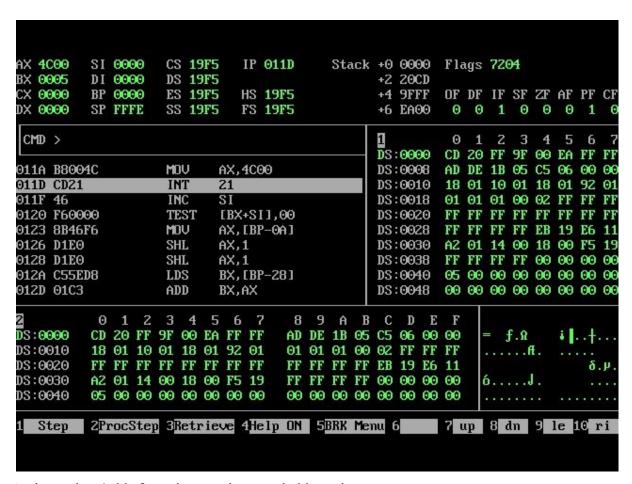
The value of 6 is moved to the register ax.



The value is added again followed the incrementation to bx, bx is 2 here and ax is 18, means the process of addition is being repeated 3 times until now...



Until now the process is repeated by 6 times giving us the value in bx 4, as we initiated with 0, ax has value of 24 and process continues till we get 36...



Ax has value 4c00, for exit as we have coded in code...

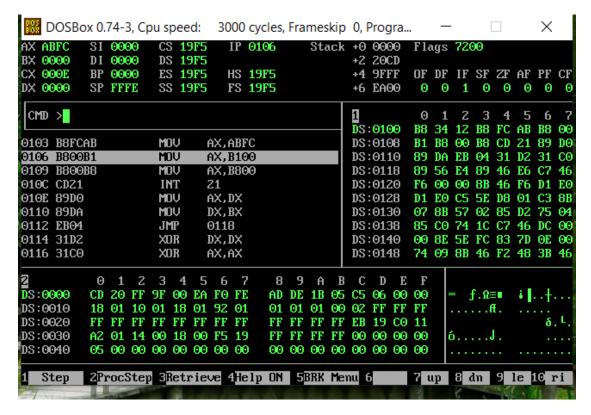
Task 4

Following is the screenshot of how the given values are stored in the registers and explained:

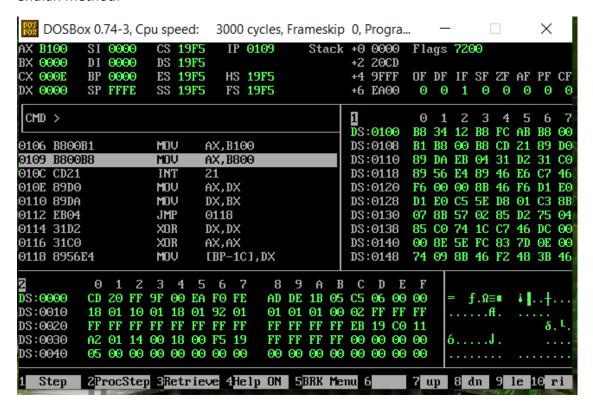
| DOS BOX | DOS | Box 0 | .74- | 3, C | pu s | pee | ed: | 30 | 00 | cycles, | Fra | mes | kip | 0, 1 | Prog | gra | | _ | | | | | × | |
|------------|--------------|------------------------|------------------------|------------------------|------------------------|------------------------|------------------------|------------------------|-----|------------------------|------------------------|------------------------|------------------------|------|------------------------|------------------------|------------------------|------|-----|--------|-----|-----|----|-------|
| | 1234 0000 | | 00 00 | | | | 9F5 9F5 | | ΙP | 0103 | | Sta | ack | | 00 20 | | Fla | ags | 720 | 90 | | | | |
| CX | 000E | BP | 00 | 00 | E | 3 1 | 9F5 | | | 19F5 | | | | +4 | 9F | FF | OF | DF | IF | SF | ΖF | ΑF | PF | CF |
| DX | 0000 | SP | FF | FE | S | 3 1 | 9F5 | 1 | FS_ | 19F5 | | | | +6 | EAG | 90 | 0 | 0 | _1 | 0 | 0 | 0 | 0 | 0 |
| Cr | D > | | | | | | | | | | | | 1 | 1 | | | 0 | 1 | 2 | 3 | 4 | 5 | 6 | |
| | | | | | | | | | | | | | \dashv | | 010 | | B8 | 34 | 12 | B8 | FC | | B8 | - |
| | Ю B83 | | | | |)V | | AΧ,: | | | | | _ | | :010 | | B1 | B8 | 00 | B8 | | 21 | 89 | |
| صنصنا ر | 3 B81 | | | | |)V | | AX,f | | | | | | | :01: | | 89 | DA | EB | | | D2 | | |
| | 6 B80 | | | | | JV. | | ΑΧ,] | | | | | | | :01: | | 89 | 56 | E4 | | | | C7 | 46 |
| | 9 B80 | | | | |)V | | ΑΧ,] | 880 | 0 | | | | | :01 | | F6 | 00 | 00 | 8B | | | D1 | |
| | C CD2 | | | | | Tr | | 21 | | | | | | | :01 | | D1 | EΘ | | 5E | | | 9 | |
| | E 891 | | | | |)V | | AX,I | | | | | | | :01 | | 07 | 8B | 57 | | | D2 | | |
| | .0 891 | | | | | JV. | | DX,1 | | | | | | | :01 | | 85 | CO | 74 | | | 46 | | |
| | .2 EB0 | | | | | TP | | 9118 | | | | | | | :01 | | 00 | 8E | | FC | | 7D | ΘE | |
| 011 | .4 311 |) | | | XI | OR | | DX,1 | JX | | | | \perp | אמ | :01 | 1 8 | 74 | 09 | 88 | 46 | FΖ | 48 | 36 | 46 |
| 2 | | e | 1 | 2 | 3 | 4 | 5 | 6 | 7 | 8 | 9 | A | В | С | D | E | \mathbf{F} | | | | | | | |
| DS: | 0000 | CD | 20 | $\mathbf{F}\mathbf{F}$ | 9F | 00 | ΕA | FΘ | FΕ | AD | DΕ | 1B | 05 | C5 | 06 | 00 | 00 | | = ; | f . Ω: | | i I | + | |
| DS: | 0010 | 18 | 01 | 10 | 01 | 18 | 01 | 92 | 01 | 01 | 01 | 01 | 00 | 02 | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | | | f | Ŧ. | | | |
| DS: | 0020 | $\mathbf{F}\mathbf{F}$ | FF | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | EB | 19 | CO | 11 | | | | | | δ | , L |
| | 0030 | A2 | 01 | 14 | 90 | 18 | 90 | F5 | 19 | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | $\mathbf{F}\mathbf{F}$ | 00 | 00 | 90 | 90 | _ · | ó., | | J. | | | |
| DS: | 0040 | 05 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | 00 | | | | • • | | | • • • |
| 1 | Step | 21 | roc | Ste | g 3 <u>i</u> | Ret | rie | ve 4 | Не | lp ON | 5 | BRK | Men | nu (| 5 | | 7 (| ιp | 8 | dn | 9 | le | 10 | ri |

Here the value 34 is stored at address 0101 and value 12 is stored at address 0102

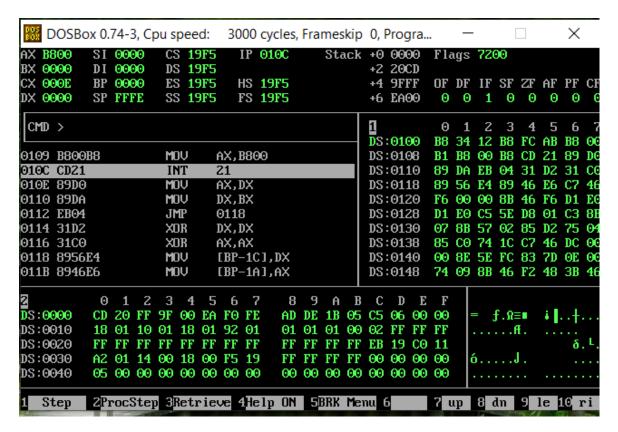
Because intel architecture follows little-Endian method to store value



Here value FC is stored at address 0104 and AB is stored at address 0105 following little-endian method.



Here value 00 is stored at address 0107 and B1 is stored at address 0108



Here 00 is stored at address 0108,2 and B1 is stored at 0108,1 , all these values are stored in little endian method since it's a intel architecture.

Little-Endian: Means that the least significant byte is stored at the lower memory address, while the most significant byte.

Big-Endian is the opposite; the most significant byte is stored at the lower memory address, and the least significant byte is stored at the higher memory address.

a. 1234

- In Little-Endian: Memory Address: 34 (lower), 12 (higher)
- In Big-Endian: Memory Address: 12 (lower), 34 (higher)

b. ABFC

- In Little-Endian: Memory Address: FC (lower), AB (higher)
- In Big-Endian: Memory Address: AB (lower), FC (higher)

c. **B100**

- In Little-Endian: Memory Address: 00 (lower), B1 (higher)
- In Big-Endian: Memory Address: B1 (lower), 00 (higher)

d.**B800**

In Little-Endian: Memory Address: 00 (lower), B8 (higher)

• In Big-Endian: Memory Address: B8 (lower), 00 (higher)

Name: Ahmed-Mustafa Roll no: 22p-9060 Section: BS CS 3A