

24/05/21

Q1 A What is Process Control Block? With the help of diagram, explain all the elements of PCB

ANS

- Any process is identified by its Process Control Block (PCB) which is the data structure used by the operating system to keep track on the processes.
- All the information associated with the process is kept in PCB, there is separate PCB for each process.
- Traffic controller module keeps track on the status of the processes.
- If many processes wait for the same device then PCB's of all these processes are linked together in chain waiting for that device.
- If device becomes free then traffic controller checks the PCB chain to see if any process is waiting for the device.
- If the processes are available in the device chain, then again it is placed back to ready state, to request that device again.

IDENTIFIER
STATE
PRIORITY
PROGRAM COUNTER
MEMORY POINTERS
I/O STATUS INFORMATION
ACCOUNTING INFORMATION
:
:

FOR EDUCATIONAL USE

A simplified PCB

**IDENTIFIER:** A unique identifier associated with this process to distinguish it from all other processes.

**STATE:** If the process is currently executing, it is in the running state.

**PRIORITY:** Priority level relative to other processes.

**PROGRAM COUNTER:** The address of the next instruction in the program to be executed.

**MEMORY POINTERS:** Includes pointers to the program code and data associated with this process plus any memory blocks shared with other processes.

**CONTEXT DATA:** These are data that are present in registers in the processor while the process is executing.

**I/O STATUS INFORMATION:** Includes outstanding I/O requests, I/O devices assigned to this process, a list of files in use by the process and so on.

**ACCOUNTING INFORMATION:** May include the number of processor time and clock time used, time limits, account numbers and so on.

Q1 B

ANS

Explain different functions of operating systems

The operating system performs the following functions in a device:-

- ~~Suspension~~
- ~~File Management~~

1) MEMORY MANAGEMENT: It is the management of the main memory. Whatever program is executed, it has to be ~~executed~~ present in the main memory. Therefore, there can be more than one program present at a time. Hence OS is required to manage the memory by allocation, deallocation, distribution and recording.

2) PROCESSOR MANAGEMENT: When more than one process runs on the system, the OS decides how and when a process will use the CPU. Hence the name is also CPU scheduling where the OS allocates, deallocates processors and keeps record of CPU status. It uses certain CPU scheduling algorithms like FCFS, SJF, Priority, Round-Robin, etc.

3) DEVICE MANAGEMENT: The processes may require devices for their use. The OS decides the need of the processes and allocates/deallocates devices accordingly.

- 4) FILE MANAGEMENT: The files on a system are stored in different directories. The OS keeps record of the status and location of files and allocates/deallocates resources.
- 5) SECURITY: The OS keeps the system and programs safe and secure through different methods of authentication
- 6) ACCOUNTING
- 7) ERROR DETECTION
- 8) RECORDS OF SYSTEM PERFORMANCE
- 9) COMMUNICATION BETWEEN DIFFERENT SOFTWARES, etc.

Q3

5000 cylinders [0 - 4999]

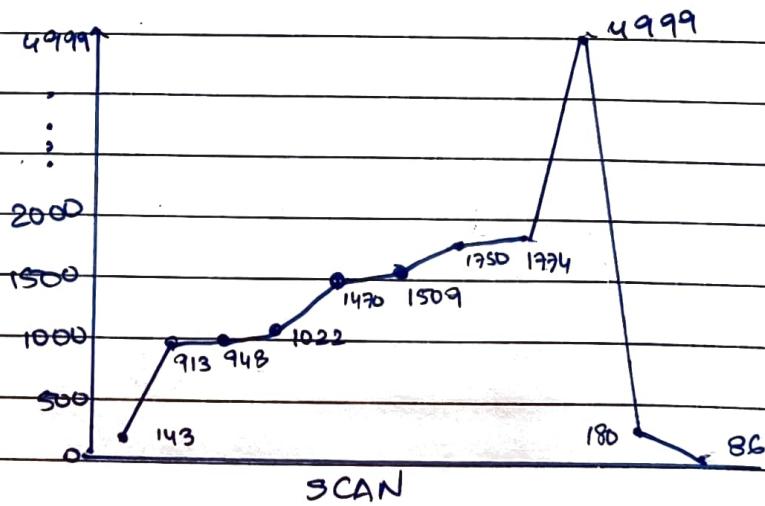
current request = 143

Previous request = 125

Queue: 86, 1470, 913, 1774, 948, 1509, 1022, 1750, 130

- (i) The SCAN schedule is 143, 913, 948, 1022, 1470, 1509, 1750, 1774

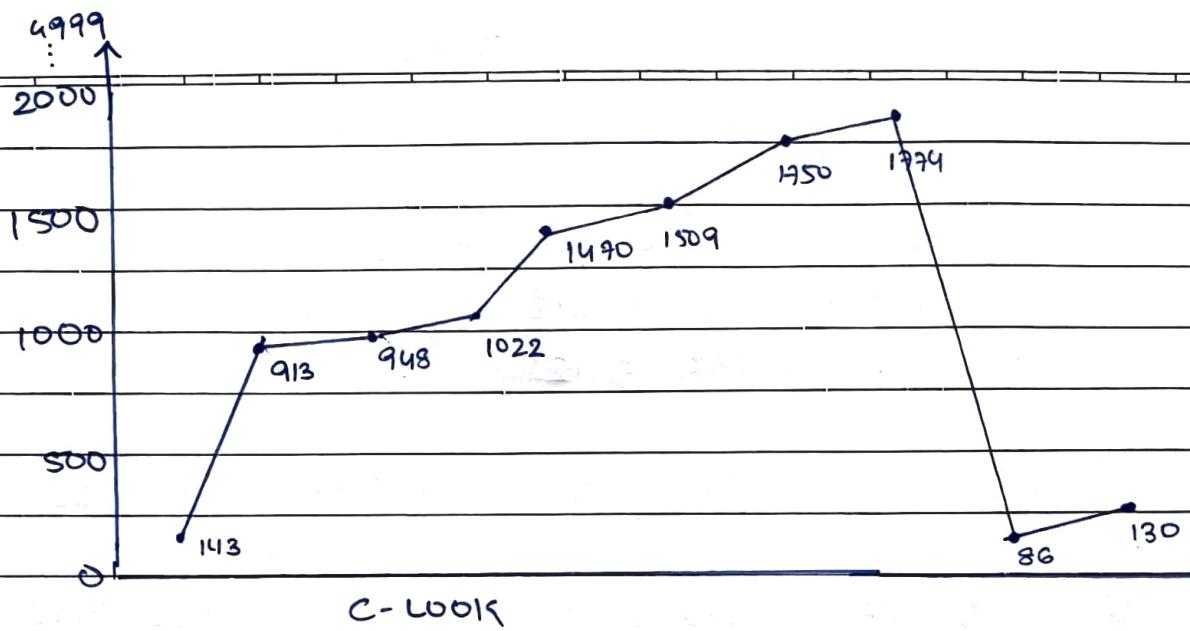
$$\begin{aligned} \text{Total distance travelled} &= |143 - 913| + |913 - 948| + |948 - 1022| \\ &\quad + |1022 - 1470| + |1470 - 1509| + |1509 - 1750| \\ &\quad + |1750 - 1774| + |1774 - 4999| + |4999 - 180| + |180 - 86| \\ &= 9769 \end{aligned}$$



- (ii) The C-LOOK SCHEDULE is 143, 913, 948, 1022, 1470, 1509, 1750, 1774, 86, 130.

$$\begin{aligned} \text{Total distance travelled} &= |143 - 913| + |913 - 948| + |948 - 1022| + |1022 - 1470| \\ &\quad + |1470 - 1509| + |1509 - 1750| + |1750 - 1774| + |1774 - 86| + |86 - 130| \\ &= 3363 \end{aligned}$$

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Q5

## ALLOCATION

	A	B	C	D
P <sub>0</sub>	0	0	1	2
P <sub>1</sub>	1	0	0	0
P <sub>2</sub>	1	3	5	4
P <sub>3</sub>	0	6	3	2
P <sub>4</sub>	0	0	1	4

## MAX

	A	B	C	D
P <sub>0</sub>	0	0	1	2
P <sub>1</sub>	1	7	5	0
P <sub>2</sub>	2	3	5	6
P <sub>3</sub>	0	6	5	2
P <sub>4</sub>	0	6	5	6

## AVAILABLE

	A	B	C	D
	1	5	2	0

ANS i) Need matrix = Max - Allocation

= NEED

	A	B	C	D
P <sub>0</sub>	0	0	0	0
P <sub>1</sub>	0	7	5	0
P <sub>2</sub>	1	0	0	2
P <sub>3</sub>	0	0	2	0
P <sub>4</sub>	0	6	4	2

ii) To decide whether system is in safe state or not.

PROCESS P<sub>0</sub> : Need  $\leq$  Availability  $[(0,0,0) \leq (1,5,2,0)]$  is true  
 P<sub>0</sub> is executed.

$$\begin{aligned} \text{New Availability} &= \text{Availability} + \text{Allocation} \\ &= (1,5,2,0) + (0,0,1,2) \\ &= (1,5,3,2). \end{aligned}$$

PROCESS P<sub>1</sub> : Need  $\leq$  Availability  $[(0,7,5,0) \leq (1,5,3,2)]$  is false  
 $\therefore P_1$  is not executed.

PROCESS P2: Need  $\leq$  Availability  $[(1,0,0,2) \leq (1,5,3,2)]$  is True  
 $\therefore P2$  is executed.

$$\begin{aligned}\text{New Availability} &= \text{Availability} + \text{Allocation} \\ &= (1,5,3,2) + (1,3,5,4) \\ &= (2,8,8,6)\end{aligned}$$

PROCESS P3: Need  $\leq$  Availability  $[(0,0,2,0) \leq (2,8,8,6)]$  is True  
 $\therefore P3$  is executed

$$\begin{aligned}\text{New availability} &= \text{Availability} + \text{Allocation} \\ &= (2,8,8,6) + (0,6,3,2) \\ &= (2,14,11,8)\end{aligned}$$

PROCESS P4: Need  $\leq$  Availability  $[(0,6,4,2) \leq (2,14,11,8)]$  is True  
 $\therefore P4$  is executed

$$\begin{aligned}\text{New availability} &= \text{Availability} + \text{Allocation} \\ &= (2,14,11,8) + (0,0,1,4) \\ &= (2,14,12,12)\end{aligned}$$

PROCESS P1: Need  $\leq$  Availability  $[(0,7,5,0) \leq (2,14,12,12)]$  is True  
 $\therefore P1$  is executed.

$$\begin{aligned}\text{New Availability} &= \text{Availability} + \text{Allocation} \\ &= (2,14,12,12) + (1,0,0,0) \\ &= (3,14,12,12)\end{aligned}$$

$\therefore$  SYSTEM IS IN SAFE STATE  $[P_0, P_2, P_3, P_4, P_1]$

- (iii) Safe sequences :-  $[P_0 \rightarrow P_2 \rightarrow P_1 \rightarrow P_3 \rightarrow P_4]$  ;  
 $[P_0 \rightarrow P_2 \rightarrow P_3 \rightarrow P_1 \rightarrow P_4]$  ;  $[P_0 \rightarrow P_2 \rightarrow P_4 \rightarrow P_1 \rightarrow P_3]$  ;  
 $[P_0 \rightarrow P_2 \rightarrow P_1 \rightarrow P_4 \rightarrow P_3]$  ;  $[P_0 \rightarrow P_2 \rightarrow P_4 \rightarrow P_3 \rightarrow P_1]$  ;  
 $[P_2 \rightarrow P_3 \rightarrow P_4 \rightarrow P_0 \rightarrow P_1]$  ;  $[P_3 \rightarrow P_4 \rightarrow P_0 \rightarrow P_2 \rightarrow P_1]$  .

Q6

Explain Paging and Segmentation w.r.t Virtual memory.

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PAGING	SEGMENTATION
<ul style="list-style-type: none"> <li>Main memory partitioned into small fixed-size chunks called frames</li> </ul>	Main memory not partitioned
<ul style="list-style-type: none"> <li>Program broken into pages by the compiler or memory management system.</li> </ul>	Program segments specified by the programmer to the compiler.
<ul style="list-style-type: none"> <li>Internal fragmentation within frames</li> </ul>	No internal fragmentation
<ul style="list-style-type: none"> <li>No external fragmentation</li> </ul>	External fragmentation
<ul style="list-style-type: none"> <li>OS must maintain a page table for each process showing which frame each page occupies</li> </ul>	OS must maintain a segment table for each process showing the load address and length of each segment
<ul style="list-style-type: none"> <li>OS must maintain a free frame list.</li> </ul>	OS must maintain a list of free holes in memory
<ul style="list-style-type: none"> <li>Processor uses page number, offset to calculate absolute addresses</li> </ul>	Processor uses segment number, offset to calculate absolute addresses

- |   |   |
|---|---|
| <ul style="list-style-type: none"> <li>Not all pages of a process need to be in main memory frames for the process to run. Pages may be read in as needed.</li> </ul> | <p>Not all segments of a process need to be in main memory for the process to run. Segments may be read in as needed.</p> |
| <ul style="list-style-type: none"> <li>Reading a page into main memory may require writing a page out to disk.</li> </ul>   | <p>Reading a segment into main memory may require writing one or more segments out to disk.</p>                           |

Q4

a) Real time OS

b) Short term scheduler, Medium term scheduler,  
long term scheduler.

ANS a) Real time operating systems [RTOS] are used in environments where a large number of events, mostly external to the computer system, must be accepted and processed in a short time or within deadlines. Such applications are industrial control, telephone switching equipment, flight control and real time simulations.

RTOS can be of 2 types :-

HARD RTOS : These operating systems guarantee that critical tasks be completed within a range of time.

For e.g.: A robot is hired to weld a car body, if the robot welds too early or late, the car cannot be sold, so a hard RTOS is used.

SOFT RTOS : This operating system provides some relaxation in time limit.

For e.g.: Multimedia systems, digital audio systems, etc. Explicit, programmer defined and controlled processes are encountered in real time systems.

A separate process is charged with handling a single external event. The process is activated upon occurrence of the related event signaled by an interrupt.

Priority based preemptive scheduling is used by RTOS

### Advantages :-

- Minimum consumption
- Task shifting
- Error free
- Focus on Application

### Disadvantages :-

- Limited tasks
- Use heavy system resources
- Complex algorithms
- Thread priority.

Ans

### b] LONG TERM SCHEDULER :-

- When programs are submitted to the system for the purpose of scheduling, long term scheduler's job is to choose processes from the queue and place them into main memory for execution purpose
- Long-term scheduler controls the degree of multiprogramming
- It is not present in time shared OS

### SHORT TERM SCHEDULER :-

- Process which are in ready queue wait for CPU.
- A short term scheduler chooses the process from ready queue and assigns it to the CPU based on some policy.
- Short term scheduler is faster than long term scheduler.

### MEDIUM TERM SCHEDULER

- If the degree of the multiprogramming increases, medium-term scheduler swap out the processes from main memory.
- The swapped out processes are again swapped in by the medium-term scheduler.
- This is done to control the degree of multiprogramming or to free up memory.