

U1A

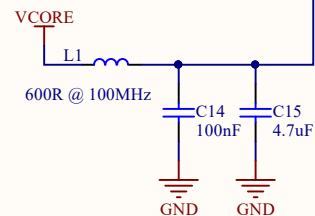
BANK 1A

CONDONE
CRESET_N
GPIOL_00
GPIOL_01_SS_N
GPIOL_02_CCK
GPIOL_03_CD14
GPIOL_04_CD10
GPIOL_05_CD15
GPIOL_06_CD11
GPIOL_07_CD16
GPIOL_09_CD12
GPIOL_10_CD17
GPIOL_11_CD13
GPIOL_12_CTRL0
GPIOL_13_CTRL1
GPIOL_14_CLK0
GPIOL_15_CLK1
VCCIO1A

BANK 1B

GPIOL_16_CLK2
GPIOL_17_CLK3
GPIOL_18_CTRL2
GPIOL_19_CTRL3
GPIOL_20_PLLIN
GPIOL_21_NSTATUS
TCK
TDI
TDO
TMS
VCCIO1B
VCCA_PLL

T8F81

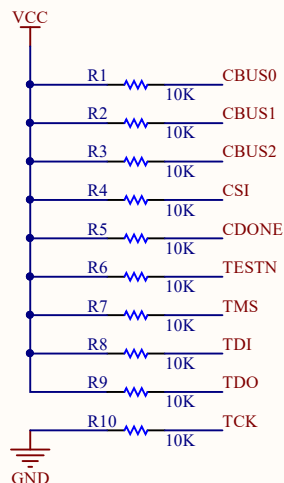


U1B

BANK 2A

GPIOR_00
GPIOR_01
GPIOR_02_RESERVED OUT
GPIOR_03
GPIOR_05
GPIOR_06
GPIOR_07
GPIOR_08
GPIOR_10
GPIOR_11
GPIOR_12
GPIOR_13
GPIOR_14
GPIOR_15_CBUS0
GPIOR_16_CBUS1
GPIOR_17_CBUS2
GPIOR_18_CLK7
GPIOR_19_CLK6
VCCIO2A

T8F81

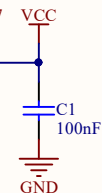


U1C

BANK 2B

GPIOR_20_CLK5
GPIOR_21_CLK4_CBSL1
GPIOR_22_CTRL5
GPIOR_23_CTRL4
GPIOR_24
GPIOR_25
GPIOR_26_CBSL0
GPIOR_27
GPIOR_28
GPIOR_30
GPIOR_31
GPIOR_32
GPIOR_34_CSI
GPIOR_35_CSO
GPIOR_36
GPIOR_37_TESTN
VCCIO2B

T8F81

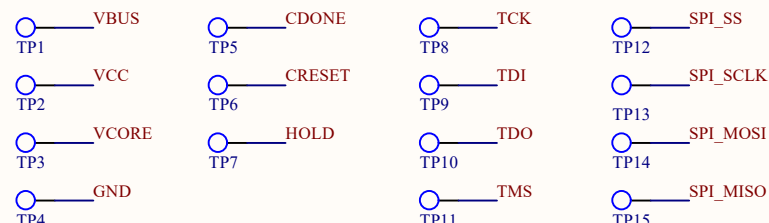
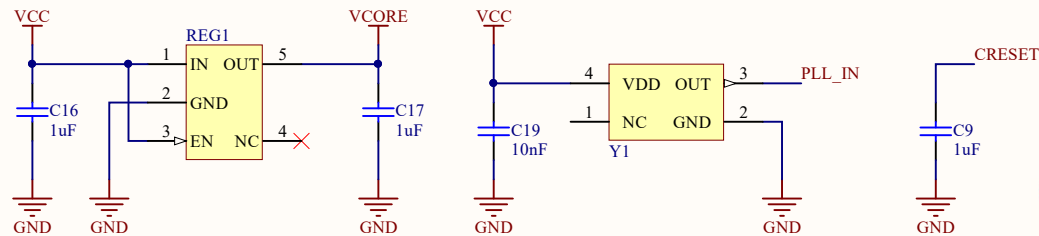
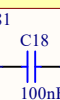


U1D

A2
B1
B7
D1
D5
E4
E9
G2
G7
H1

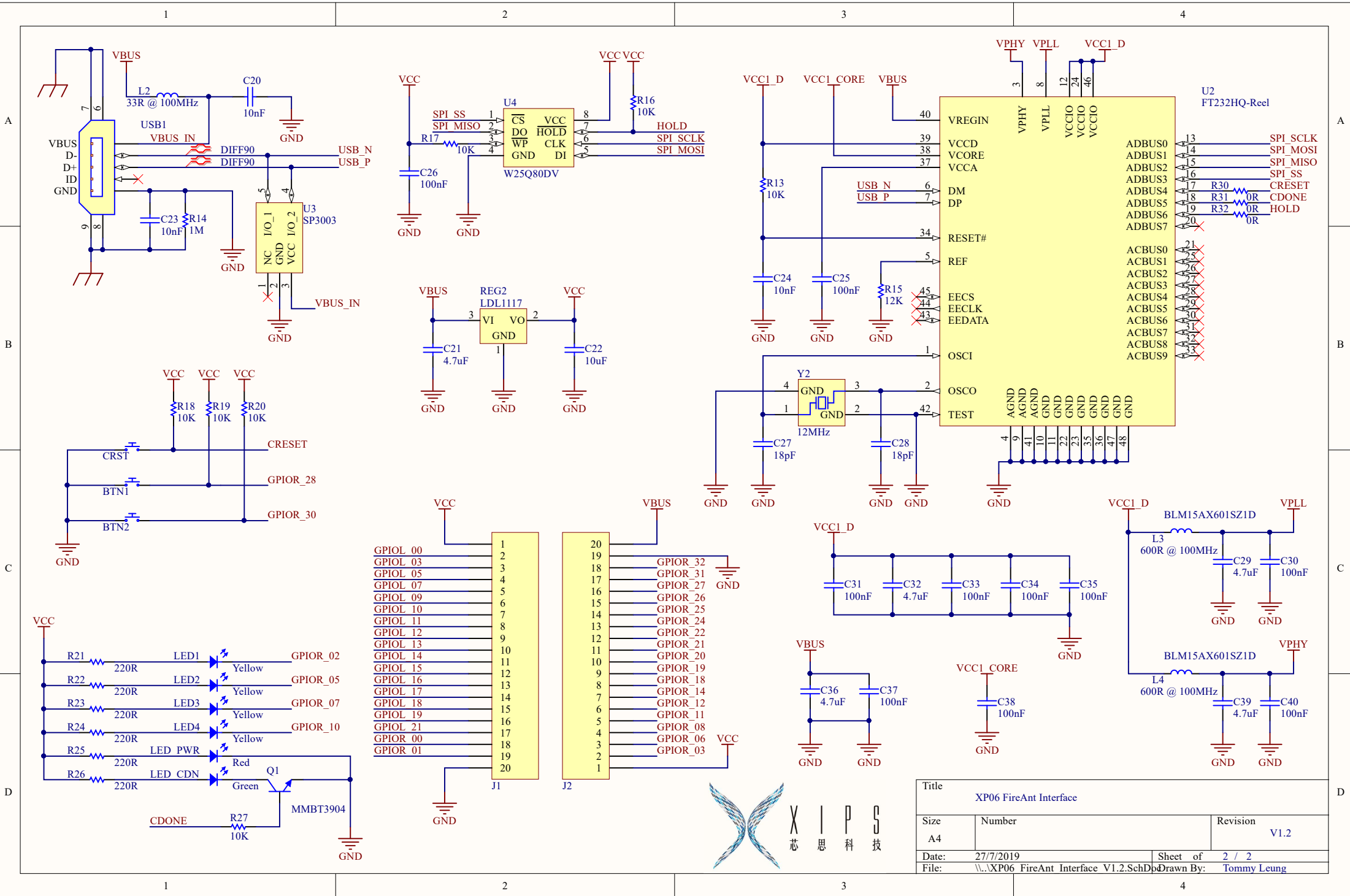
GND
VCC
GND
VCC
GND
VCC
GND
VCC
GND
GND
GND

T8F81



芯 思 科 技

Title		
XP06 FireAnt FPGA		
Size	Number	Revision
A4		V1.2
Date:	27/7/2019	Sheet of 1 / 2
File:	\\.\XP06 FireAnt FPGA V1.2.SchDoc	Drawn By: Tommy Leung



Title		
XP06 FireAnt Interface		
Size	Number	Revision
A4		V1.2
Date:	27/7/2019	Sheet of 2 / 2
File:	\\..XP06 FireAnt Interface V1.2.SchDp	Drawn By: Tommy Leung