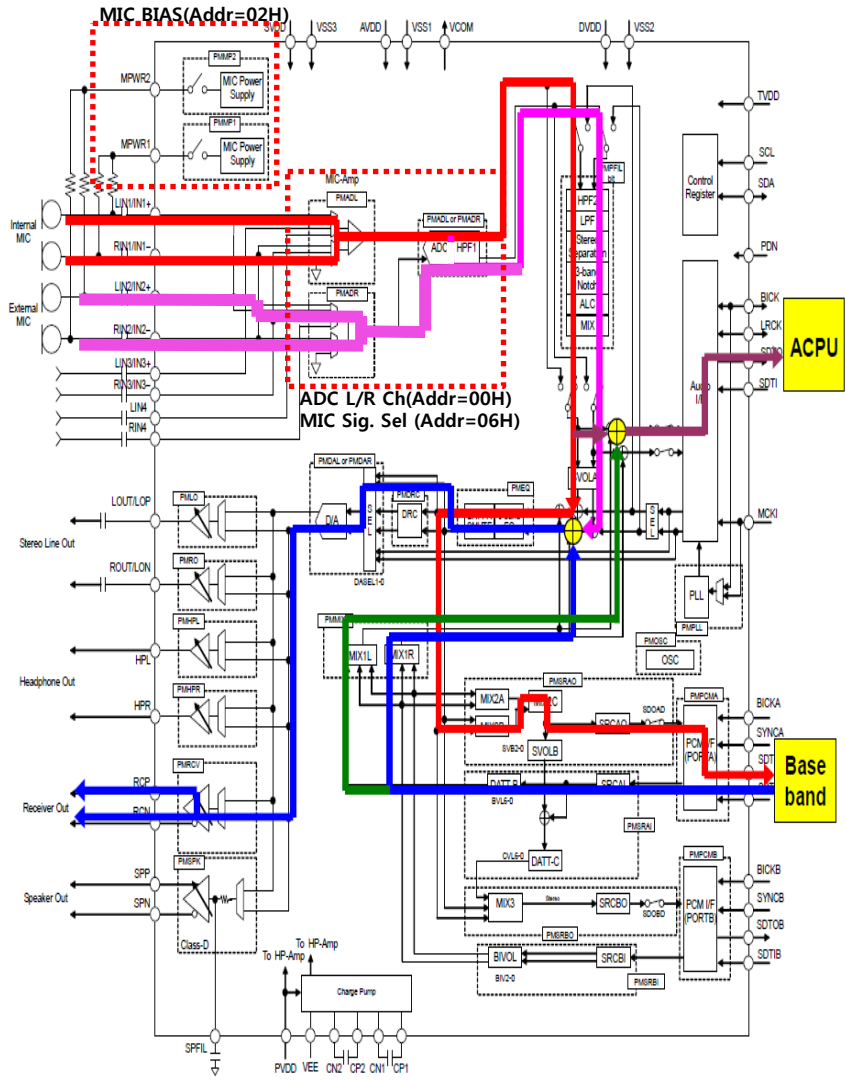


AK4678 2 MIC Input Configuration

1-2. Phone Call & Recording



<Sequence>

1. Power Supply
2. Wait 1 μ s
3. PDN pin: "L" \rightarrow "H"
4. Address=00H, Data=00H (Dummy Command; Digital block power-up)
5. Address=03H, Data=B8H (MCKI=19.2MHz, fs=48kHz)
6. Address=04H, Data=23H (BCKO=64fs, PLL Master Mode)
7. Address=05H, Data=03H (SDOD bit = "0", I2S)
8. MCKI Clock Input
9. Address=00H, Data=01H (VCOM & PLL Power-up)
10. Wait 11ms (VCOM power-Up + PLL Lock Time)
11. Address=06H, Data=14H (MIC-Amp Lch IN1+/-)
12. Address=07H, Data=BBH (MGAIN = +18dB)
13. Address=14H, Data=85H (PFMXL/R1-0 bits="01", SRMXL1-0 bits="00", SRMXR1-0 bits="10")
13. Address=14H, Data=45H (PFMXL/R1-0 bits="01", SRMXL1-0 bits="00", SRMXR1-0 bits="01")
14. Address=19H, Data=12H (PFSEL bit = "0", PFSDO bit = "1", DASEL1-0 bits = "00")
15. Address=1CH, Data=20H (SVAL2-0 bits = "000"(0dB), SVAR2-0 bits = "010"(-12dB))
16. Address=20H, Data=03H (PCM I/F A: I2S, 16bit Linear)
17. Address=25H, Data=00H (MX1L2-0 bits = "000", MX1R2-0 bits = "000")
18. Address=26H, Data=10H (MX2B1-0 bits = "00", MX2C1-0 bits = "01")
19. Address=28H, Data=30H (SDOL1-0 bits = "11")
20. Address=09H, Data=20H (DACRR bit = "1")
21. Address=10H, Data=BBH (RCVG = 0dB)
- (If needed, programmable filter (ALC, IVOL/R, HPF, EQ and etc) and 5-band equalyzer are set-up.)
22. SYNCA/BICKA clock input
23. Address=02H, Data=01H (MIC Power 1 on)
23. Address=02H, Data=05H (MIC Power 1, 2 on, PMMP1, PMMP2)
24. Address=00H, Data=13H (Programmable Filter, MIC-Amp Lch and ADC Lch Power-Up)
24. Address=00H, Data=33H (Programmable Filter, MIC-Amp Lch/Rch and ADC Lch/Rch Power-Up)
25. Address=01H, Data=09H (5-band EQ, DAC Rch Power-Up)
26. Address=1FH, Data=8FH (MIX1 block, Oscillator, SRCAL/O, PCM I/F A Power-Up)
27. Wait 80ms (MIC-Amp & ADC initial time(80ms) with SRC initial time(19.5ms=156/fs2@fs2=8kHz))
28. Phone Call & Recording
29. Address=0DH, Data=02H (RCVPS bit = "1")
30. Address=0DH, Data=03H (Receiver Power-Up)
31. Wait 1ms
32. Address=0DH, Data=01H (RCVPS bit = "0"; Release Power-save)
33. Playback from Receiver
34. Address=0DH, Data=03H (RCVPS bit = "1"; Receiver Power-save)
35. Address=0DH, Data=00H (Receiver Power-Down & Release Power-save)
36. Address=1FH, Data=00H (MIX1 block, Oscillator, SRCAL/O, PCM I/F A Power-Down)
37. Address=01H, Data=00H (5-band EQ, DAC Rch Power-Down)
38. Address=00H, Data=01H (Programmable Filter, MIC-Amp Lch and ADC Lch Power-Down)
39. Address=02H, Data=00H (MIC Power 1 off)
40. Address=04H, Data=22H (PLL Power-Down)
41. Address=00H, Data=00H (VCOM Power-Down)
42. PDN pin: "H" \rightarrow "L"
43. Clock Stop
44. Stop Power Supply