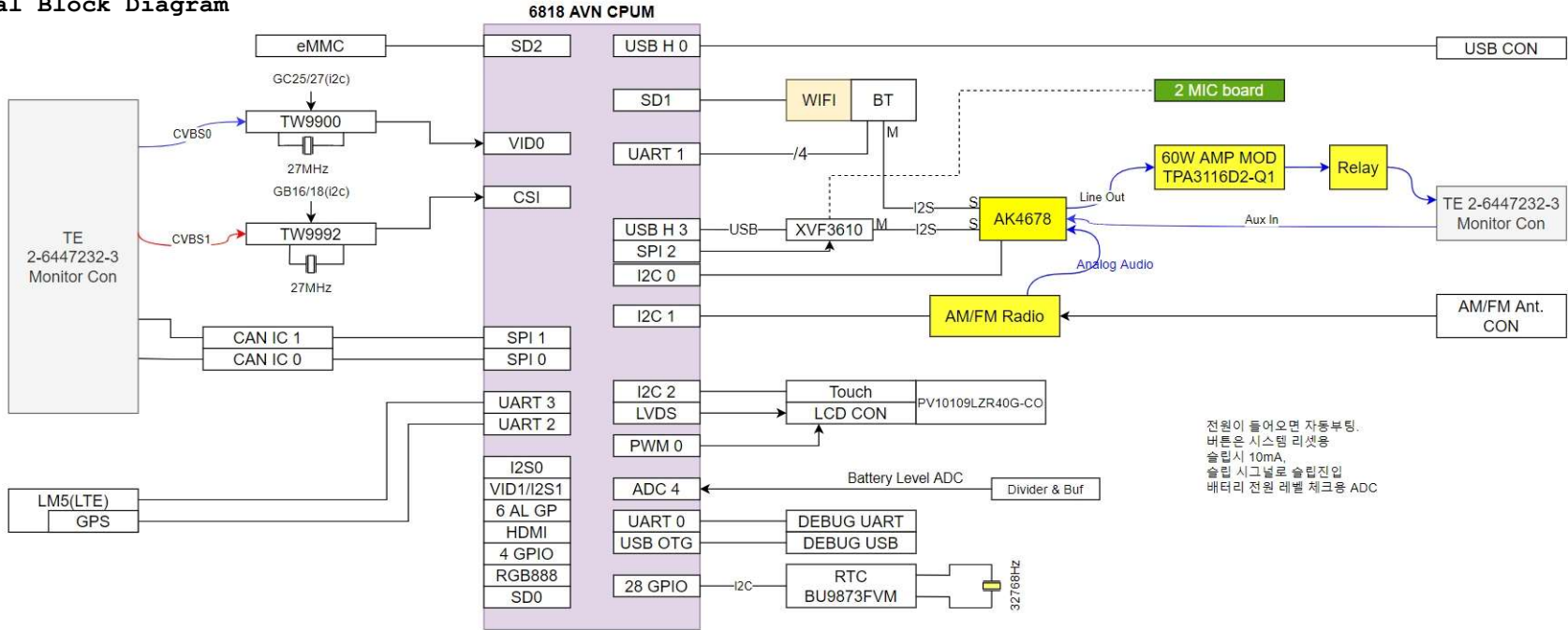
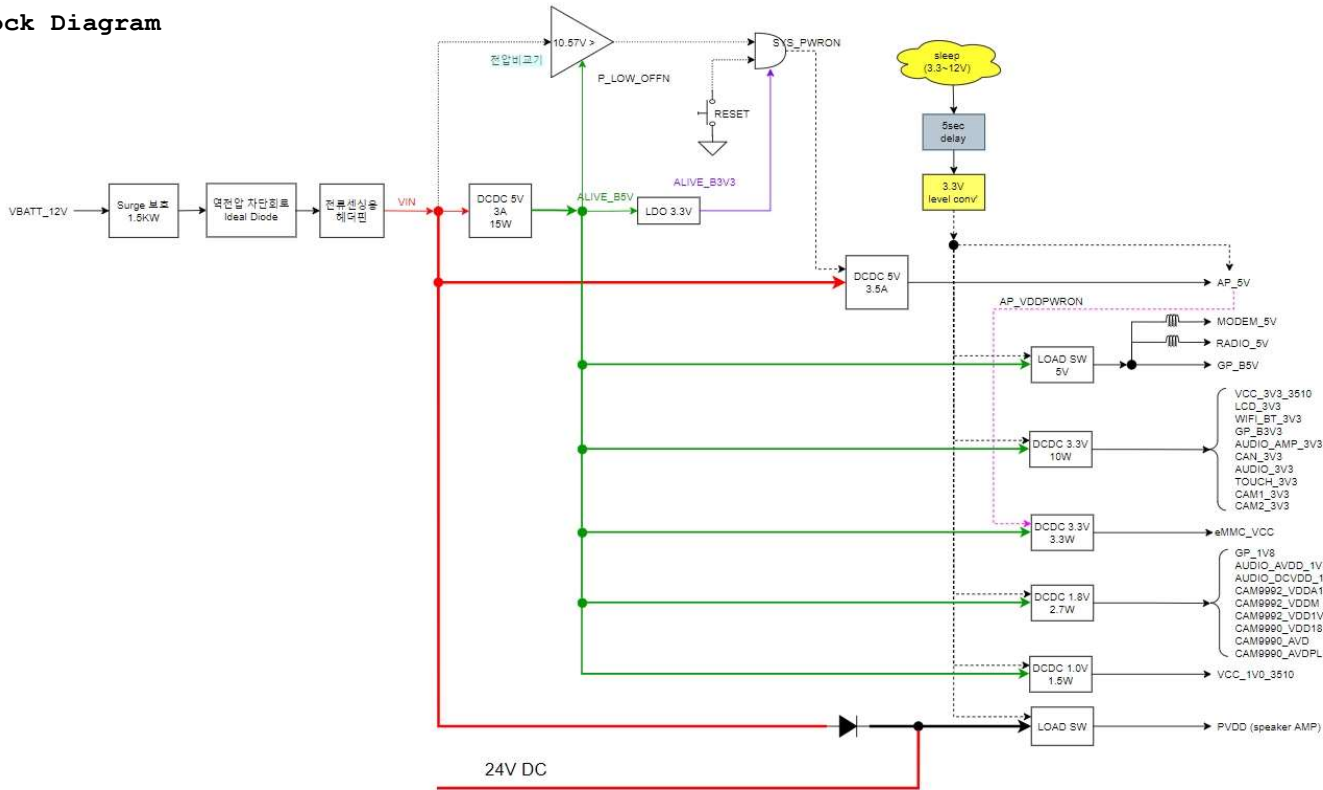
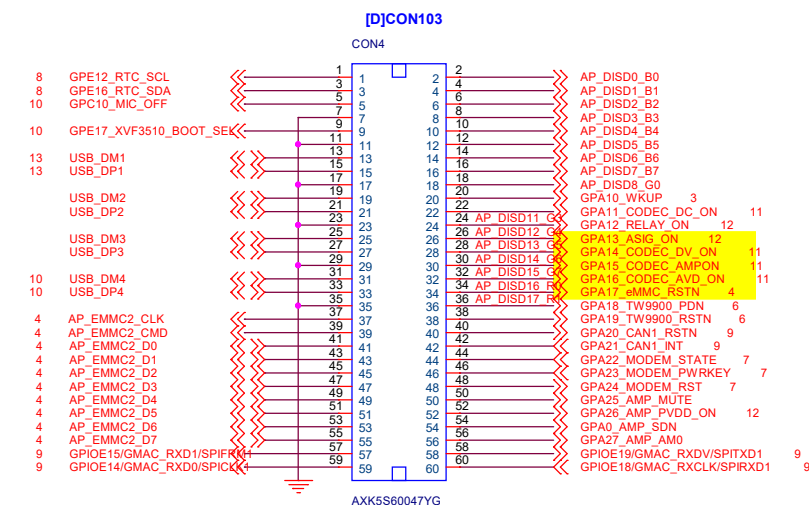
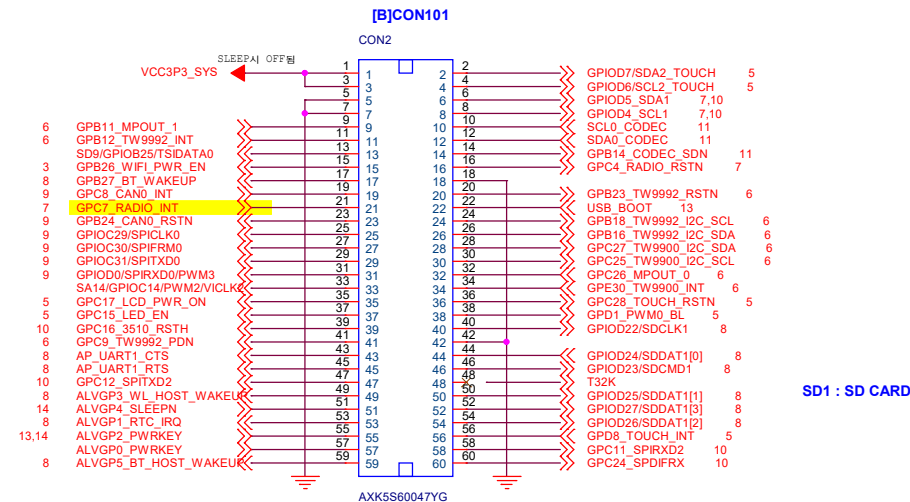


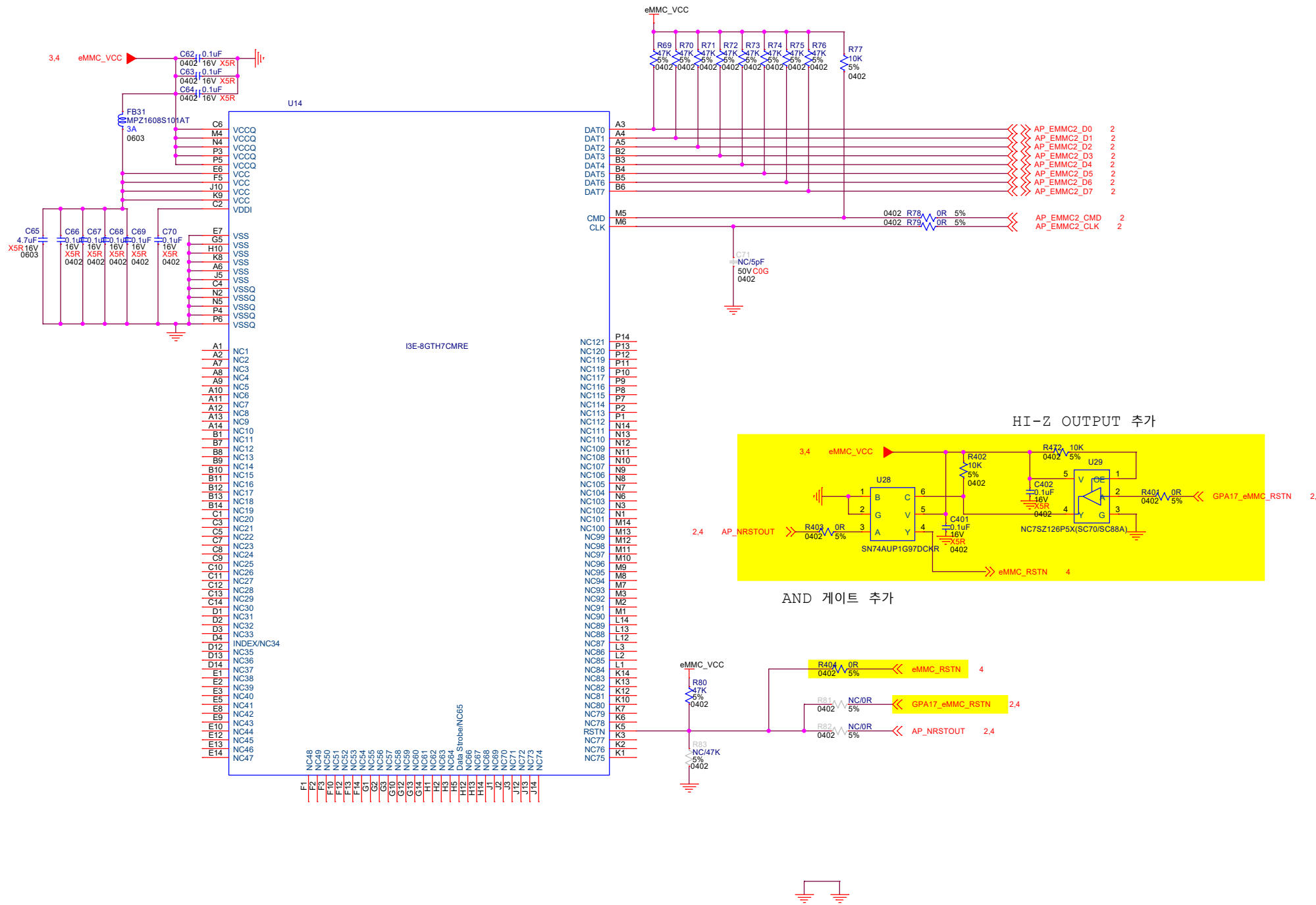
Functional Block Diagram



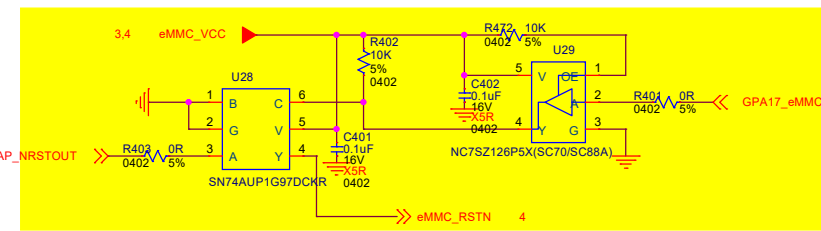
Power Block Diagram



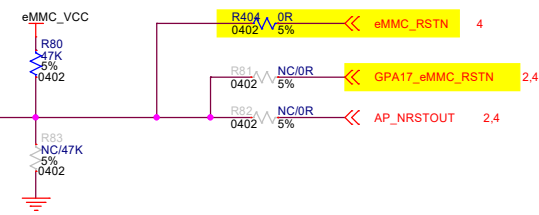




HI-Z OUTPUT 추가

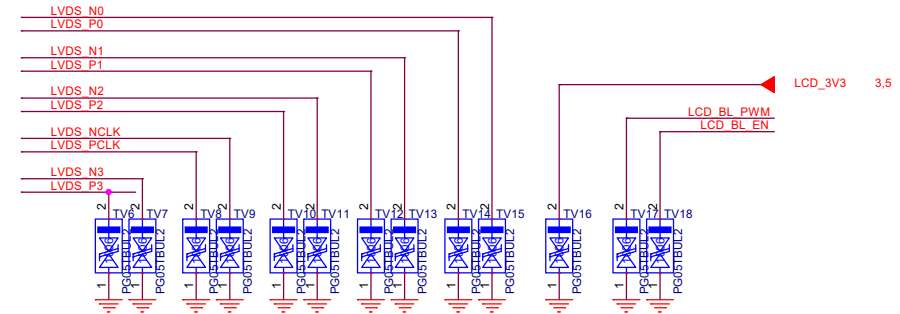
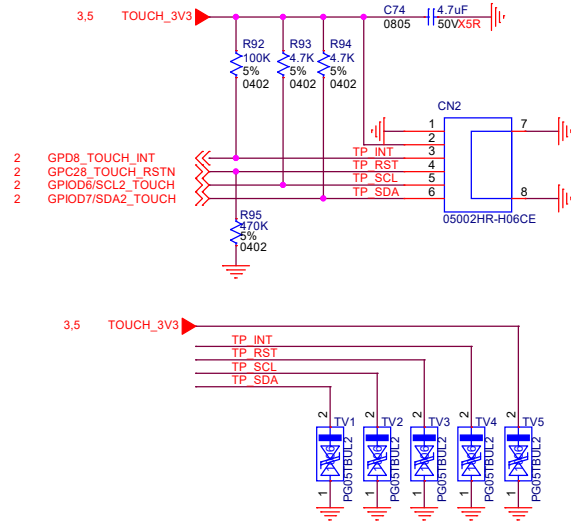
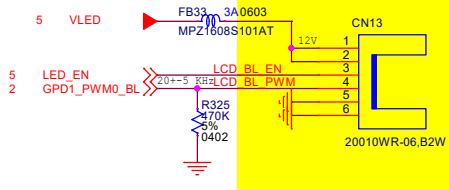
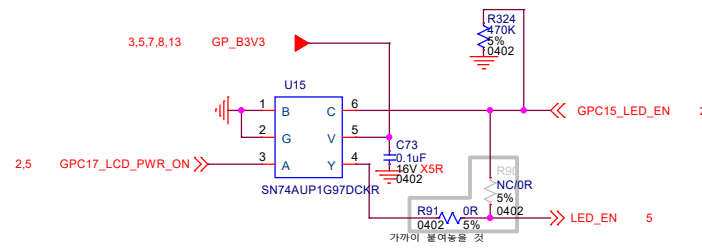
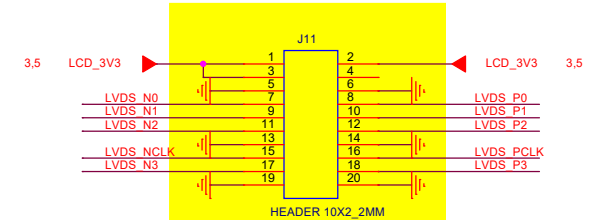
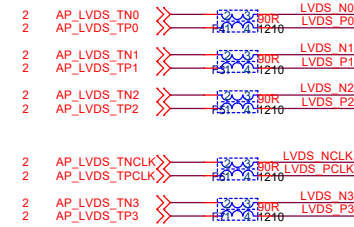
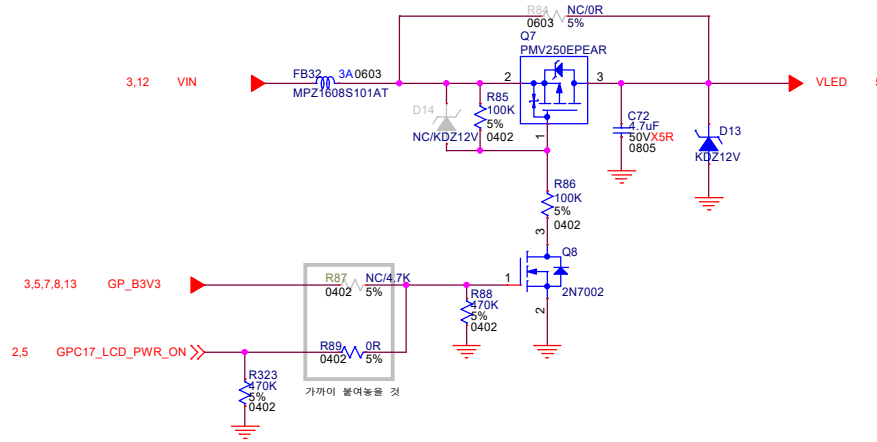


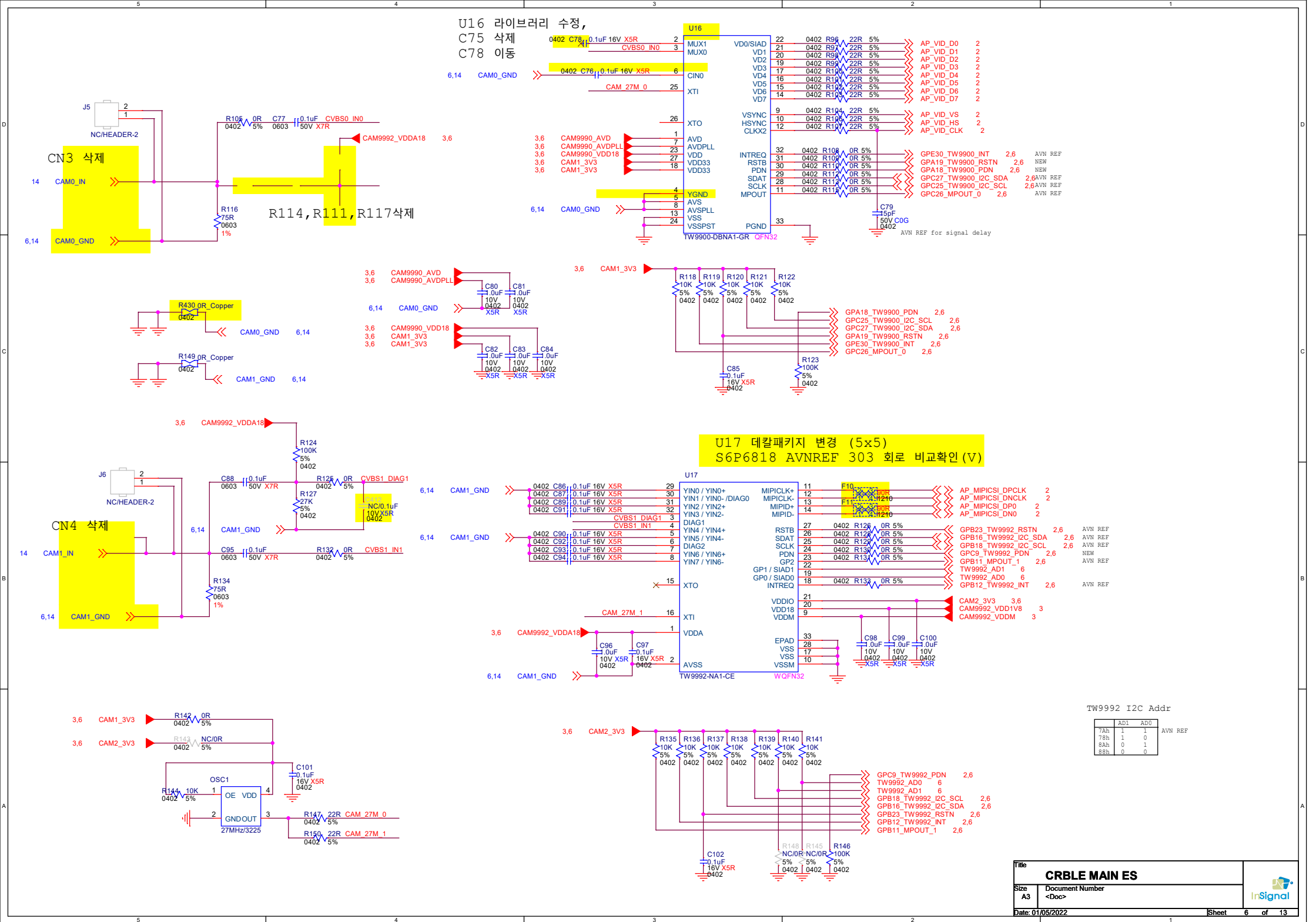
AND 게이트 추가



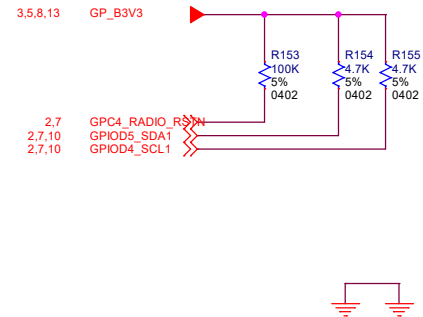
PV10109LZR40G-CO

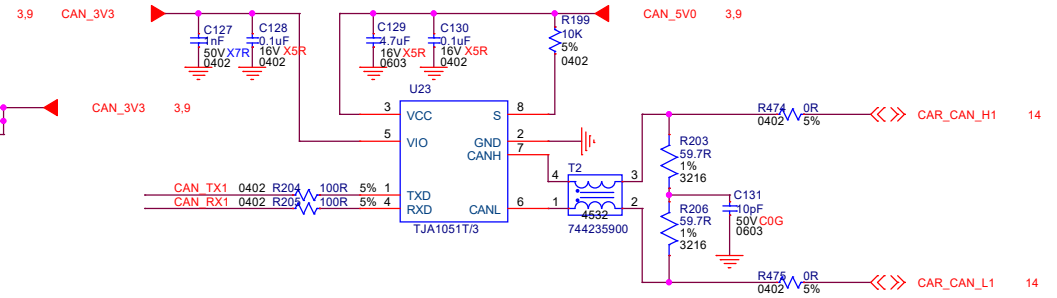
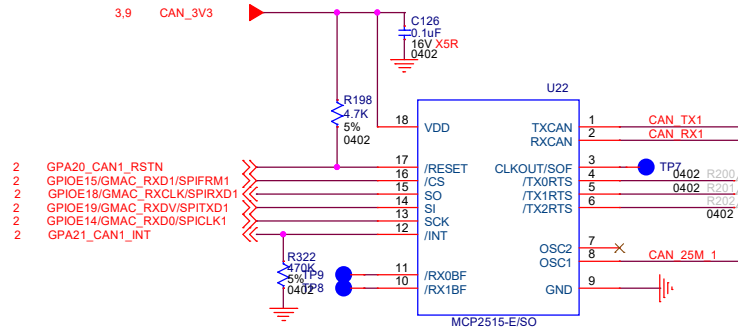
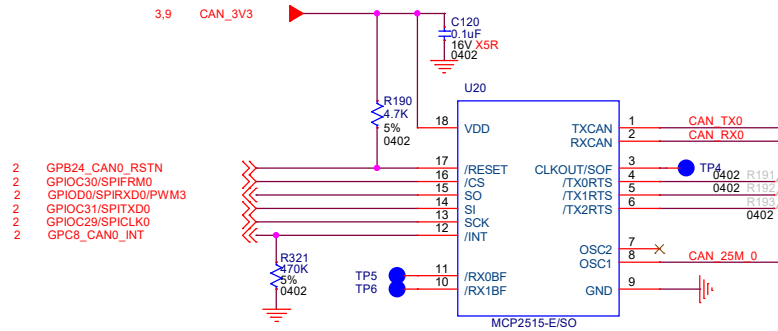
CN1 삭제 및
J11, CN13 추가

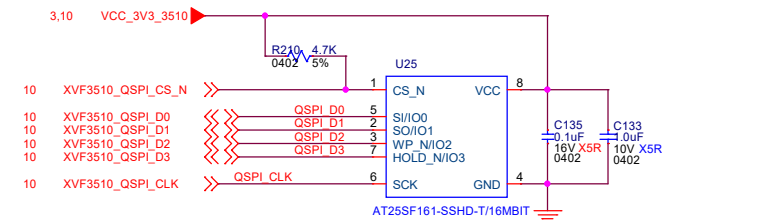
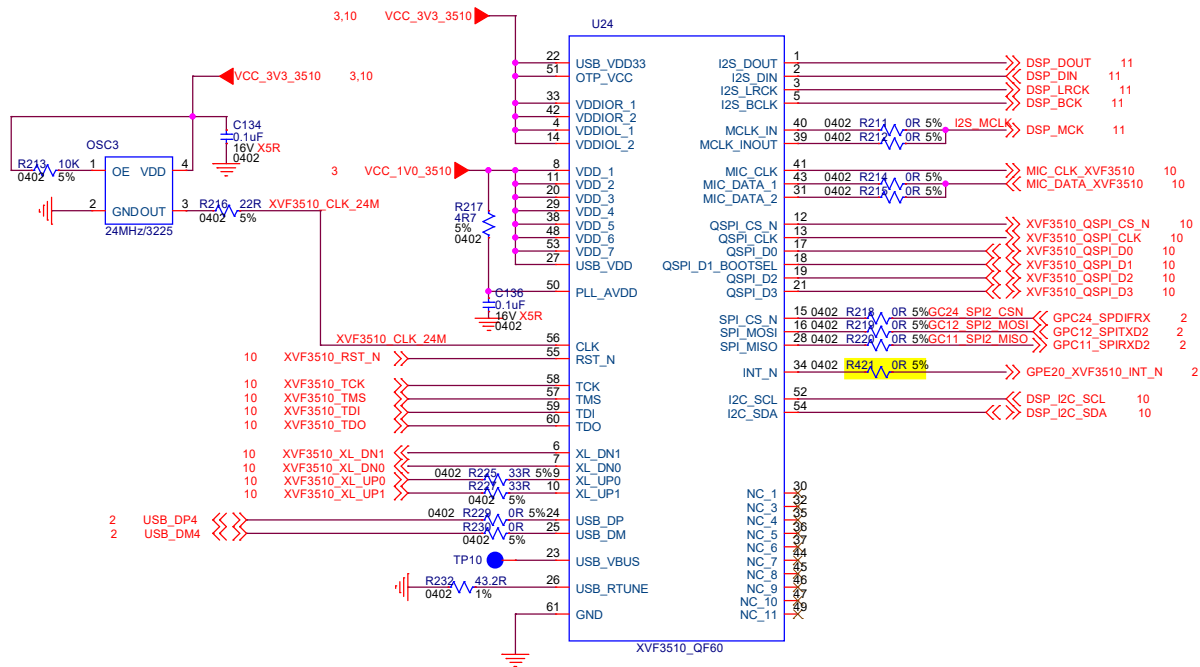




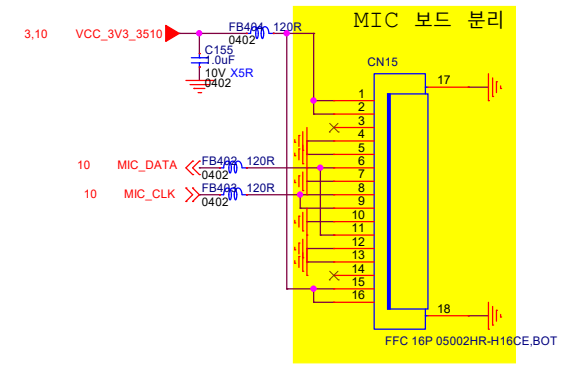
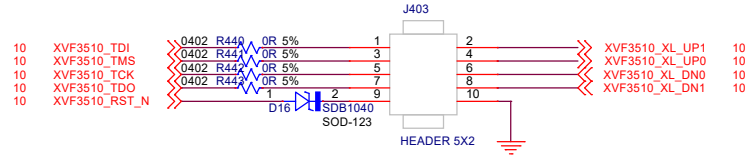
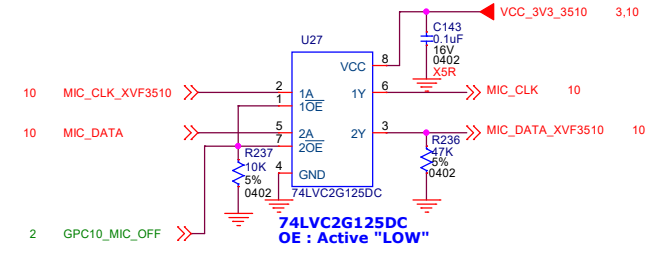
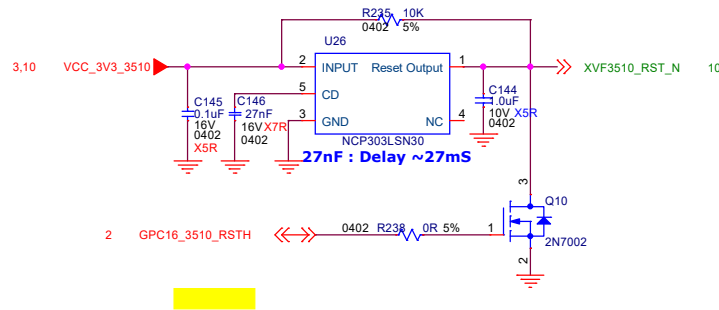
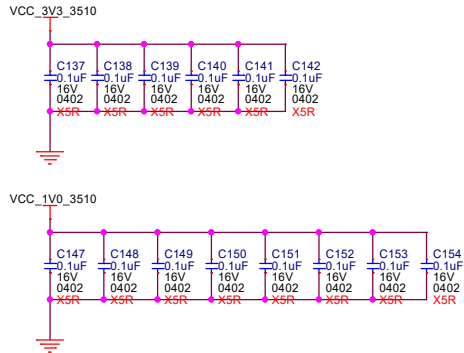
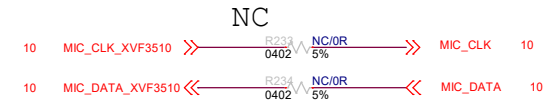
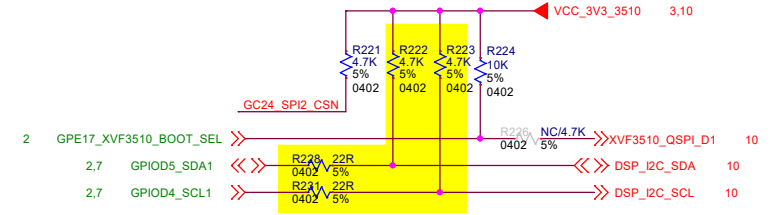
변경,
411, R412추가



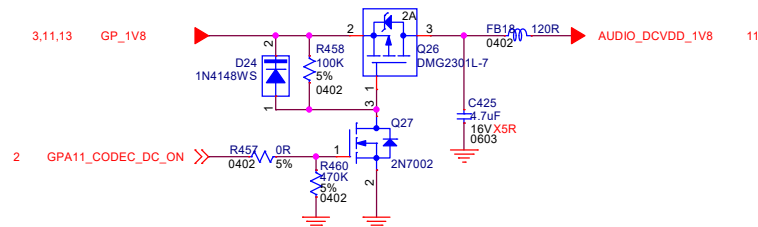
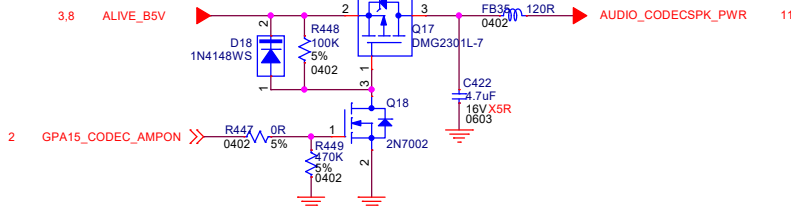
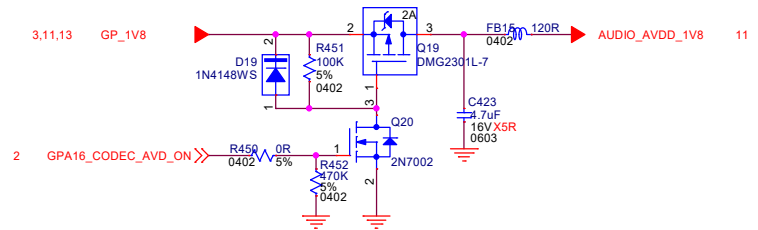
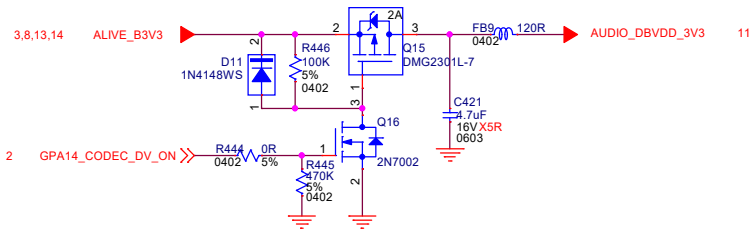
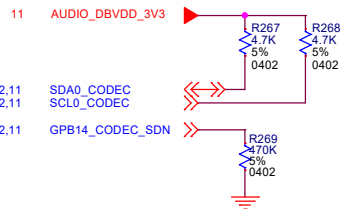
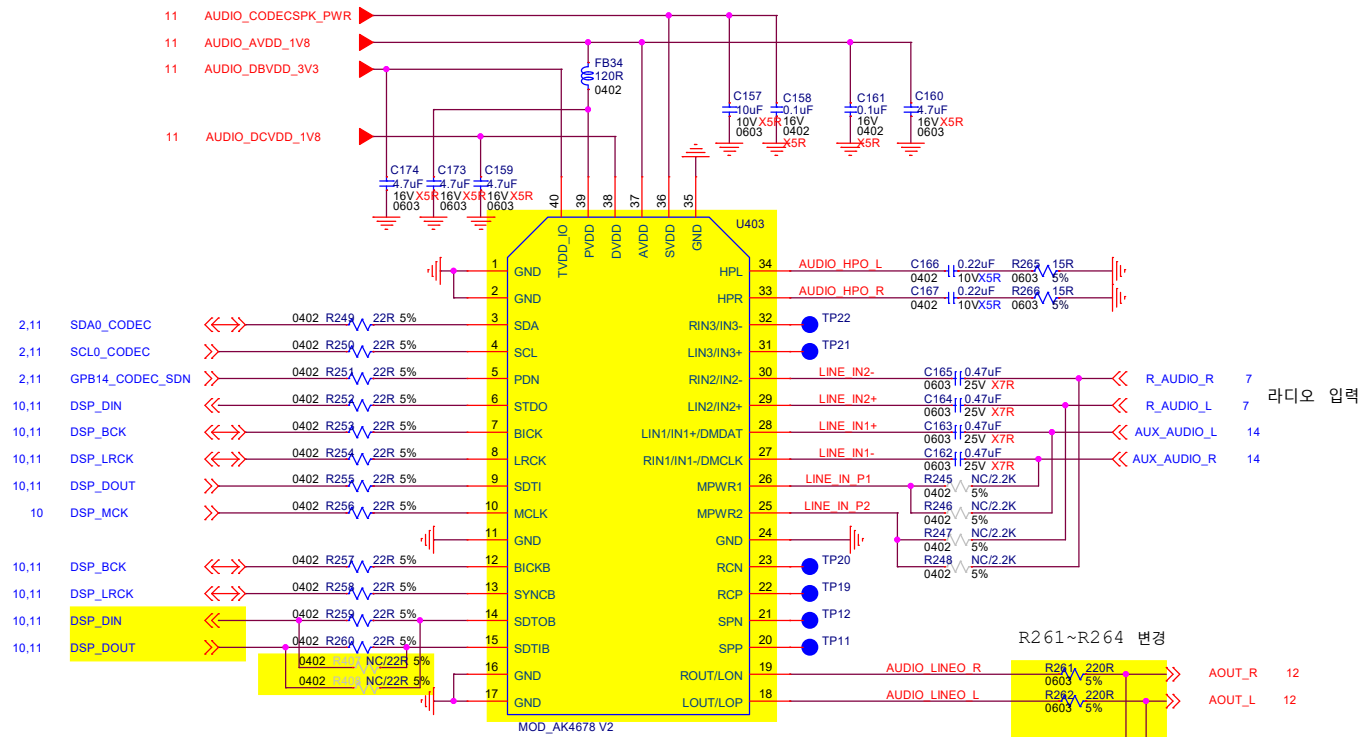




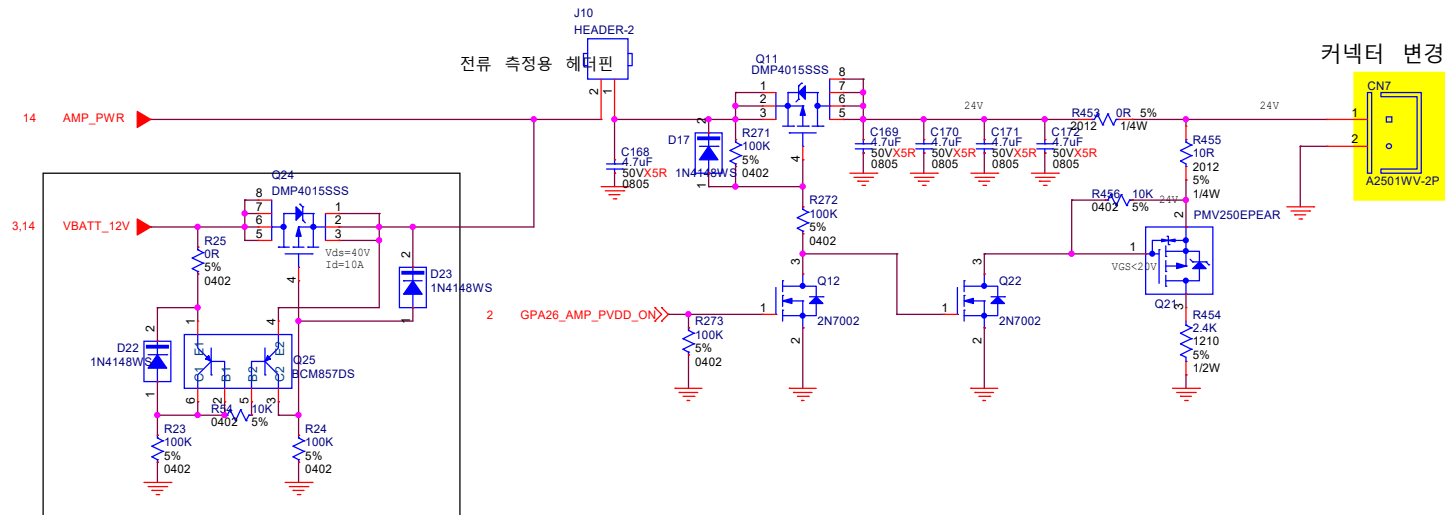
XVF3510_BOOT_SEL = 0 -> BOOT AS QSPI MASTER
XVF3510_BOOT_SEL = 1 -> BOOT AS SPI SLAVE



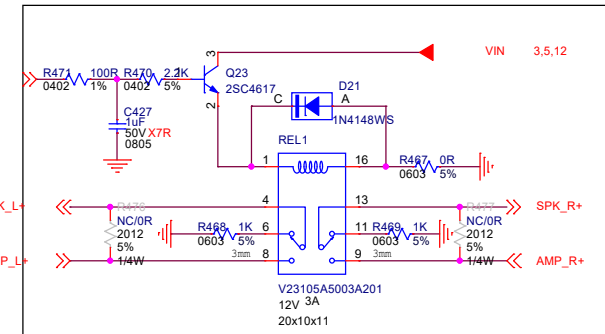
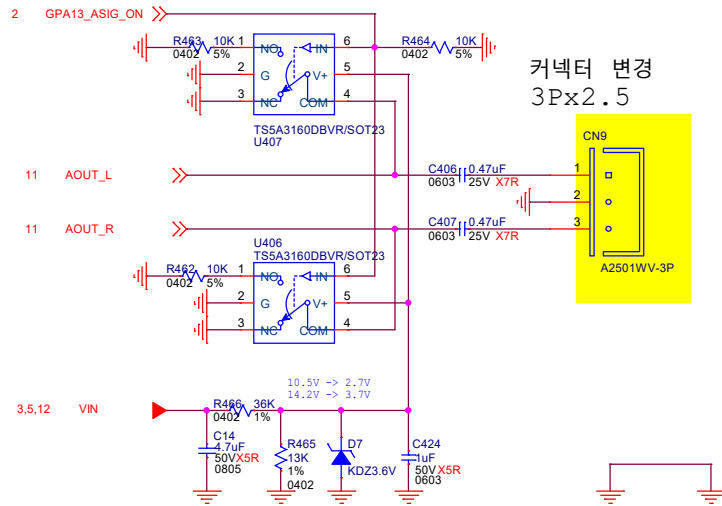
커넥터 변경 : 2.54 홀타입 2x5
 JTAG 연결



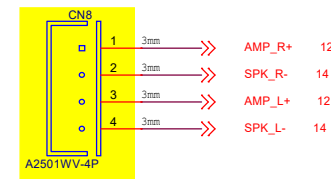
DGND

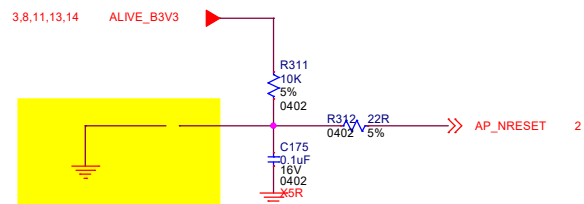
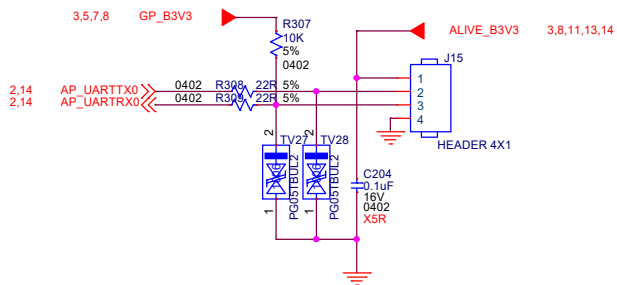


OUTPUT CODEC

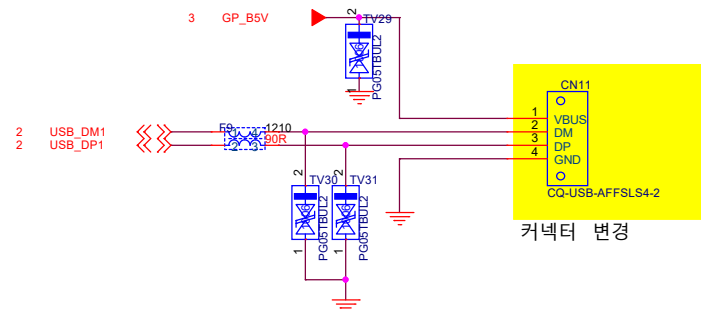
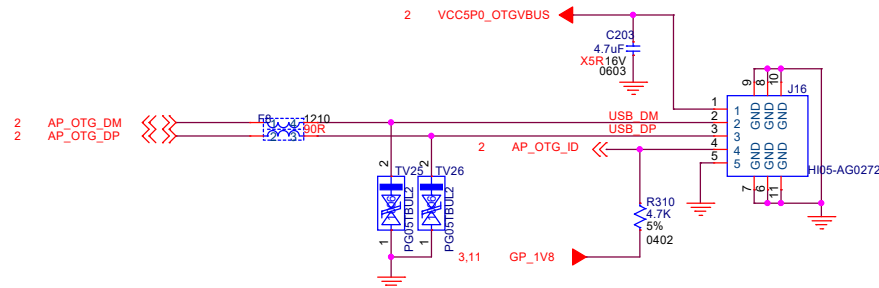


AMP OUT





커넥터 삭제



커넥터 변경

