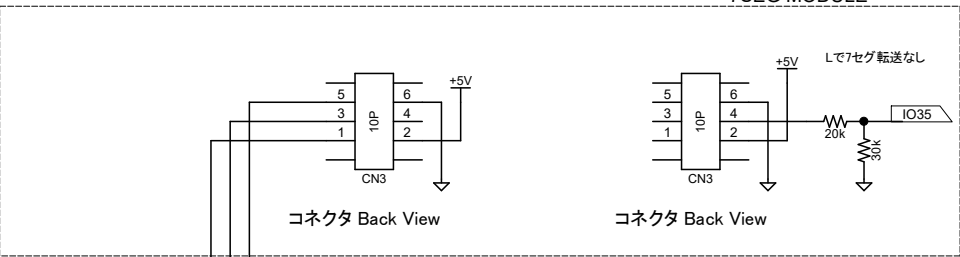
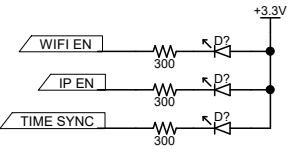
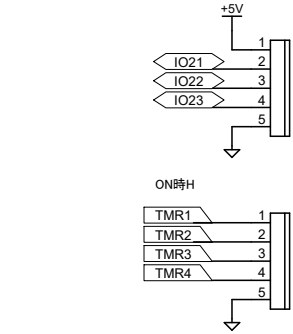
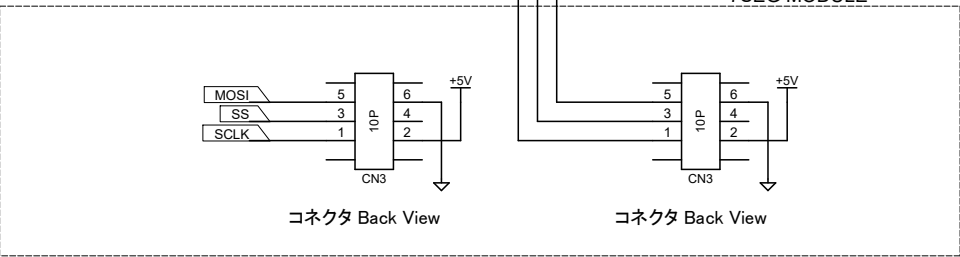


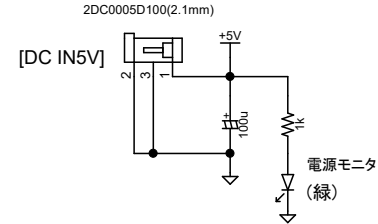
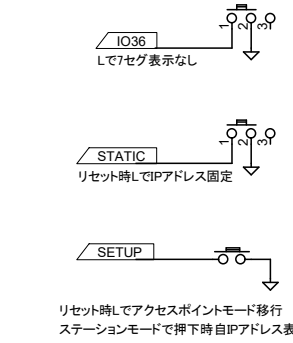
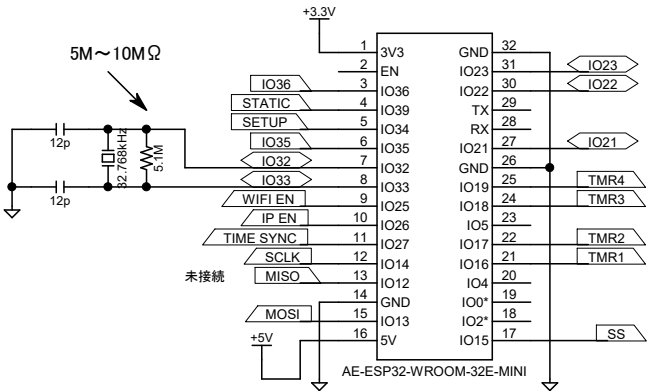
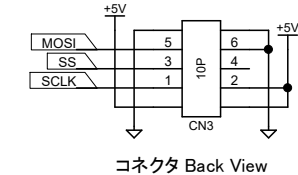
年月日(2段目)



時分秒(1段目)



中央の6PINがVFR0090(SPI 7SEG MODULE)互換です



2024.03.09 初版  
2024.05.02 7SEG配線、7SEG検出、表示OFF追加  
2024.08.08 SETUP、7SEG検出、表示OFFを入力専用ピンに移動

TITLE		DRAWING_No.	
KDB-WiFiClock01			
SHEET	DATE	DESIGN	黒猫電脳研究室
／	2024.08.08	Junnichi Tomaru	