

Instituto Federal de Santa Catarina – Campus Florianópolis

Departamento Acadêmico de Eletrônica



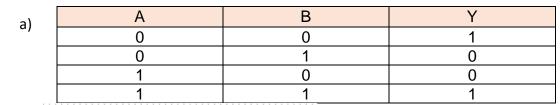
Eletrônica Digital I

Prof. Matheus Leitzke Pinto matheus.pinto@ifsc.edu.br

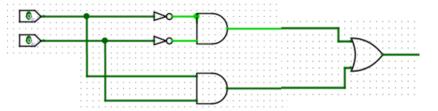
Lista de Exercícios - GABARITO

Soma de Produtos

1) Obtenha a expressão em SdP e o circuito lógico correspondente de cada tabela verdade apresentada abaixo.

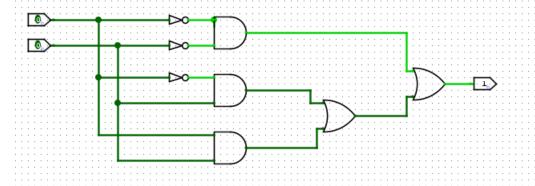


 \overline{A} , \overline{B} + A, B



Α	В	Υ
0	0	1
0	1	1
1	0	0
1	1	1

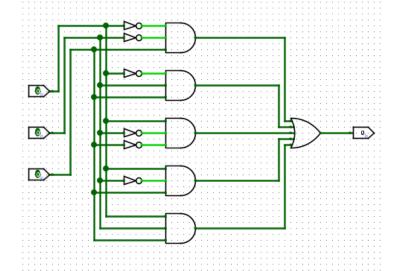
b)
$$\overline{A}.\overline{B} + \overline{A}.B + A.B$$



c) $\overline{A}.\overline{B}.C + \overline{A}.B.C + A.\overline{B}.\overline{C} + A.\overline{B}.C + A.B.C$

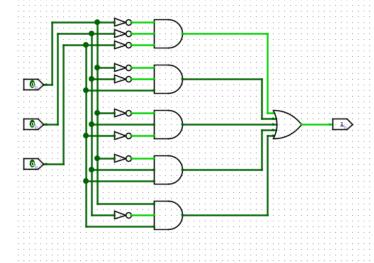
Á	В	С	Υ
0	0	0	0
0	0	1	1
0	1	0	0

0	1	1	1
1	0	0	1
1	0	1	1
1	1	0	0
1	1	1	1



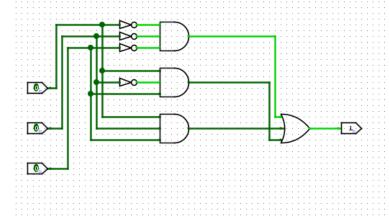
d) $\overline{A}.\overline{B}.\overline{C} + \overline{A}.\overline{B}.C + \overline{A}.B.\overline{C} + \overline{A}.B.C + A.\overline{B}.C$

d) n.b.c n.b.c n.b.c n.b.c n.b.c				
А	В	С	Υ	
0	0	0	1	
0	0	1	1	
0	1	0	1	
0	1	1	1	
1	0	0	0	
1	0	1	1	
1	1	0	0	
1	1	1	0	



e) $\overline{A}.\overline{B}.\overline{C} + A.\overline{B}.C +$	A. B. C		
Α	В	С	Y

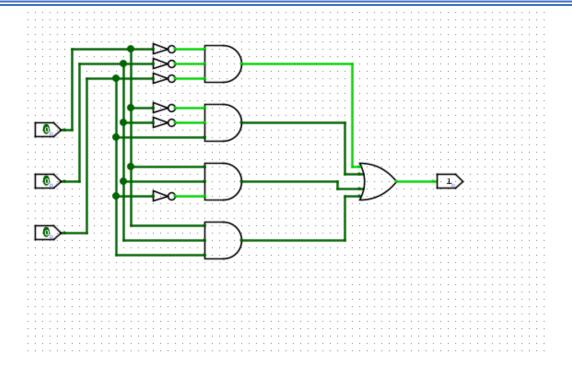
0	0	0	1
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	1
1	1	0	0
1	1	1	1



f) $\overline{A}.\overline{B}.\overline{C} + \overline{A}.\overline{B}.C + A.B.\overline{C} + A.B.C$

A	В	С	Υ
0	0	0	1
0	0	1	1
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	1
1	1	1	1

Eletrônica Digital I pág.: 4



g) $\overline{A}.\overline{B}.C.D + \overline{A}.B.\overline{C}.\overline{D} + \overline{A}.B.C.\overline{D} + \overline{A}.B.C.D + A.\overline{B}.\overline{C}.\overline{D}$

9/ 11.0.0.0 11.0.0.0 11.0.0.0 11.0.0.0				
А	В	С	D	Υ
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	1
0	1	0	0	1
0	1	0	1	0
0	1	1	0	1
0	1	1	1	1
1	0	0	0	1
1	0	0	1	0
1	0	1	0	0
1	0	1	1	0
1	1	0	0	0
1	1	0	1	0
1	1	1	0	0
1	1	1	1	0

Eletrônica Digital I pág.: 5

