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## **Lista de Exercícios – Circuitos lógicos**

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**1)** Chegue até o nível 50 no aplicativo "Make it true".

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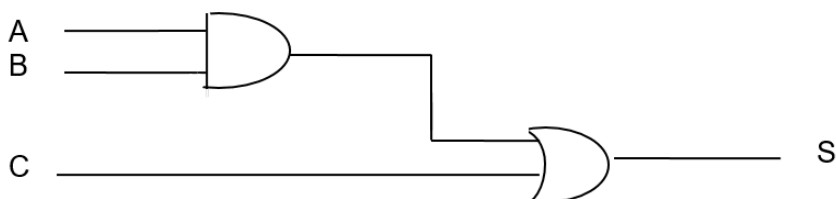
**2)** Por que os circuitos lógicos estudados aqui são chamados combinacionais?

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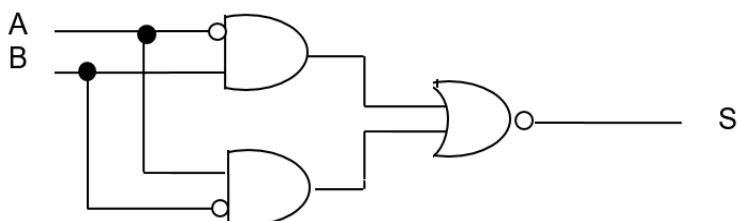
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**3)** Dados os circuitos lógicos abaixo, construa a tabela-verdade de cada.

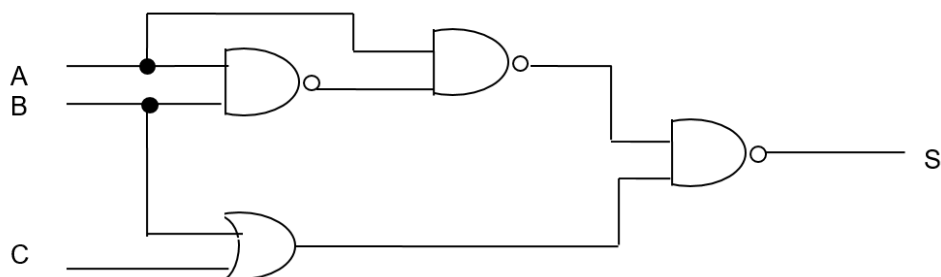
**a)**



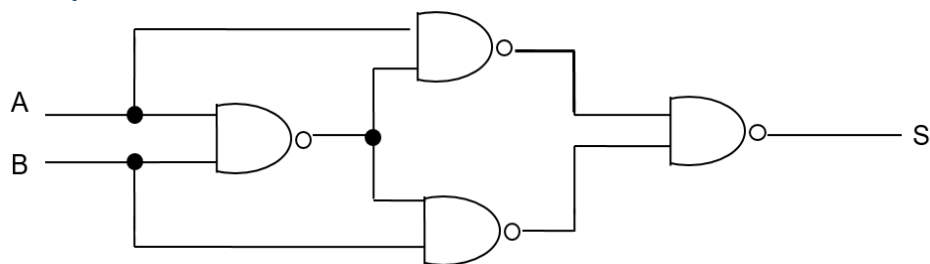
**b)**



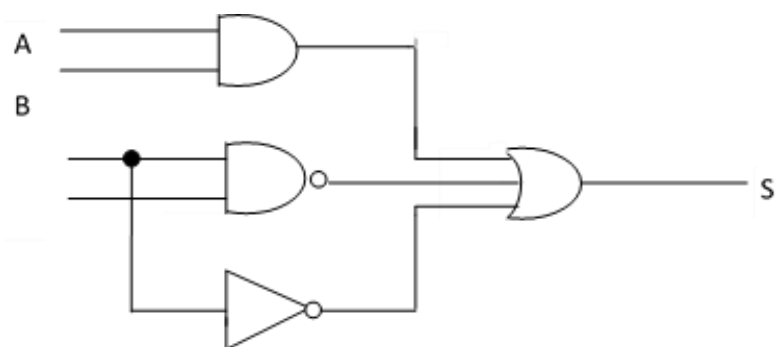
**c)**



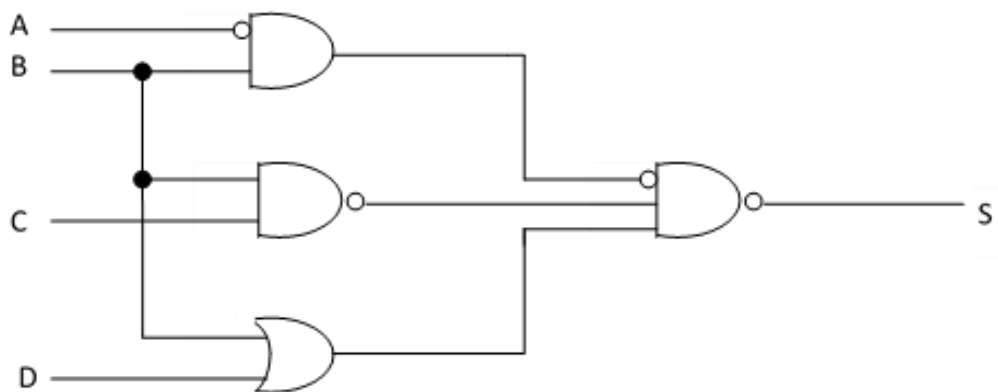
d)



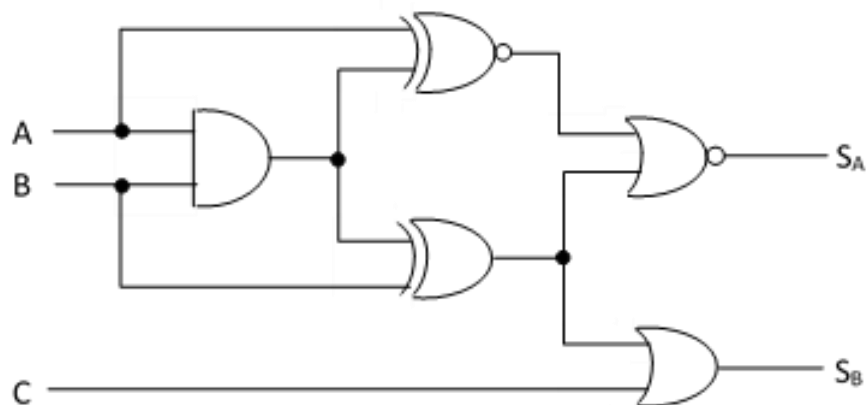
e)



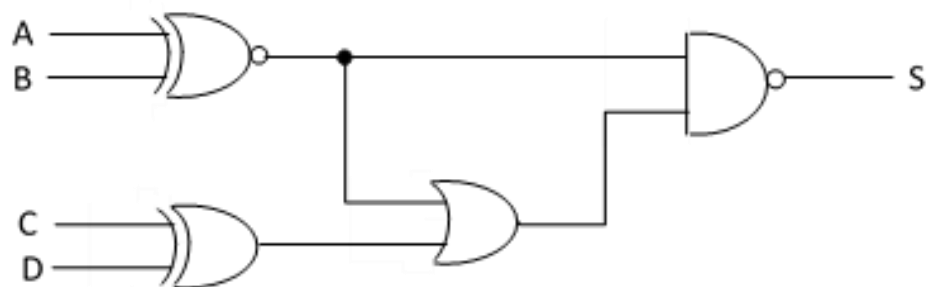
f)



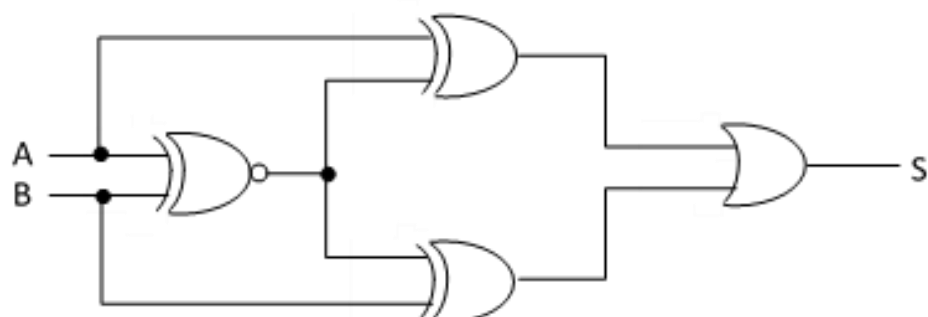
g)



h)



i)



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- 4)** Monte os circuitos lógicos acima, utilizando os CIs disponíveis no Thinkercad.
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