

Reveal Secrets in Adoring Poitras

A generic attack on white-box cryptography

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Correct and Secure Implementation

October 11, 2017, Crete

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Outline

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- What Is White-Box Cryptography (WBC)?
- WhiBox Contest

2 ■ Breaking Adoring Poitras

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- De-Virtualization
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- Boolean Circuits Minimization
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- Algebraic Analysis

What Is White-Box Cryptography (WBC)?

- WBC is resistant against **key extraction** in a **software** implementation of a cryptographic algorithm.
- The attacker **entirely** controls the running environment.
 - ▶ to record the computation trace (memory address/value, access type/time, etc)
 - ▶ to modify the control flow / intermediate value, etc
- No provably secure construction exists.
- All known practical constructions has been broken by **generic attacks** (DCA and DFA) before 2016.
- Applications:
 - ▶ digital rights management (DRM)
 - ▶ mobile payments

WhiBox Contest - CHES 2017 CTF

- Organized by ECRYPT CSA
- Two categories:
 - ▶ designers
 - ▶ breakers
- AES-128, physical limitation (<50M source code, <20M binary, <1s execution)
- 94 submitted challenges are all broken (most of them were alive < 1 day)
- *Hardest challenge: Adoring Poitras.*
 - ▶ Surviving for 28 days ($2.3 \times$ the 2nd hardest one)
 - ▶ Submitted by cryptolux (Biryukov-Udovenko)
 - ▶ Only broken by team_cryptoexperts
(Goubin-Paillier-Rivain-Wang)

Untidy Code

More than 1k functions

Readability Processing

- Duplicate / redundancy / unused codes elimination
- Functions / variables renaming
- Constants rewriting
- Code combination

Only 20 functions are remaining

```
void copy(uint KLedyCW, uint lRjmjY) {int_arr[(a+KLedyCW) & 0x3ffff] = int_arr[lRjmjY & 0x3ffff];}
void encode(uint owhj0, uint nlBqXn) {assign(owhj0, in_ptr[nlBqXn]);}
void decode(uint hFqeIO, uint jvXpt) {out_ptr[hFqeIO]=int_arr[(a+jvXpt)&0x3ffff];}
void rshift_xor(uint HCOL, uint ISRFdIp, uint uFYFMX) { int_arr[HCOL&0x3ffff]^=1&(int_arr[(a+ISRFdIp)&0x3ffff]>>ufYFMX); /*prior to the assignment, we have to make sure that the value is not zero*/
void lshift_xor(uint NCjBw, uint OMQBxqa, uint AyoLFz) { uint dyf5=(int_arr[(a+AyoLFz)&0x3ffff])&1;int_arr[(a+NcJBw)&0x3ffff]^=dyf5;
void expand_bit(uint SteQld, uint ZubEP, uint ZiQz) {int_arr[(a+SteQld)&0x3ffff]=~((int_arr[(a+ZubEP)&0x3ffff]>>ZiQz)&1);}

uint lookup1(uint AKBKig) {return int_arr[(a+AKBKig)&0x3ffff];}
uint lookup2(uint WAdV, uint ZcVdJ) {return int_arr[(WAdV-ZcVdJ)&0x3ffff];}

void assign(uint UbEJi, uint UmwjUh0) {int_arr[(a+UbEJi)&0x3ffff]=UmwjUh0;}
void assign_a(uint wE0kx) {a = wE0kx;}
void assign_b(uint fnmqxL) {b=int_arr[fnmqxL]&0x0ffff;}
void update_a() {a=lookup2(1592,mix(b)); printf("%lu\n",a);}
void update_b() {b=0x7fff&lookup2(522,mix(b));}

void mystery(uint wJxeA, uint QBGXUN) {uint t = (~int_arr[(a+QBGXUN)&0x3ffff])&0x7fff; assign(wJxeA,lookup2(2979,mix(t)));}

// bitwise operation
void xor(uint oEHmwk, uint KCZu, uint MtCA) {int_arr[(a+oEHmwk)&0x3ffff]=int_arr[(a+KCZu)&0x3ffff]^int_arr[(a+MtCA)&0x3ffff];}
void and(uint bmmFp, uint UNFg, uint PqCtYZ) {uint t = int_arr[(a+UNFg)&0x3ffff]&int_arr[(a+PqCtYZ)&0x3ffff];}
void or(uint eTGI, uint udoxFs, uint mezPNN) {int_arr[(a+eTGI)&0x3ffff]=int_arr[(a+udoxFs)&0x3ffff]|int_arr[(a+mezPNN)&0x3ffff];}
void not(uint YfnT, uint JKTW) {int_arr[(a+YfnT)&0x3ffff]=~int_arr[(a+JKTW)&0x3ffff];}

// jump
void goto_f(uint LKhOC) {pc = bop + LKhOC;}
void jump_if(uint DbvJO, uint FleFNIf, uint LeHf) { if(lookup2(2979,mix(b))==lookup2(DbvJO, FleFNIf) || count >= 64) {printf("%d\n",count); pc = bop + LeHf;}}
```

Universal Turing Machine

\Rightarrow UTM(RASP)

Universal Turing Machine (2)

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Universal Turing Machine (3)

De-virtualization - Simulate the UTM

```
else if (eMmr == 3) {
    void (*QiEb)(uint, uint, uint);
    QiEb = (void*)funcptrs[*pc++];
    uint *AnezsV = (uint*)pc;
    pc += eMmr*8;
    /* QiEb(AnezsV[0], AnezsV[1], AnezsV[2]); */
#ifndef SIMULATE
    printf("%8s(%d,%d,%d);\n", flist[*((pc-1-eMmr*8)], AnezsV[0], AnezsV[1], AnezsV[2]);
#endif
}
else if (eMmr == 4) {
    void (*QiEb)(uint, uint, uint, uint);
    QiEb = (void*)funcptrs[*pc++];
    uint *AnezsV = (uint*)pc;
    pc += eMmr*8;
    /* QiEb(AnezsV[0], AnezsV[1], AnezsV[2], AnezsV[3]); */
#ifndef SIMULATE
    printf("%8s(%d,%d,%d,%d);\n", flist[*((pc-1-eMmr*8)], AnezsV[0], AnezsV[1], AnezsV[2], AnezsV[3]);
#endif
}
```

We get a bitwise-based program (600k operations).

Bitwise-Based Program

Input: plaintext bits (b_1, b_2, \dots, b_{128})

Output: ciphertext bits (c_1, c_2, \dots, c_{128})

```
for i = 1 to 128 do
    t[addr1,i] ← 0bbibi⋯bi
    for j = 1 to 64 do
        t[addr2,i + j * 212] ← t[addr1,i]                                ▷ expand  $b_i$  to unsigned long integer (64 bits)
    end for
end for

BITWISEOPERATIONLOOP1
BITWISEOPERATIONLOOP2
...
BITWISEOPERATIONLOOP2573

for i = 1 to 129 do
    t[addr3,i] ← vi                                                 ▷  $v_i \in GF(2)$  is a constant
    for j = 1 to 64 do
        tmp ← t[addr4,i + j * 212] ⊕ t[addr5,i + j * 212]
        t[addr3,i] ← t[addr3,i] ⊕ PARITY(tmp)                            ▷ PARITY computes the number of 1-bit modulo 2
    end for
end for

BITWISEOPERATIONLOOP2574
...
BITWISEOPERATIONLOOP2582

for i = 1 to 128 do
    ci ← t[addr6,i]
end for
```

Bitwise-Based Program to Boolean Circuits

- 64 (loop length) * 64 (number of bits in a unsigned long integer) independent AES computations operated in boolean circuits
- 3 out of 64*64 are the real and identical AES computations (e.g., bit 42 of loop 26)
- Hence, the bitwise-based program can be simplified as a boolean circuits with 600k gates (XOR, AND, OR, NOT).

Breakers are stopped by this step??

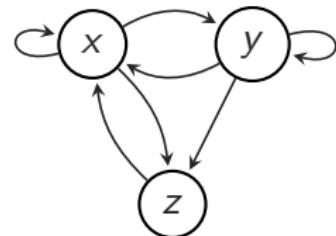
Boolean Circuits Minimization

- Constant variable detection and propagation
- Deduplication
- “Potential” pseudorandomness detection and removal
- Dead code elimination
- Repeat the above steps until no more constant / duplicate / “potential” pseudorandomness can be detected

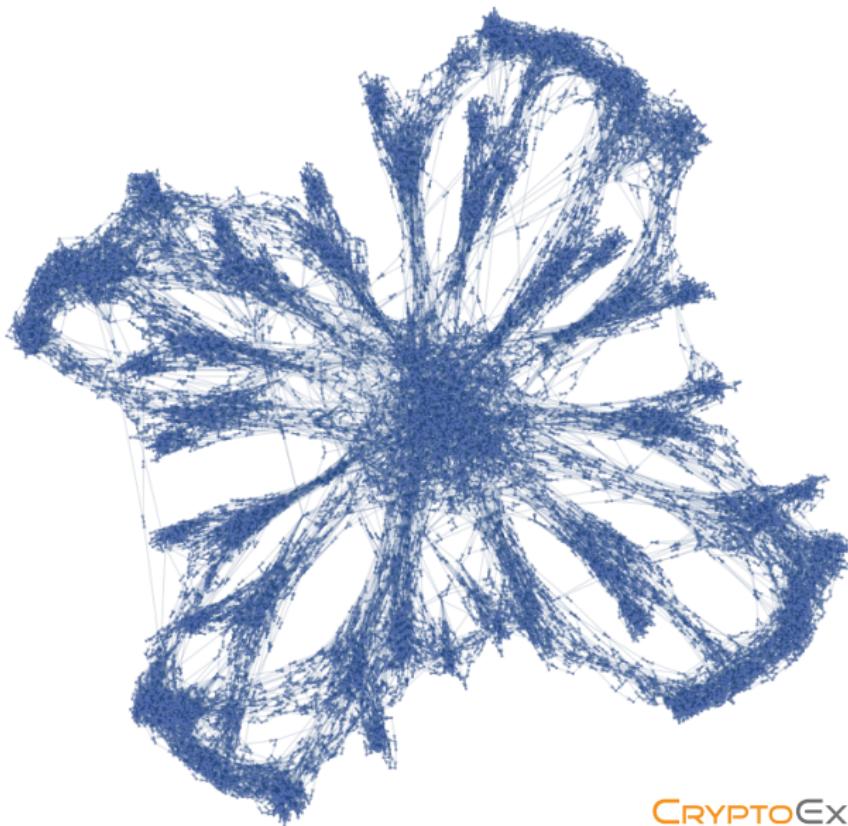
The circuits is reduced to 280k boolean gates (53% smaller)

Data Dependency Graph (DDG)

```
x =a;  
y =b;  
x =y + x;  
y =x * y;  
z =x - y;  
x =z * x;
```



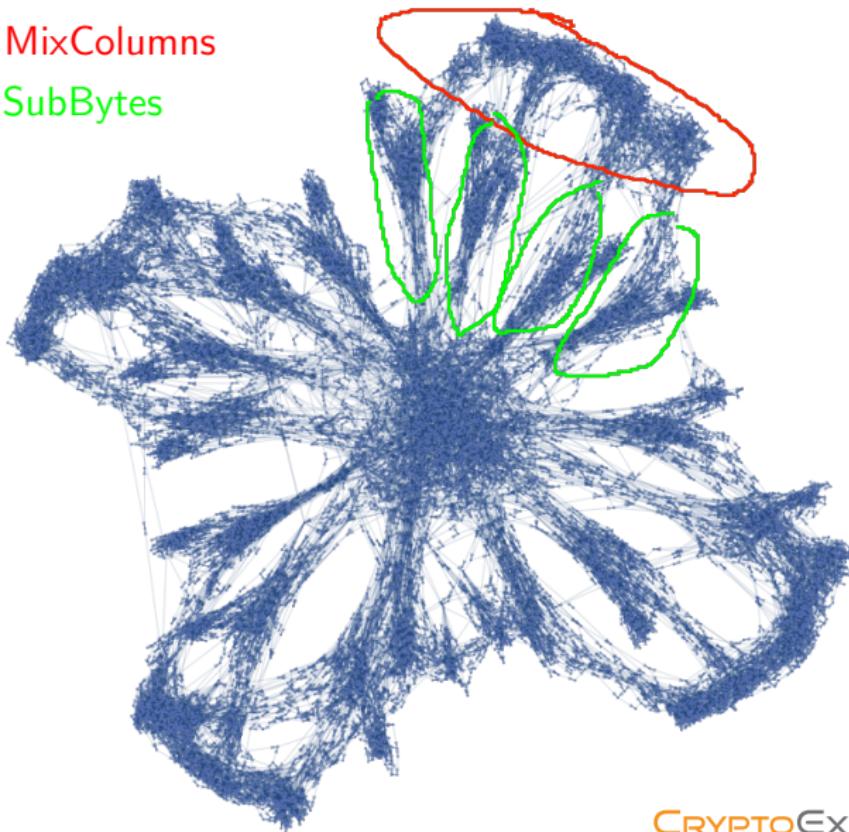
DDG of the Circuits (First 5%)



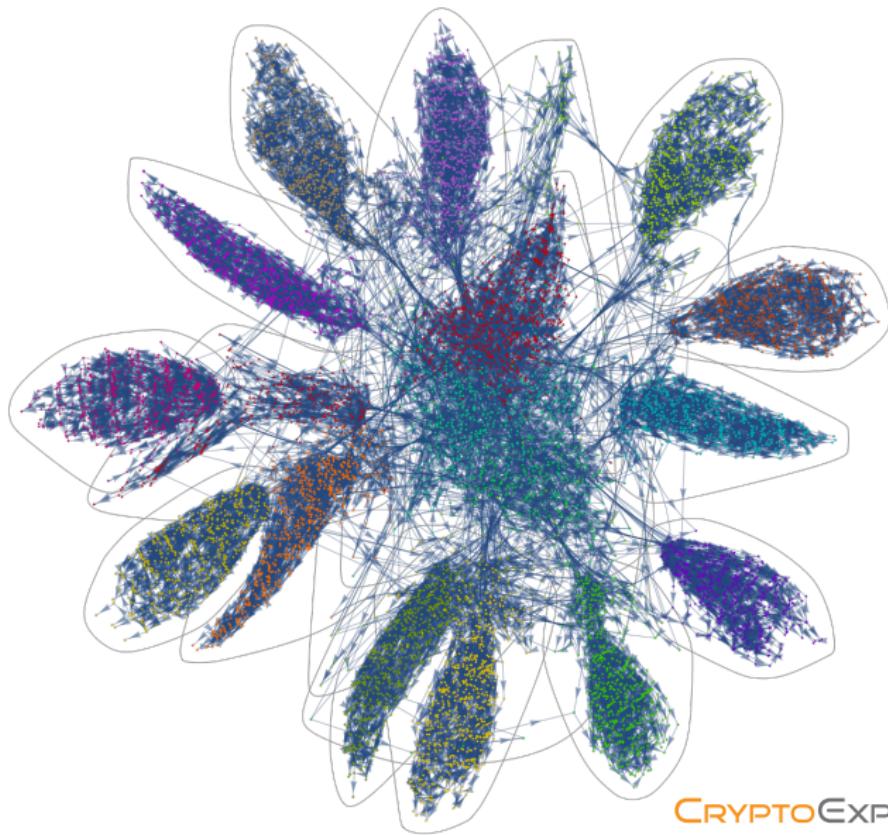
First Round Computation of AES

MixColumns

SubBytes



Extracting the Branches (Clustering)



Assumption

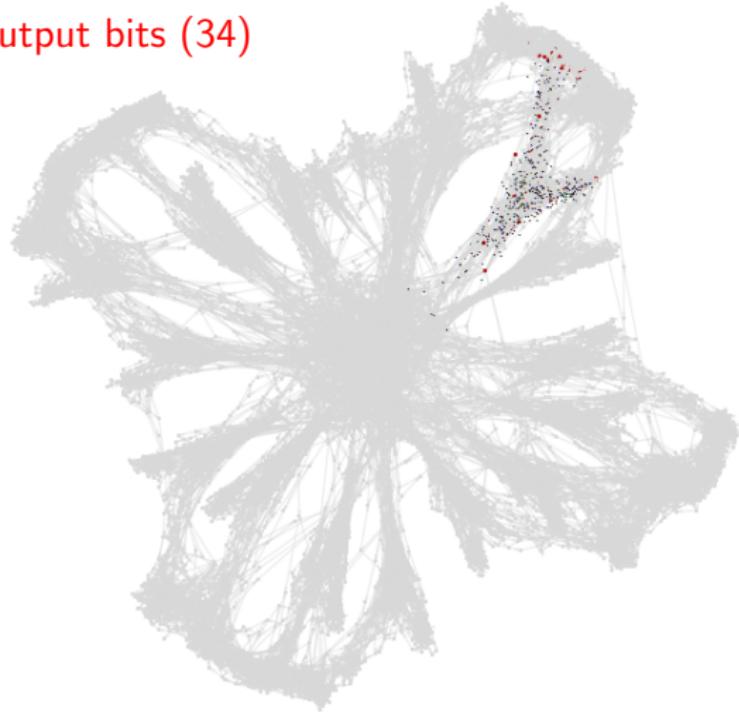
Assumption (Informal)

Each of the green "branch" corresponds to an individual S-Box computation in the first round of AES, **the t -bit output** (s_1, s_2, \dots, s_t) of which is a **linear encoding** of a real S-Box output bit.

Output Bits of A Branch

Bits in a branch (530)

S-Box output bits (34)



Solve A System of Linear Equations

$$\begin{bmatrix} s_1^{(1)} & s_2^{(1)} & \dots & s_{34}^{(1)} & 1 \\ s_1^{(2)} & s_2^{(2)} & \dots & s_{34}^{(2)} & 1 \\ \vdots & \vdots & \ddots & \vdots & \vdots \\ s_1^{(n)} & s_2^{(n)} & \dots & s_{34}^{(n)} & 1 \end{bmatrix} \begin{bmatrix} a_1 \\ a_2 \\ \vdots \\ a_{34} \\ a_{35} \end{bmatrix} = \begin{bmatrix} \text{SBox}(x^{(1)} \oplus \hat{k})[i] \\ \text{SBox}(x^{(2)} \oplus \hat{k})[i] \\ \vdots \\ \text{SBox}(x^{(n)} \oplus \hat{k})[i] \end{bmatrix}$$

If $n \geq 35 + 8 + \lambda$, $\Pr[\hat{k} \neq k^* \text{ has a solution}] \leq 2^{-\lambda}$.

Results

```
In[488]:= LinearBreak[data]
key=0x0
key=0x10
key=0x20
key=0x30
key=0x40
key=0x50
key=0x60
key=0x70
key=0x80
key=0x90
key=0xa0
key=0xb0
key=0xc0
!!!!!! 2 - 0 - 0xcf !!!!!!!
!!!!!! 2 - 1 - 0xcf !!!!!!!
!!!!!! 2 - 2 - 0xcf !!!!!!!
!!!!!! 2 - 3 - 0xcf !!!!!!!
!!!!!! 2 - 4 - 0xcf !!!!!!!
!!!!!! 2 - 5 - 0xcf !!!!!!!
!!!!!! 2 - 6 - 0xcf !!!!!!!
!!!!!! 2 - 7 - 0xcf !!!!!!!
key=0xd0
key=0xe0
key=0xf0
```

Why DCA / DFA does not work?

15 used / 34 output bits

Why DCA / DFA does not work?

Each real bit is encoded by at least 2 intermediate bits.

Why DCA / DFA does not work?

Each intermediate bit is encoding at least for 2 real output bits.

Summary and Future Works

- White-box cryptography is widely deployed.
- All known constructions are broken by DFA and DCA attacks before 2016.
- A algebraic analysis attack is applied to break challenges.

Future works:

- Countermeasures to design
- Generalization of this attack
- Theoretical construction

Thank you!

Question?