



LINCOLN UNIVERSITY COLLEGE  
FACULTY OF ENGINEERING AND BUILT ENVIRONMENT

ELECTRONICS COMMUNICATION SYSTEM  
DEE 4413

ASSIGNMENT (15%)

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PROGRAMME: Diploma in Electrical Electronics Engineering (ODL)

SEMESTER: Y1S1

1) Explain in detail the communication system and explain the elements of communication systems.

(5 Marks)

2) A carrier wave of frequency 10 MHz and peak value 10V is amplitude modulated by a 5-kHz sine wave of amplitude 6V. Determine

- (i) Modulation factor
- (ii) Sideband frequencies
- (iii) Amplitude of sideband components

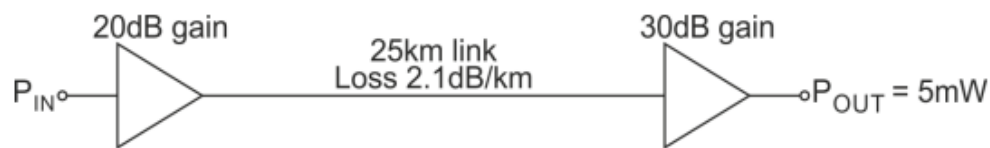
Then, draw the frequency spectrum.

(5 Marks)

3) Define the phase locked loop and draw the block diagram of the PLL.

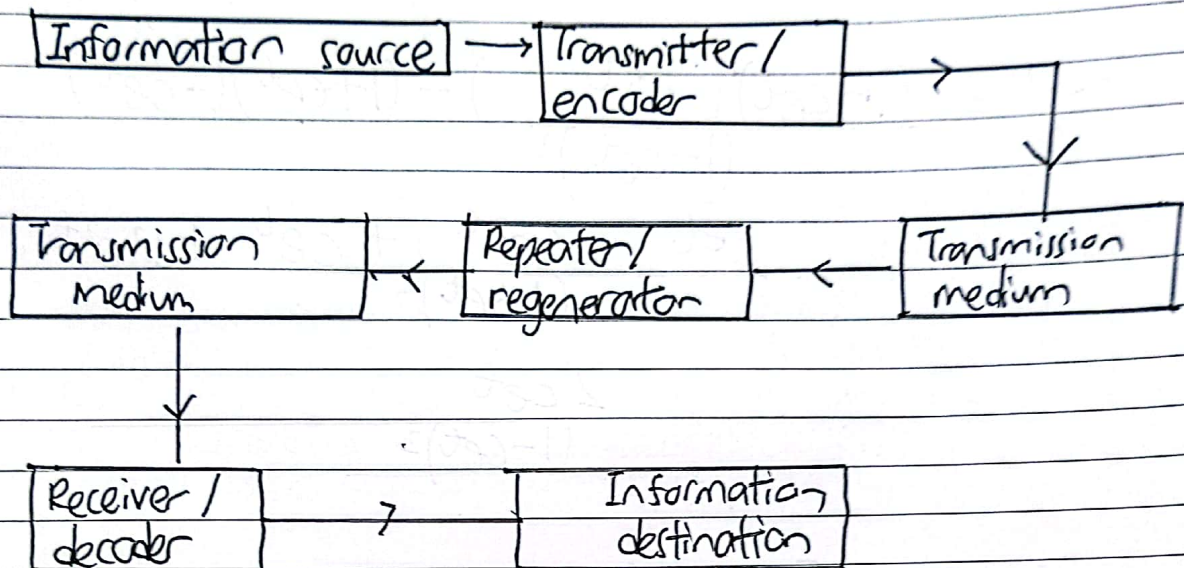
(3 Marks)

4) Calculate the value  $P_{IN}$  for the following system:



(2 Marks)

- 1) Explain in detail the communication system and explain the elements of communication system.



- 1) **Information source** : This entity generates the information to be communicated and it could be a person, a computer, a sensor etc.
- 2) **Transmitter/Encoder** : The transmitter takes the information from the source and encodes it into a suitable format for transmission. This may involve converting analog signals to digital, modulation for radio transmission, or other encoding techniques.
- 3) **Transmission medium** : This is the physical pathway through which the encoded information is sent from the transmitter to the receiver. It could be a wire, fiber optic cable or wireless signals through the air.
- 4) **Amplifier/Regenerator** : In some cases, especially for long-distance communication, the signal may degrade as it travels through the transmission medium. Amplifiers or regenerators may be used to boost the signal strength or regenerate the signal to maintain its integrity.



5) Receiver/Decoder: The receiver is responsible for capturing the transmitted signal from the medium and decoding it back into a format that can be understood by the intended recipient.

This involves processes such as demodulation, decoding and error correction.

6) Information Destination: This is the final destination of the communicated information. It could be another person, a computer, a storage device, or any entity that receives and utilizes the information for its intended purpose.

ex  
put

x  $E_{IN}$



2) A carrier wave of frequency 10 MHz and peak value 10V is amplitude modulated by a 5-kHz sine wave of amplitude 6V. Determine

- i) modulation factor
- ii) Sideband frequency
- iii) Amplitude of sideband components

Then, draw the frequency spectrum

i) The modulation factor, also known as the modulation index, is given by

$$m = \frac{A_m}{A_c}, \text{ where } A_m = \text{amplitude of the modulating signal}$$

$$m = \frac{6V}{10V} = 0.6$$

$$A_c = \text{amplitude of the carrier signal}$$

ii) sideband frequency are given by:

$$f_{\text{sideband}} = f_c \pm f_m, \text{ where } f_{\text{sideband}} = \text{sideband frequency}$$

$$f_{\text{sideband}} = 10 \text{ MHz} \pm 5 \text{ kHz}$$

$$f_c = \text{frequency of the carrier signal}$$

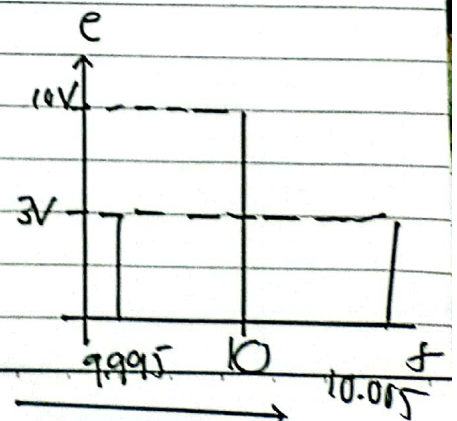
$$f_m = \text{frequency of the modulating signal}$$

So sideband frequencies are  $10 \text{ MHz} + 5 \text{ kHz}$  and  $10 \text{ MHz} - 5 \text{ kHz}$

iii) Amplitude of sideband components:

$$A_{\text{sideband}} = m \cdot A_c = 0.6(10V) = 6V$$

$$\text{Amplitude of each sideband} = \frac{m E_c}{2} = \frac{0.6 \times 10}{2} = 3V$$



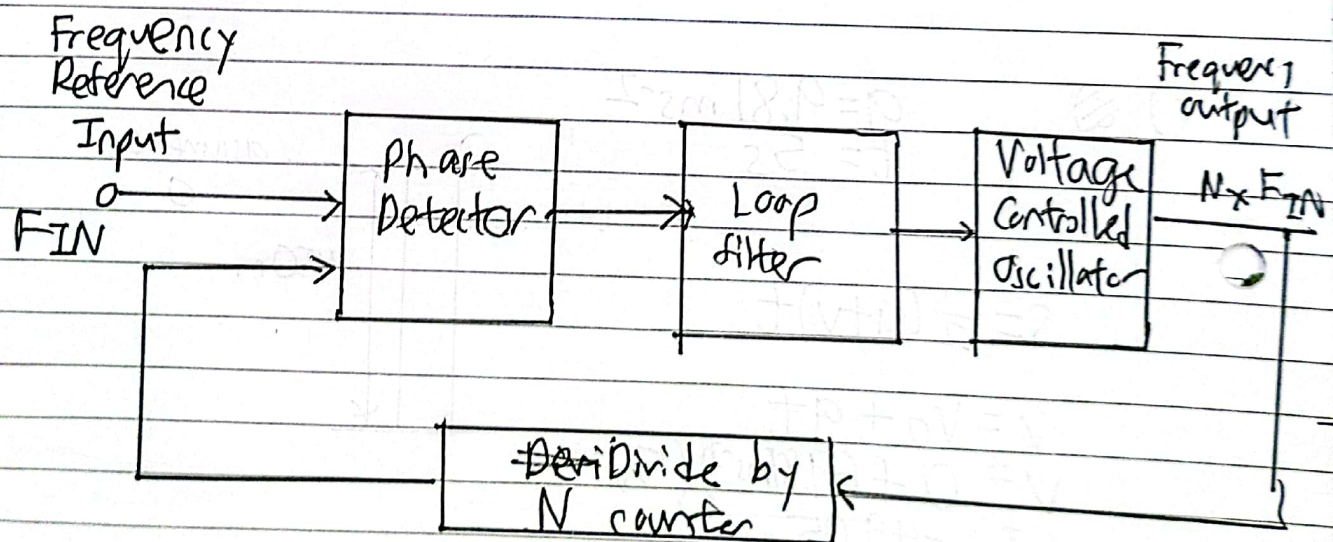
frequency  
(MHz)



5) Define the phase locked loop and draw the block diagram of the PLL.

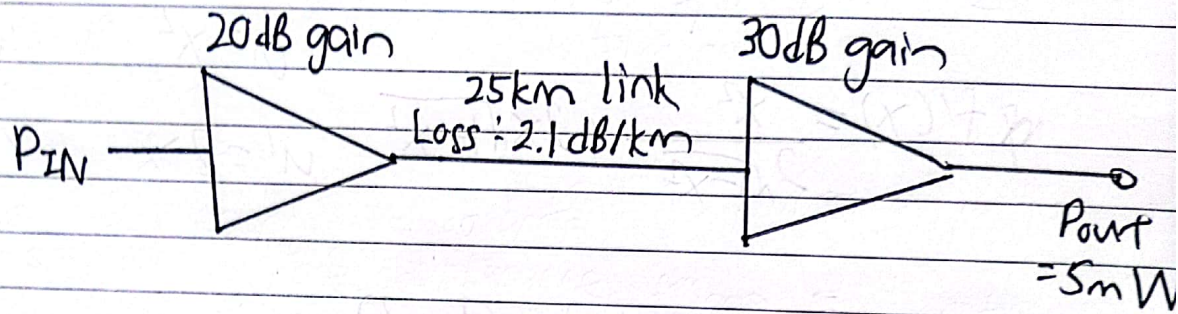
PLL stands for 'Phase-Locked Loop' and it is basically a closed loop frequency control system, which functioning is based on the phase difference between the input and output signals of the controlled oscillator (CO). The phase locked loop method of frequency synthesis is now the most commonly used method of producing high frequency oscillations between in modern communications equipment.

PLL circuits are now frequently being used to demodulate FM signals, making obsolete the Foster-Seely and ratio detectors of the early years. Other application for PLL circuits include AM demodulators, FSK decoders, two-tone decoders and motor speed controls.





4) Calculate the value of  $P_{IN}$  for the following system



$$\text{Loss in link} = 25 \times -2.1 = -52.5 \text{ dB}$$
$$\text{Overall Gain/Loss}$$

$$= 20 - 52.5 + 30$$
$$= -2.5 \text{ dB}$$

$$G_{dB} = -2.5 = 10 \log_{10} \left( \frac{S}{P_{IN}} \right)$$

$$-0.25 = \log_{10} \left( \frac{S}{P_{IN}} \right)$$

$$10^{-0.25} = \frac{S}{P_{IN}}$$

$$P_{IN} = \frac{S}{10^{-0.25}}$$

$$= \frac{5}{0.562} = 8.97 \text{ mW}$$