

Group 47
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g47_5_26_Decoder

Description:

Our decoder circuit takes a 5-bit binary number input (INDEX), representing a number from 0 to 31, and decodes it to assert one of 26 output lines (D). The circuit produces an error signal if the input is greater than 25 (ERROR).

Input:

INDEX signal (5 bits) - binary number to be decoded

Output:

D signal (26 bits) - separate (decoded) output lines

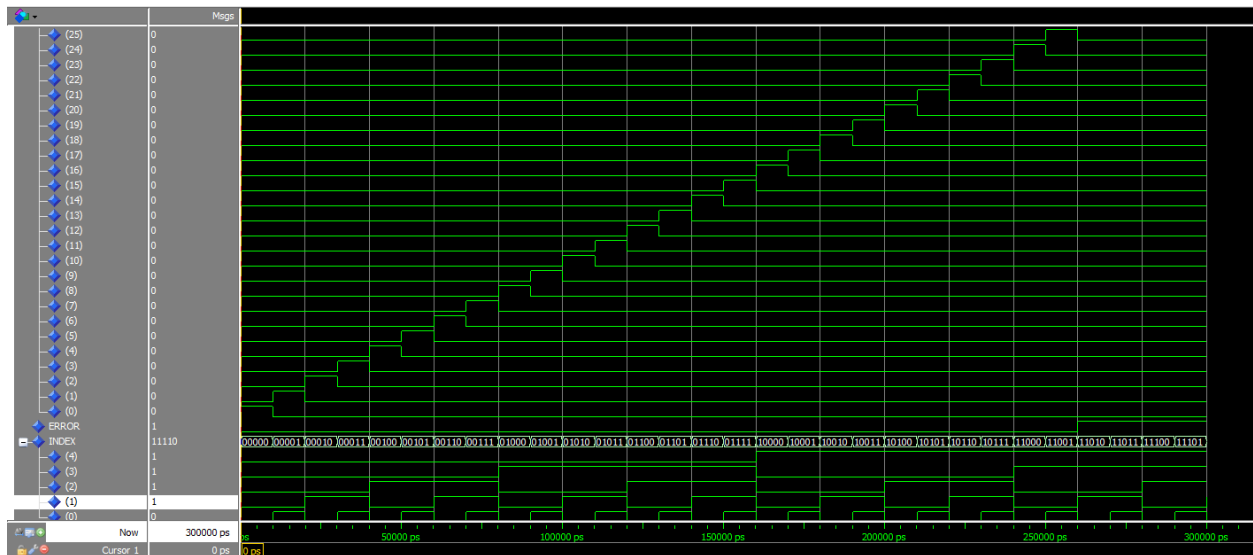
ERROR signal (1 bit) - Error signal (for numbers outside of range)

Diagram:

Schematic:

Testing:

In order to test our circuit, we used exhaustive testing by observing the output of every valid input. We know the circuit works correctly because, according to the image below, every input provides the desired output.



A - Boolean Equations:

$$e = AB \cdot (C + D)$$

$$P_{16} = A\bar{B}\bar{C}\bar{D}\bar{E}$$

$$D_{11} = A B \bar{C} \bar{D} E$$

$$D_{14} = A B C D E$$

$$D_{19} = A \text{ } \cancel{B} \text{ } CDE$$

$$D_{20} = A \bar{B} C \bar{D} \bar{E}$$

$$D_{21} = A \bar{B} C \bar{D} E$$

$$D_{21} = A \bar{B} C D E$$

$$D_{23} = ABCE$$

$$D_{24} = ABC\bar{D}E$$

$$D_{25} = A \bar{B} \bar{C} \bar{D} E$$

$$D_1 = ABC \text{ DE}$$

$$D_{14} = \overline{A} B C D E$$

$$D_{15} = \bar{A}BCDE$$