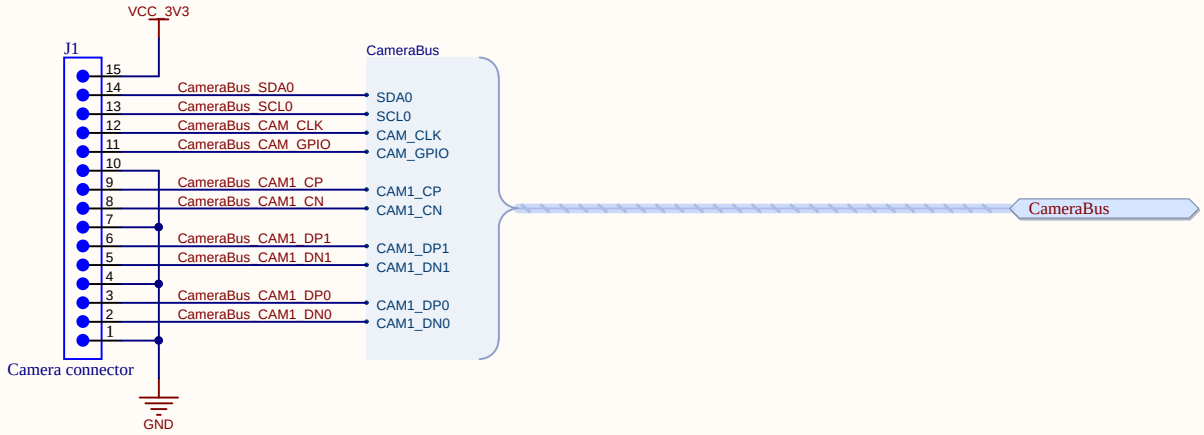
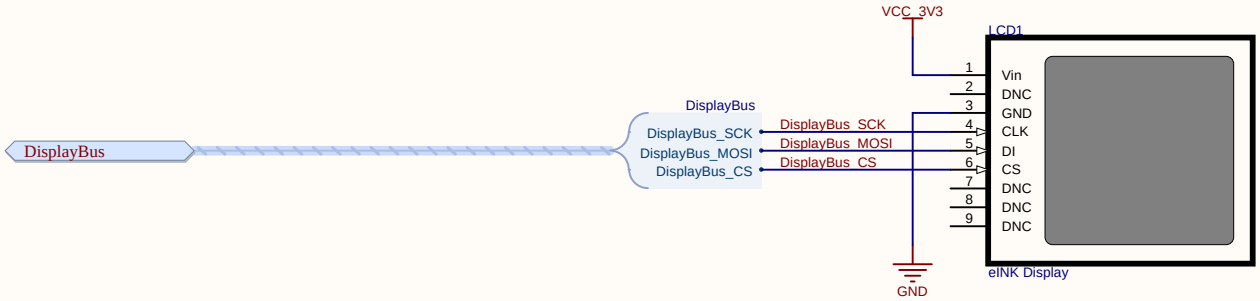
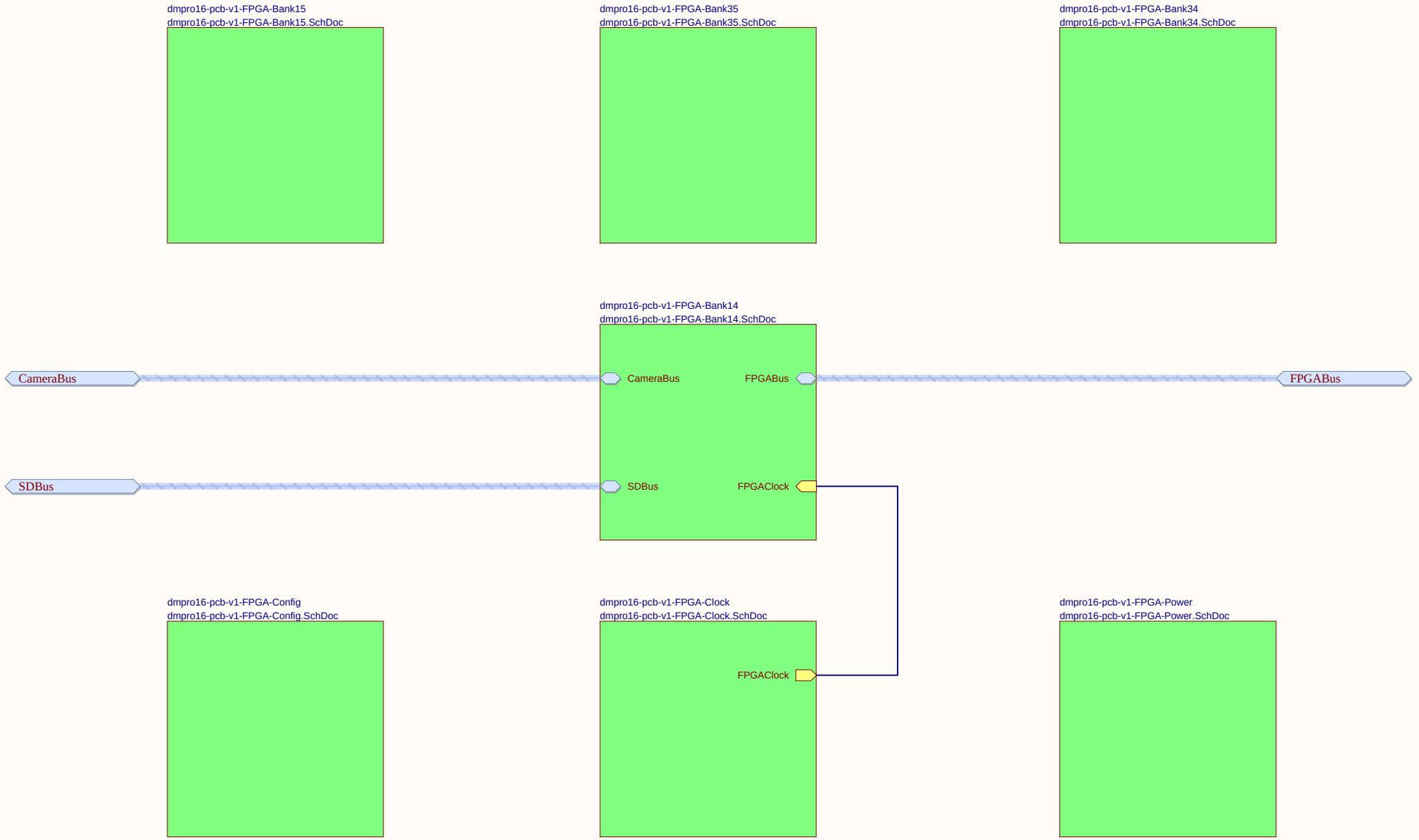


Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-CameraBoard.Sch			

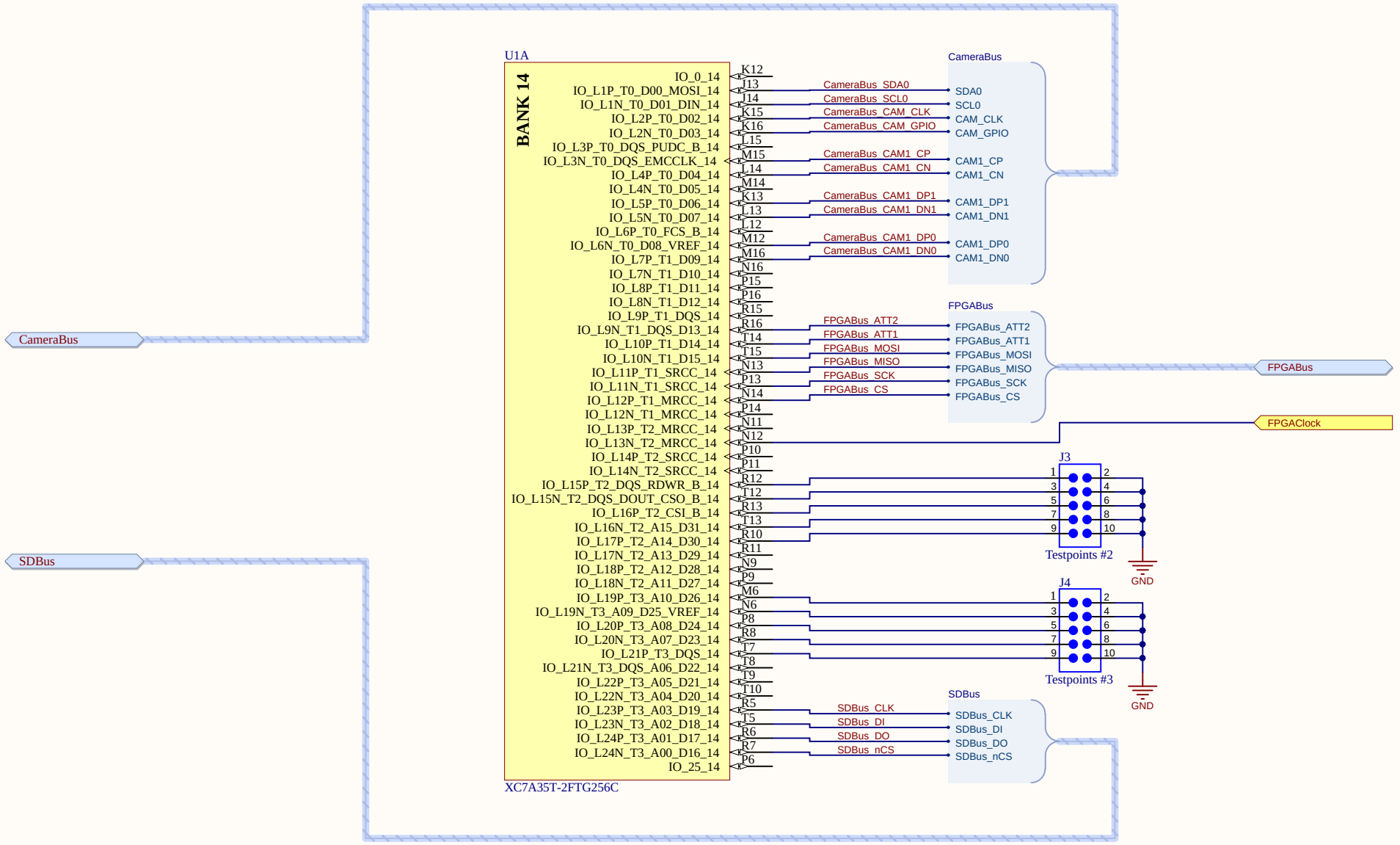


Title			
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-DisplayBoard.Sch			

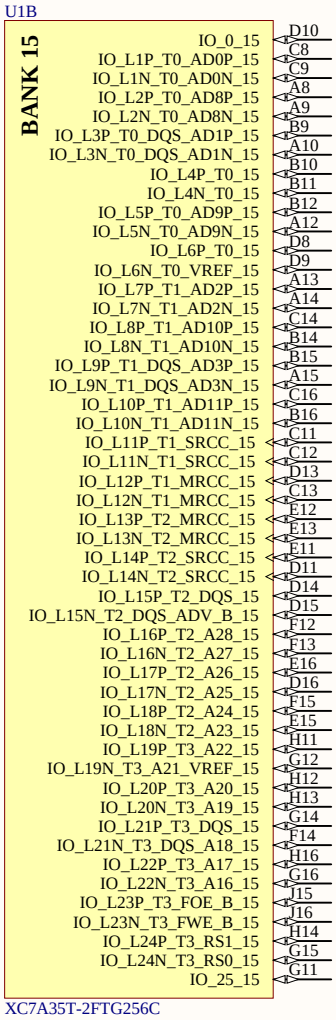
Altium Limited
L3, 12a Rodborough Rd
Frenchs Forest
NSW
Australia 2086



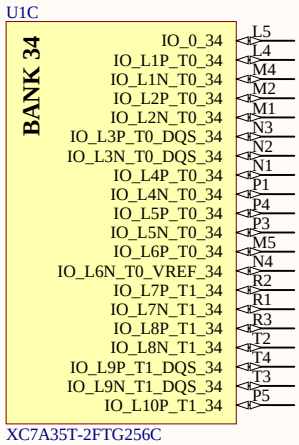
Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA.SchDoc			



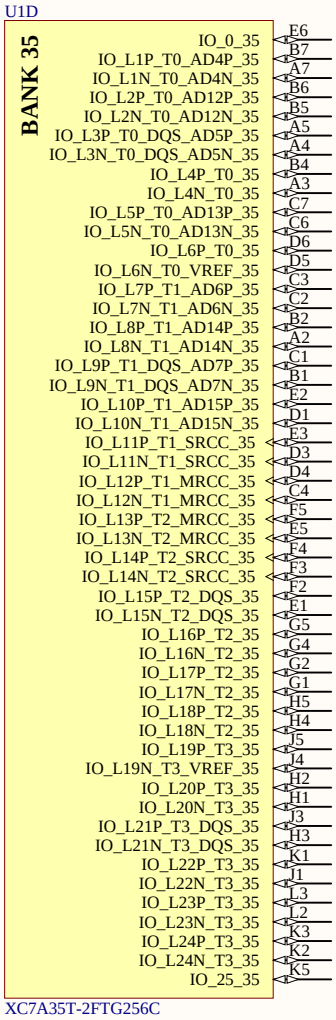
Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Bank14.Sc			



Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Bank15.Sc			



Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Bank34.Sc			

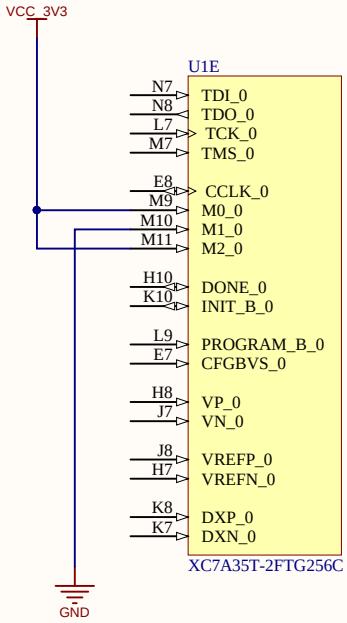


Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:16	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Bank35.Sc			

Add a crystal oscillator here, to be the logic clock for FPGA operations.

FPGAClock

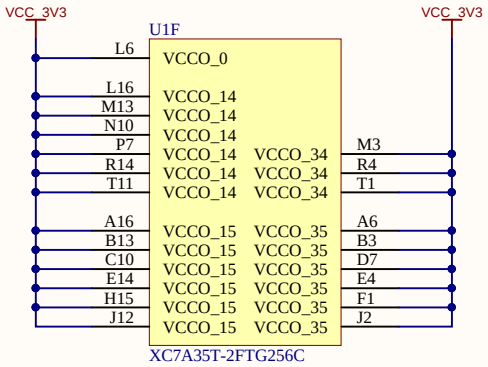
Title		
Size: A4	Number	Revision
Date: 25.09.2016		Sheet of
File: C:\Users\..\dmpro16-pcb-v1-FPGA-Clock.SchDoc		Drawn By:

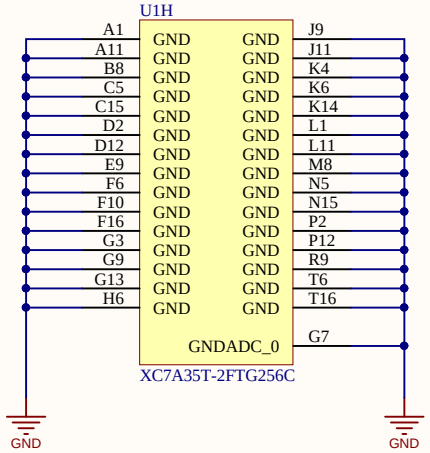


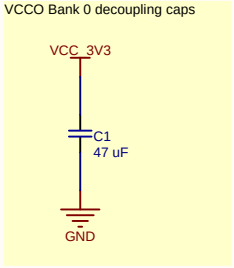
How to hook up programming interface etc.???

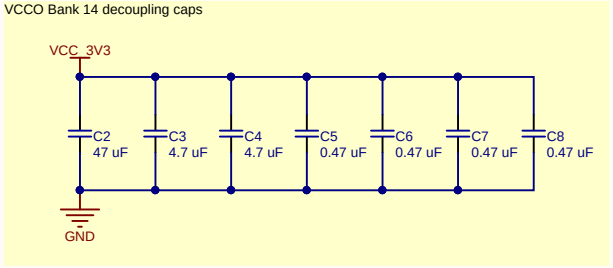
Title			
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Config.Sc			

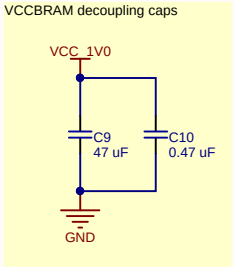
Altium Limited
L3, 12a Rodborough Rd
Frenchs Forest
NSW
Australia 2086

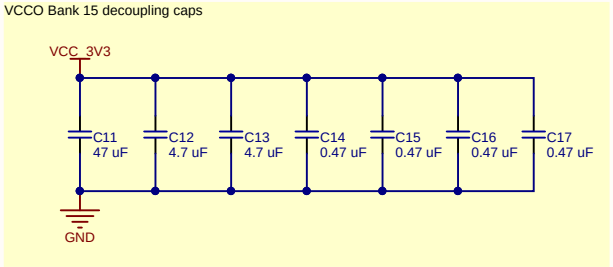


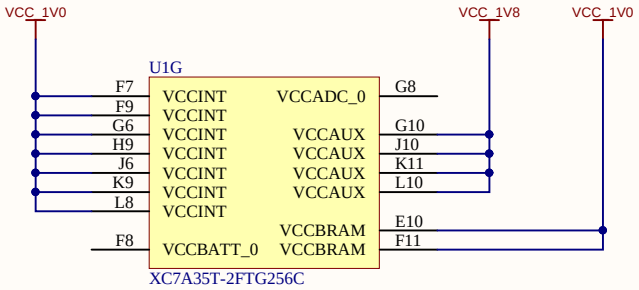




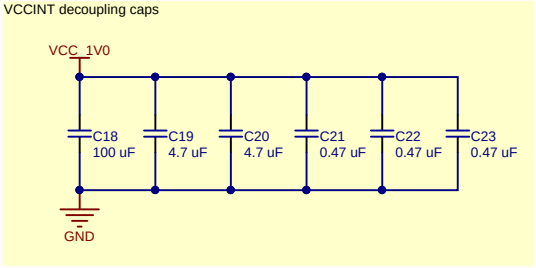


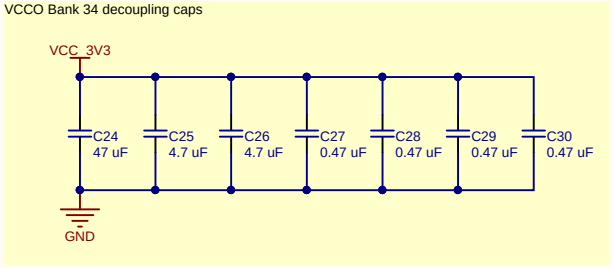


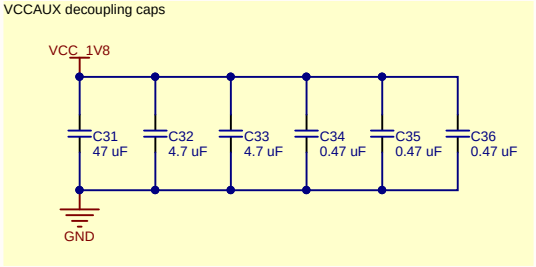


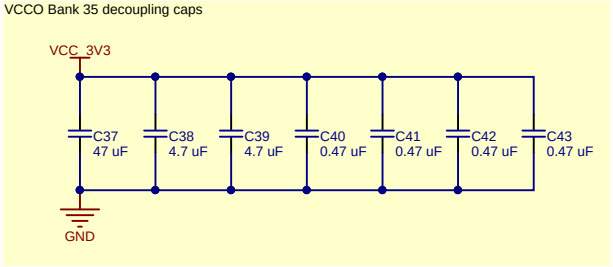


Do VCCADC and VCCBATT have to be connected to anything? If so, what can they be connected to?

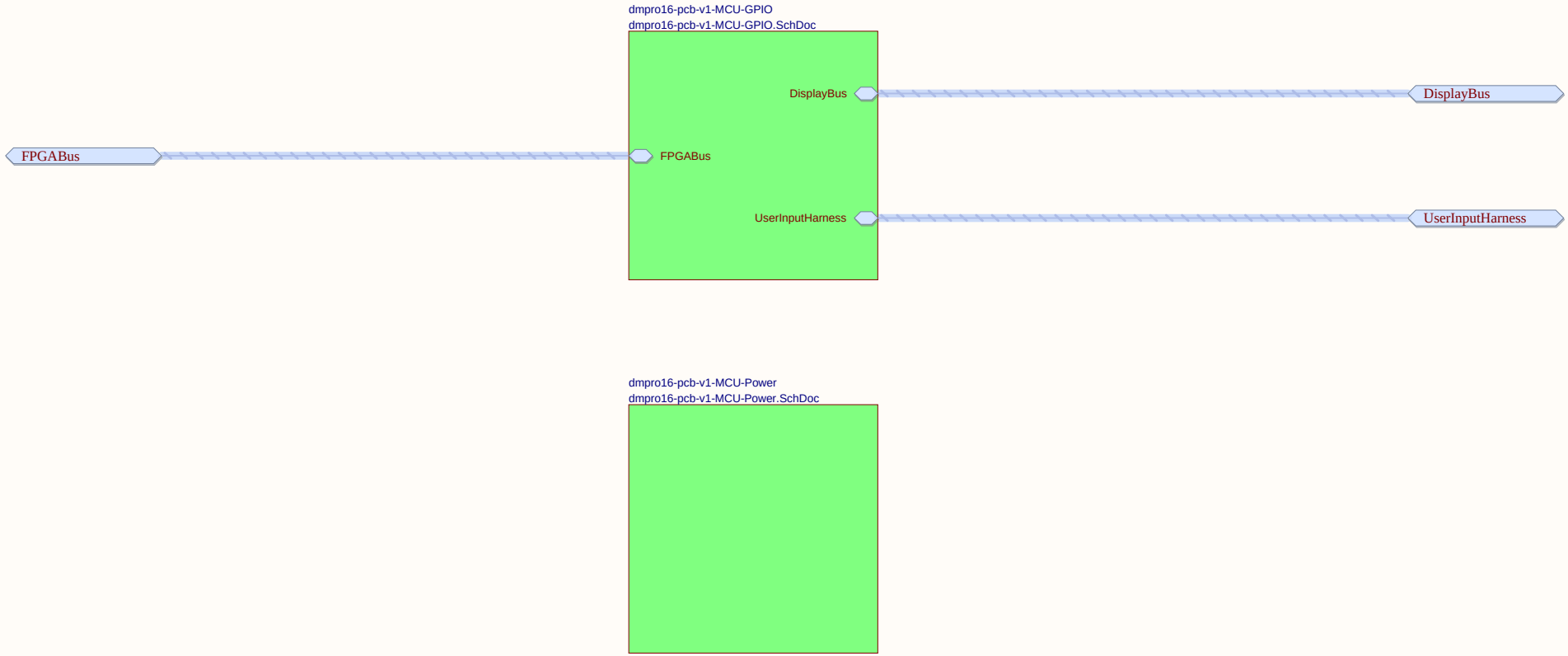




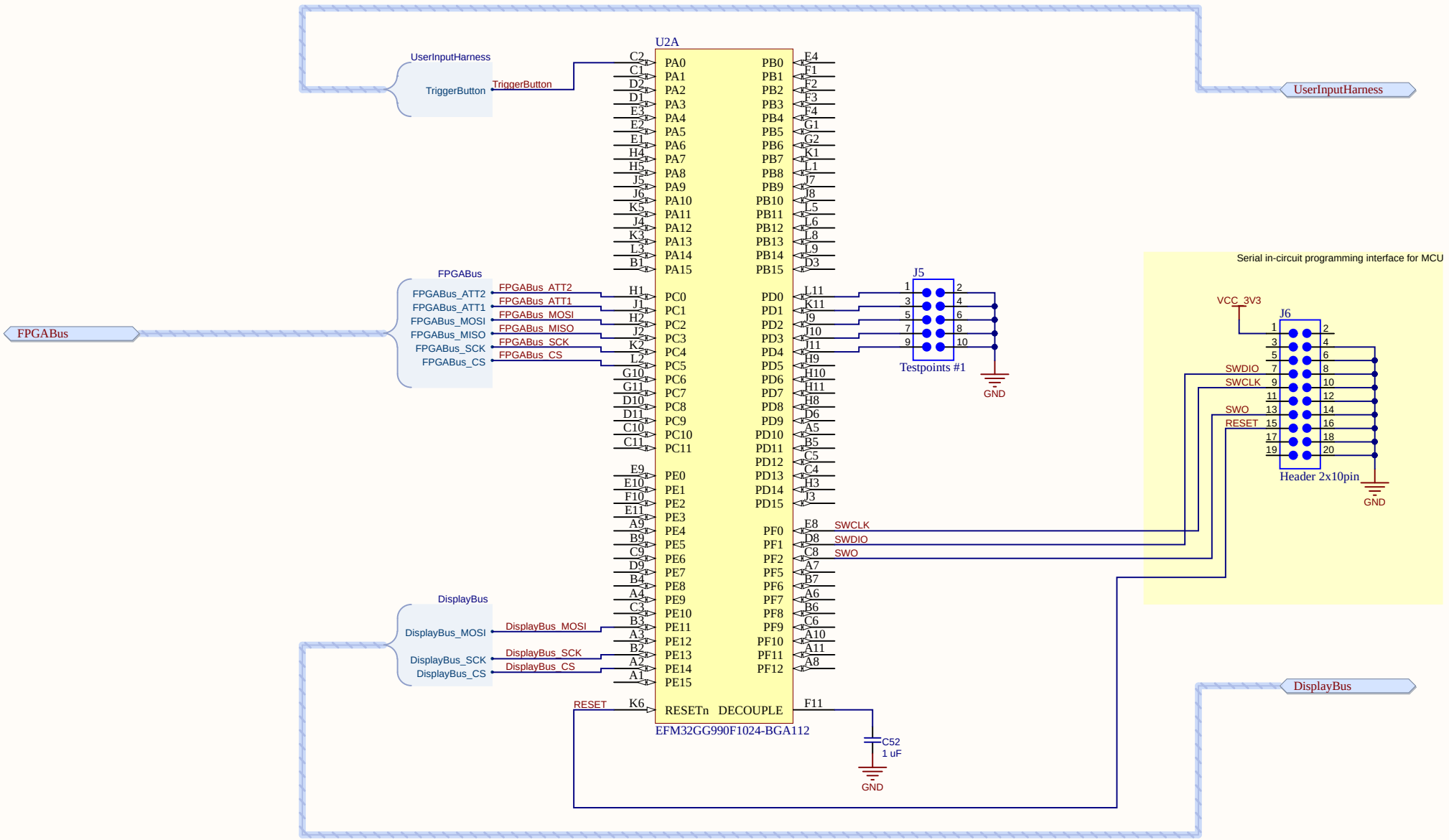




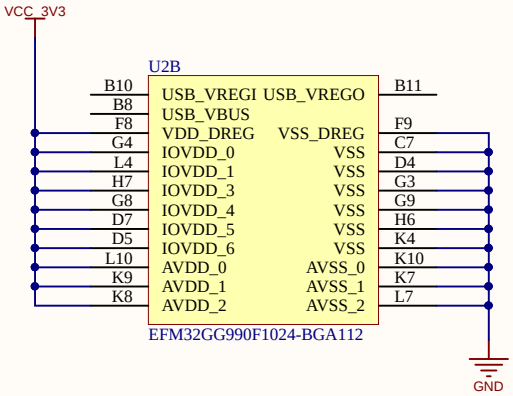
Title			Altium Limited L3, 12a Rodborough Rd Frenchs Forest NSW Australia 2086
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-FPGA-Power.Sch			

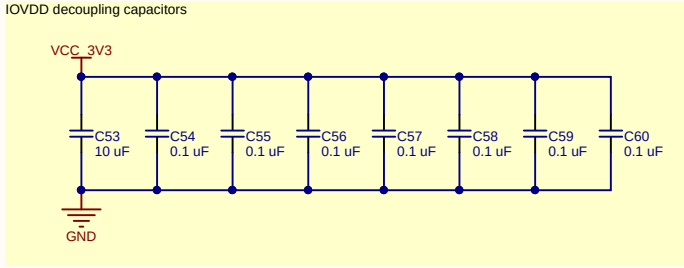


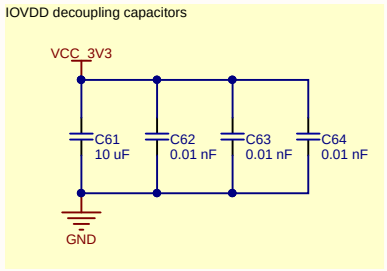
Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-MCU.SchDoc			

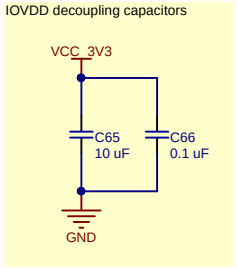


Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-MCU-GPIO.SchI			



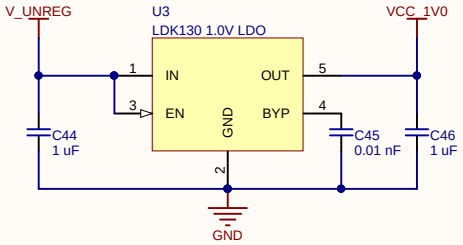


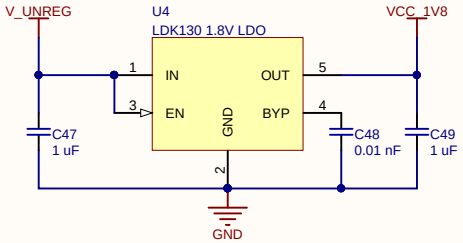


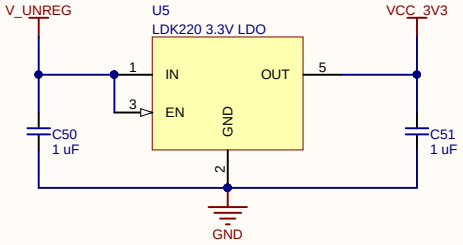


Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-MCU-Power.Sch			

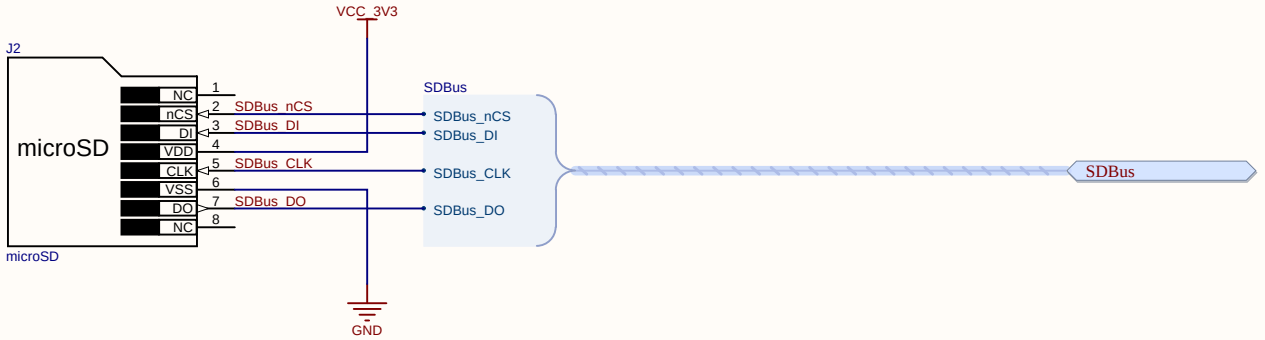
Need some way of connecting a battery to the voltage regulators. Will 4 AAA cells suffice?



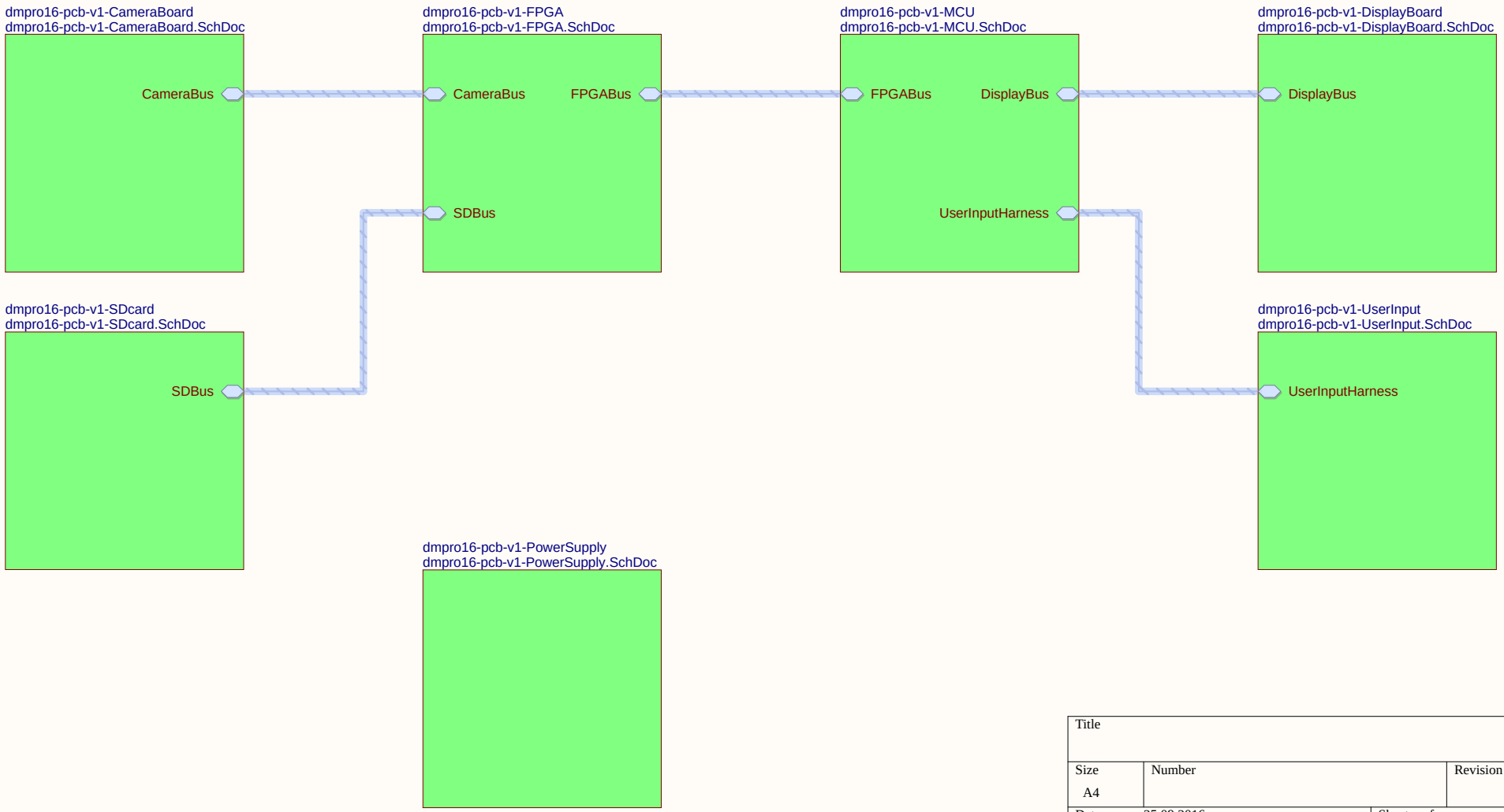




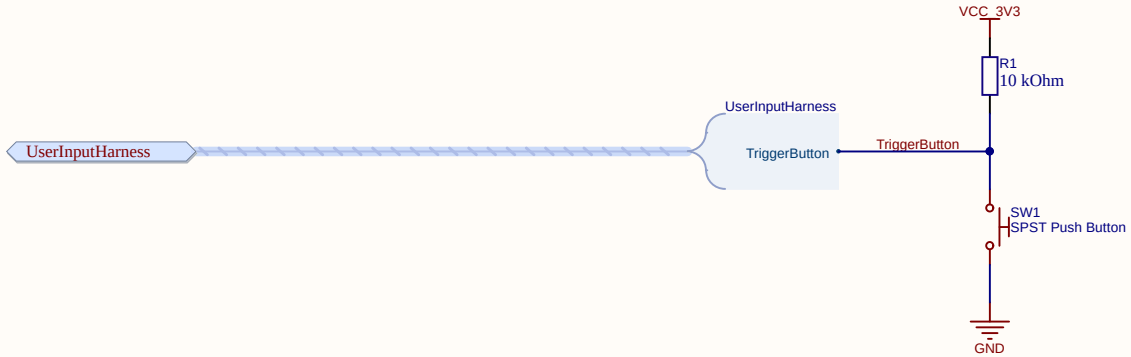
Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-PowerSupply.Sch			



Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:17	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-SDcard.SchDoc			



Title		
Size: A4	Number	Revision
Date: 25.09.2016	Sheet of	
File: C:\Users\..\dmpro16-pcb-v1-TopLevel.SchDoc	Drawn By:	



Trigger Button: Triggers the MCU to wake up from sleep and start a new aqcuisition.

Title			*Altium Limited* *L3, 12a Rodborough Rd* *Frenchs Forest* *NSW* *Australia 2086*
Size: A4	Number:	Revision:	
Date: 25.09.2016	Time: 21:43:18	Sheet of	
File: C:\Users\magnehh\Documents\GitHub\dmpro16\documents\deliveries\pcb\dmpro16-pcb-v1\dmpro16-pcb-v1-UserInput.SchDo			