

# Viswa Marepalli

+44 7483 867564 | [viswa.marepalli25@imperial.ac.uk](mailto:viswa.marepalli25@imperial.ac.uk) | [github.com/justanotherbyte](https://github.com/justanotherbyte) | [linkedin.com/in/mviswa](https://linkedin.com/in/mviswa) | [viswa.space](https://viswa.space)

## EDUCATION

---

### Imperial College London

MEng, Electronic and Information Engineering

London, UK

Sept 2025 — Present

### Dartford Grammar School

International Baccalaureate

Dartford, UK

Sept 2023 — May 2025

- Achieved 40/45 points, with 7/7/7 at Higher Level.
- Robotics Society lead: piloted two teams to construct rovers using the Raspberry Pi 3B.

## EXPERIENCE

---

### Software Engineering Intern

Jul 2025 — Jul 2025

Autumn (YC S25)

London, UK

- Rewrote Python SDK to adhere to PEP 8 and other industry conventions. Introduced AsyncIO support and wrote a server handler mountable to any ASGI app; fully compatible with the JavaScript SDK.
- Wrote Autumn's new CLI tool, allowing users to onboard onto Autumn by creating/editing pricing plans all from their code-base and their command line using a typed TypeScript configuration.
- Built usage limits, allowing users to set limits on how much a product can be used, e.g: \$0.05 per chatbot message **up to 1000 chat messages**. Implementation required adjustment of server routes, updates to React components and universally accepted DX.
- Collaborated with two founders and one other intern on initial ideas for scaling and CLI DX.
- Continuing maintenance and support and assisting with a migration of the Python SDK's core API client to [Stainless](#).

## PROJECTS

---

### Maintainer, Rogue Firmware

Sept 2025 — Present

- Built a hobby firmware in Rust for the Raspberry Pi 4B's ARM Cortex A72 quad-core BCM2711 SoC.
- Implemented GPIO and UART operations using Memory-Mapped IO operations, taking advantage of Rust's safety features to prevent invalid states and race conditions.
- Wrote a linker script by adapting existing scripts written for projects written in C. This involved adjusting start addresses, referring to the peripherals datasheet.

### Maintainer, autumn-py ([github.com/useautumn/autumn-py](https://github.com/useautumn/autumn-py))

Jun 2025 — Present

- Rewrote Autumn's (YC S25) Python SDK, equipping it with asynchronous support via aiohttp.
- Redesigned backend handler to be framework agnostic. Moved away from framework specific implementations to a universal ASGI implementation, allowing the server handler to be mounted to any ASGI app, powered by Starlette's ASGI toolkit.
- Documented usage both on Autumn's Mintlify and Sphinx docs.

### Maintainer, imoog ([github.com/justanotherbyte/imoog](https://github.com/justanotherbyte/imoog))

Nov 2021 — Apr 2022

- Built a content delivery node in Python using Starlette, storing files via pluggable database backends, with out-of-the-box support for PostgreSQL and MongoDB.
- Supports pluggable caching backends, with default support for in-memory and Redis caches.
- Rewrote in Rust using [tokio](#)'s eco-system and axum to experiment with performance improvements.

### IRIS Big Data: ATLAS

October 2023 — Jan 2024

- Researched the  $Z^0$  boson and led a team of five, writing a research poster titled: ***Finding the invariant mass of the  $Z^0$  boson via decay product summation.***
- Presented our findings at a research conference in London.

## SKILLS

---

- **Programming Languages:** Python, Rust, TypeScript, C/C++, HTML/CSS
- **Technologies:** React, Tailwind CSS, Git, Ubuntu Linux, Docker, Django, FastAPI/Starlette, GitHub Actions