**Digital Logic Simulator 2min Pre-Questionnaire**

Please answer the following questions to the best of your ability. Answers to these questions will be used to help design a new online Digital Logic Simulator for this course. Answers to these questions, as well as any information gathered from this questionnaire, will not be used for any other purpose other than that stated above, and will not be shared with any third parties.

All questions, including this questionnaire, are voluntary. Please don’t feel any pressure should you choose not to participate or answer any question. Thank you for your time and your help, it is much appreciated.

1. Name:

2. Year in college:

|  |  |  |  |
| --- | --- | --- | --- |
| Freshmen | Sophomore | Junior | Senior |

3. Average time spent on lab assignments:

|  |  |  |  |
| --- | --- | --- | --- |
| less than 1 hour | 2 to 3 hours | 3 to 4 hours | more than 5 hours |

4. Familiarity of sequential logic at the start of this semester:

|  |  |  |  |
| --- | --- | --- | --- |
| very familiar | somewhat familiar | not very familiar | not at all familiar |

5. Familiarity of truth tables at the start of this semester:

|  |  |  |  |
| --- | --- | --- | --- |
| very familiar | somewhat familiar | not very familiar | not at all familiar |

6. Had you previously used a digital logic simulator (such as Xilinx) before the start of this semester?

|  |  |  |  |
| --- | --- | --- | --- |
| yes | no |  |  |