# 组会报告

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# 1 工作内容

- 1. 实现基于 C 的 avx2-OMS 译码测试平台;
- 2. 提高 avx2-OMS 吞吐量。

## 2 提高 avx2-OMS 吞吐量的具体方法

### 2.1 改变数据结构使内存空间连续

原数据结构:

```
1 typedef struct check_node
2 {
3
          int8_t degree;
                                  // number of connective variable nodes
          int16_t *index;
                                  // index of connective variable nodes
4
          float* message;
5
                                  // message from cn to vn
          int8_t* message_fixed; // fixed message from cn to vn
6
          __m256i* message_avx2; // avx2 message from cn to vn
          int8_t pointer;
                                  // pointer to the message that will be used
9 } check_node;
```

#### 现数据结构:

```
1 typedef struct nr15_ldpc_simd_t
2 {
3
4
           int8_t* degree; // number of connective check nodes(length:M)
5
           int16_t* index; // index of connective check nodes(length:M_whole)
           __m256i* cn_msg_avx2; // avx2 message from cn to vn(length:M_whole)
6
                                  // avx2 llr(length:N)
7
           __m256i* llr_avx2;
8
           __m256i** llr_addr;
                                  // llr address from cn to vn(length:M_whole)
9
           __m256i* vn_msg_avx2; // temp avx2 message from vn to cn(length:19)
10 } nr15_ldpc_simd_t;
```

## 2.2 使用更小的校验矩阵

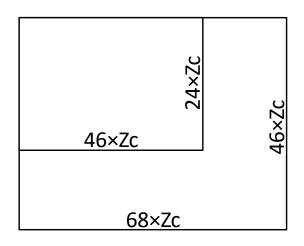


图 1: 优化前后校验矩阵尺寸变化

## 2.3 编译方式的优化

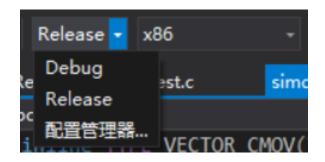


图 2: 项目版本的选择

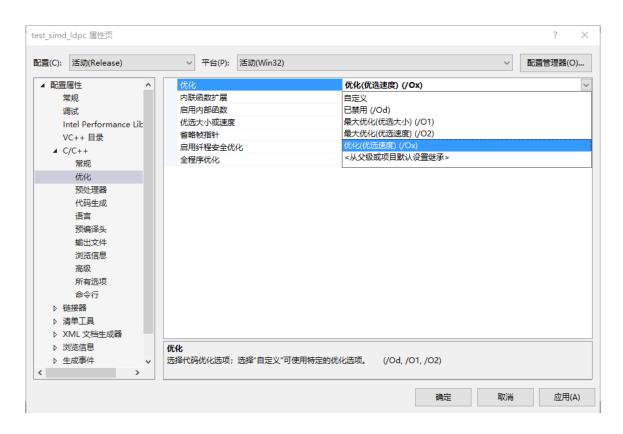


图 3: 优化方式的选择

## 2.4 限幅部分的优化

```
1
   for (r = 0; r < C; r++)
2
           for (n = 0; n < Nd / 8; n++)
3
                    resf = _mm256_mul_ps(*p_tabI, fact);
4
5
                    resf = _mm256_max_ps(resf, vminf);
6
                    resf = _mm256_min_ps(resf, vmaxf);
7
                    resi = _mm256_cvttps_epi32(resf);
8
                    p_tabI += 1;
9
                    for (i = 0; i < 8; i++)</pre>
                             ptr_llr[32 * (8 * n + i) + r] = (int8_t)p_resi[i];
10
11
           }
```

#### 2.5 判决部分的优化

```
1 uchar_itranspose_avx(h->llr_avx2, (__m256i*)decoded_bits, K);
```

# 3 VTune 测试结果分析

### 3.1 吞吐量计算

Function	Module	CPU Time ®
nr15_fec_ldpc_simd_decoder_avx2	test_simd_ldpc.exe	44.101s
main	test_simd_ldpc.exe	25.251s
vslsRngGaussian	mkl_vml_avx2.dll	20.966s
nr15_fec_ldpc_simd_rdm_dec_decbs	test_simd_ldpc.exe	16.834s
nr15_fec_ldpc_simd_cbs_enc_rm	test_simd_ldpc.exe	16.250s
[Others]		43.388s

图 4: Top Hotspots in VTune

```
B = (8448-24)*32*10^5 = 2.6957Gbit t = 44.101s Throughput = B/t = 2.6957Gbit/44.101s = 61.125Mbps
```

# 3.2 主要耗时部分

#### 3.2.1 循环 1

for (i = 0; i < *p_degreel; i++)	0.4%	0.0%	0.0%	0.586s
{				
<pre>vllr = VECTOR_LOAD(*p_indice_nod1);</pre>	0.4%	0.0%	0.0%	0.742s
<pre>vcn_msg = VECTOR_LOAD(p_msglr);</pre>				
<pre>vvn_msg = VECTOR_SUB_AND_SATURATE_VAR_8bits(vllr, vcn_msg,</pre>				
csign = VECTOR_AND(vvn_msg, msign8);				
sign = VECTOR_XOR(sign, csign);				
<pre>vabs = VECTOR_MIN(VECTOR_ABS(vvn_msg), vmax_msg);</pre>				
<pre>vn_message_avx2[i] = vvn_msg;</pre>				
<pre>vtemp = min_llr;</pre>				
min_llr = VECTOR_MIN_1(vabs, min_llr);				
<pre>submin_llr = VECTOR_MIN_2(vabs, vtemp, submin_llr);</pre>				
p_msglr += 1;				
<pre>p_indice_nodl += 1;</pre>	11.6%	0.0%	0.0%	19.363s
}				
p_degreel += 1;	0.0%	0.0%	0.0%	0.041s

图 5: 循环 1 耗时情况

#### 3.2.2 循环 2

for (i = 0; i < *p_degree2; i++)	0.2%	0.0%	0.0%	0.258s
{				
<pre>vvn_msg = vn_message_avx2[i];</pre>	0.5%	0.0%	0.0%	0.834s
<pre>vabs = VECTOR_MIN(VECTOR_ABS(vvn_msg), vmax_msg);</pre>	0.9%	0.0%	0.0%	1.433s
<pre>vres = VECTOR_CMOV(vabs, min_llr, osubmin_llr, omin_llr);</pre>	1.6%	0.0%	0.0%	2.611s
<pre>//z = VECTOR_EQUAL(vabs, min_llr);</pre>				
//vres = _mm256_blendv_epi8(omin_llr, osubmin_llr, z);				
<pre>vsig = VECTOR_XOR(sign, VECTOR_AND(vvn_msg, msign8));</pre>	0.8%	0.0%	0.0%	1.282
<pre>v2cn = VECTOR_invSIGN2(vres, vsig);</pre>	0.3%	0.0%	0.0%	0.552
v2llr = VECTOR_ADD_AND_SATURATE_VAR_8bits(vvn_msg, v2cn, v				
VECTOR_STORE(p_msglw, v2cn);	0.6%	0.0%	0.0%	0.923
VECTOR_STORE(*p_indice_nod2, v211r);	1.8%	0.0%	0.0%	2.923
p_msglw += 1;	1.2%	0.0%	0.0%	2.027
p_indice_nod2 += 1;	1.1%	0.0%	0.0%	1.894
}				
p_degree2 += 1;	0.2%	0.0%	0.0%	0.398s

图 6: 循环 2 耗时情况

#### 3.2.3 限幅部分

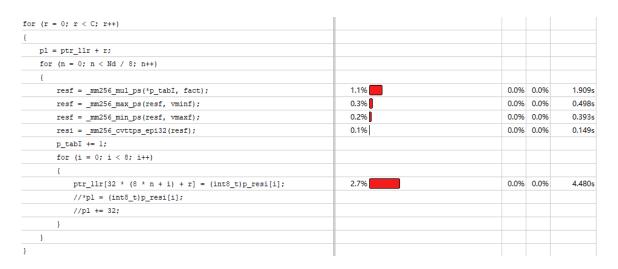


图 7: 限幅部分耗时情况