组会报告

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2018年9月2日

1 工作内容

- 1. 使用 avx2 指令实现限幅部分;
- 2. 完成代码基于 linux 平台的调试;
- 3. 在服务器上对译码模块进行性能测试。

2 使用 avx2 指令实现限幅部分

2.1 使用 packs 相关指令

原模块:

```
for (r = 0; r < C; r++)
2
           for (n = 0; n < Nd / 8; n++)
3
                    resf = _mm256_mul_ps(*p_tabI, fact);
4
5
                    resf = _mm256_max_ps(resf, vminf);
6
                    resf = _mm256_min_ps(resf, vmaxf);
7
                    resi = _mm256_cvttps_epi32(resf);
                    p_tabI += 1;
9
                    for (i = 0; i < 8; i++)</pre>
10
                             ptr_llr[32 * (8 * n + i) + r] = (int8_t)p_resi[i];
           }
11
```

现模块:

```
1 for (n = 0; n < Nd; n++)
3
           for (i = 0; i < 4; i++)</pre>
4
5
                    vllrf = _mm256_load_ps((float *)p_tabI);
                    resf = _mm256_mul_ps(vllrf, fact);
6
                    resf = _mm256_max_ps(resf, vminf);
 7
                    resf = _mm256_min_ps(resf, vmaxf);
8
9
                    resi[i] = _mm256_cvttps_epi32(resf);
10
                    p_tabI += 1;
11
12
           vtemp16[0] = _mm256_packs_epi32(resi[0], resi[1]);
           vtemp16[1] = _mm256_packs_epi32(resi[2], resi[3]);
13
14
           vtemp8 = _mm256_packs_epi16(vtemp16[0], vtemp16[1]);
```

2.2 遇到的问题

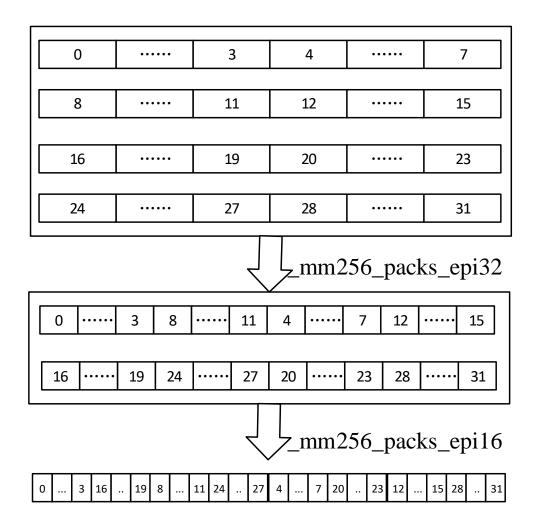


图 1: packs 相关指令的过程

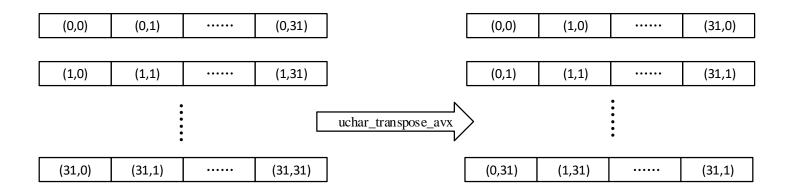


图 2: uchar_transpose_avx 函数的过程

3 代码基于 linux 平台的调试

3.1 遇到的问题

```
● ■ sherlockhsu@lab: ~/Github/test_5g_simd_ldpc
gcc -o main simd_ldpc.o test.o -lm -lpthread -lmkl_rt -fopenmp -I.

段错误 (核心已转储)
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ ./main

段错误 (核心已转储)
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make
make: 'main' is up to date.
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make clean
rm -f main simd_ldpc.o test.o
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make
gcc -Wall -03 -march=core-avx2 -c -o simd_ldpc.o simd_ldpc.c
simd_ldpc.c: In function 'nr15_fec_ldpc_simd_decoder_avx2':
simd_ldpc.c:637:9: warning: variable 'alpha_fixed' set but not used [-Wunused-bu t-set-variable]
int8_t alpha_fixed, beta_fixed;

simd_ldpc.c: In function 'nr15_ldpc_simd_matrix_init':
simd_ldpc.c:207:4: warning: ignoring return value of 'fscanf', declared with att ribute warn_unused_result [-Wunused-result]
    fscanf(fbg, "%hd", &h->H_BG[i][j]);

cc -Wall -03 -march=core-avx2 -c -o test.o test.c
gcc -o main simd_ldpc.o test.o -lm -lpthread -lmkl_rt -fopenmp -I.
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ ./main tell?
processor one named Skylate
expected the simple of the short of the shor
```

图 3: 段错误

错误原因:

使用 malloc 函数分配空间时,未对齐寄存器变量地址。

解决方法:

使用 mm malloc 函数分配寄存器相关地址空间;

使用 _mm_free 释放相关地址空间。

4 Linux 平台上的性能测试

4.1 Linux 平台上的 VTune 测试方法

- 1. source /opt/intel/vtune_amplifier/amplxe-vars.sh
- 2. amplxe-cl -collect hotspots ./main
- 3. amplxe-cl -report hotspots r000hs

4.2 本地测试

	sherlockhsu@lab: ~/Github/te:	st_5g_simd_l	dpc	
Block79:	0.00000			
Block80:	0.000000			找到你相焊权的田户(Printest) 这田户
Block81:	0.000004			13.21Ph/05DETAP1/11) (DDARCOO) (U.H.)
Block82:	0.000000			
Block83:	0.00000			
Block84:	0.00000			
Block85:	0.00000			
Block86:	0.00000			令 useradd # 添加用户username # 参数m为
Block87:	0.000004			
Block88:	0.00000			
Block89:	0.00000			
Block90:	0.00000			权变为username, chown -R username /file
Block91:	0.00000			A Systematics chown it doctriance him
Block92:	0.00000			
Block93:	0.00000			
Block94:	0.00000			
Block95:	0.000004			义限后更改当削路径为根目录/root#sudo -s /
Block96:	0.000004			
Block97:	0.00000			
Block98:	0.000011			
Block99:	0.00000			写的权限) chmod 644 name (所有者有诗
_	e:0.403220s			
Throughp	out:66.85Mbps			
sherlock	khsu@lab:~/Github/test_5g_	simd_ldpc\$	amplxe-cl	-collect hotspots ./main

图 4: 本地运行结果

	or CPU Tim me:Spin Tim er Module	ne:Other CPU Time: Fun	k CPU Overhea ction (Time:Effective T nd Time CPU Time Full)	:Overhead Time:Creati Source Fi	on CPU Time:0	Overhead Time:Sched	in Time CPU Tim	me:Spin T e:Overhea	ime:Effective Time CP ime:Imbalance or Seria d Time:Reduction CPU	l Spinning CF Time:Overhead	PU Time:Spin Time:Lo Time:Atomics CPU 1	ock Co Time:0	ntention verhead Ti
0.4456580		simd_decoder_avx2												
13. fec_logic_stand_rade_decedes														
## 1		simd_rdm_dec_decbs												
New Note											nr15 fec ldpc			[Unknown]
0														
Note														
05										libmkl_intel_lp64.so	vsRngGaussiar			[Unknown]
0														
1														
05				0.128s	0.128s				0.128s					
0.4400400														
Os														[Unknown]
Ox405cf0 Ox405cf0				0.116s	0.116s				0.116s					
RngBernoull(
OS	gBernoulli													
### BARESYSCALL Docal LAsm Intel 64 Linux ## 0.0605											viRnaBernoull			
S				0 0605	0.0605		0.0605							
## ## ## ## ## ## ## ## ## ## ## ## ##														
15_fec_ldpc_sind_encoder_scb											OS_BARESYSCAL	L_DoCallAsmIntel64L		[Unknown]
05 05 05 05 05 05 05 05														
d-linux-x86-64.so.2] 0.650s 0.650s 0.010s 0.040s 0s 0														
9S 9S 9S 9S 0S 1d-llnux-x86-64.so.2 [ld-llnux-x86-64.so.2] mcmp 9 0.050S 0.050S 0.020S 0.030S 9S 0S 9S 9S 9S 9S 0S 9S 0S									0.040s					
### ##################################														
' 0s														
8x1c0d0 erator new 0.048s 0.012s 0.036s 0s				0.050s	0.050s		0.020s		0.030s					
erator new 0.048S 0.048S 0.012S 0.036S 0S 0S 0S 0S 0S 0S 0S 0S 0S S 15pin3dwarf.so operator new(unsigned long) 0xbb3a0	Av													
											operator new(
				A 836c	0.0365									
				0.0305										memset.S

图 5: 本地 VTune 测试结果

4.3 服务器测试

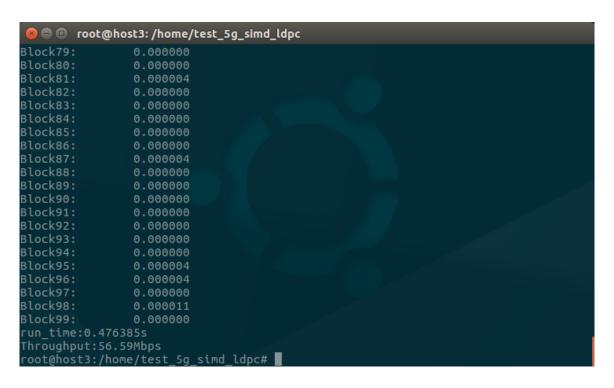


图 6: 服务器运行结果

图 7: 服务器 VTune 测试结果

5 仍存在的问题