组会报告

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1 工作内容

- 1. 修改并提交 LDPC 相关代码;
- 2. 完成仿真报告;
- 3. 选择新的数据采集方案并采集数据。

2 修改并提交 LDPC 相关代码

2.1 SIMD 部分

```
1 Change Log for Releases
  _____
3
4
  ## version 1.2
     * 修复了修复输入对数似然比反向的问题
5
6
  ## version 1.1
     * 修复了基于Base Graph 2的编码器异常问题
8
9
10 ## version 1.0
     * 实现了基于AVX2的5G LDPC编码器
11
12
     * 实现了基于AVX2的High-Throughput OMS及NMS译码器
     * 实现了基于AVX2的Low-Latency OMS及NMS译码器
13
     * 搭建了AWGN信道的5G LDPC编译码性能测试平台
14
```

2.2 MEX 部分

3 选择新的数据采集方案并采集数据

3.1 乒乓结构

图 1: 乒乓结构下的数据采集结果

图 2: 乒乓结构下的数据采集结果(服务器先开)

3.2 乒乓结构

| 问题 输出 调试控制台 终端 | and the second s | | | |
|---|--|----------|--|--|
| Port statistics ======= Statistics for port 0 | | <u>.</u> | | |
| Packets sent: Packets received: Packets dropped: | 0 107008025 0 | | | |
| Aggregate statistics === Total packets sent: | Θ | : | | |
| Total packets received: Total packets dropped: | 107008025 0 | _ | | |
| err pkg times = 90, err pkg num = 6940648, nvld pkg num=107008025 sent rate = 0.00Gbps, received rate = 7.02Gbps | | | | |

图 3: N=10 的 FIFO 结构下的数据采集结果

图 4: N=10 的 FIFO 结构下的数据采集结果(服务器先开)

| 问题 输出 调试控制台 终端 | | |
|--|-----------|--|
| Port statistics ======= Statistics for port 0 | | |
| Packets sent: | Θ | |
| Packets received: | 100683885 | |
| Packets dropped: | Θ | |
| Aggregate statistics ==== | | |
| Total packets sent: | Θ | |
| Total packets received: | 100683885 | |
| Total packets dropped: | 0 | |
| err pkg times = 20, err psent rate = 0.00Gbps, rec | | |

图 5: N=20 的 FIFO 结构下的数据采集结果

```
问题 输出 调试控制台 终端
Port statistics =========
Statistics for port 0 -----
Packets sent:
                                   Θ
Packets received:
                           209078878
Packets dropped:
                                   Θ
Aggregate statistics =====
Total packets sent:
                                   Θ
Total packets received:
                           209078878
Total packets dropped:
err pkg times = 0, err pkg num = 0, nvld pkg num=209078878
sent rate = 0.00Gbps, received rate = 7.35Gbps
```

图 6: N=20 的 FIFO 结构下的数据采集结果(服务器先开)

3.3 当前问题

```
≣ data.txt

▼ vld_data.bin

     F4 3/ 8/ ED DO 4F 00 00 00 00 00 00 00 00 NO CE 3C E3 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F FF FF FF F0 00 00 00 00 A0 CE 5C E6 10 00 00 80 00 A9 00 A9 00 00 00 00 00
AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 00 A0 CE 5C E7 10 00 00 80 00 A9 00 A9 00 00 00 00 00
AC 82 F4 51 97 EB 86 4F 00 00 00 00 00 00 00 00 A0 CE 5C E8 00 00 00 FF FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE
                                                 5C E9 00 00 00 00 FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C EA 00 00 00 FF FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C EB 00 00 00 FF FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C EC 00 00 00 FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C ED 00 00 00 FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C EE 00 00 00 00 FF FF
                                                                      FF FF
                                                                            FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C EF 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C F1 00 00 00 FF FF FF FF FF FF FF FF
                                                                                       FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F2 00 00 00 FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F3 00 00 00 FF FF FF FF FF FF
                                                                                 FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F4 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C F6 00 00 00 FF FF FF FF FF FF FF FF
                                                                                       FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F7 00 00 00 FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F8 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 FB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C F9 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F FF FF FF F0 00 00 00 00 A0 CE 5C FA 10 00 00 80 00 A9 00 A9 00 00 00 00 00
AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 A0 CE
                                                 5C FB
                                                      10 00 00 80 00 A9 00 A9 00 00 00 00
                                                                                       ΘΘ
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C FC 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB 86 4F 00 00 00 00 00 00 00 00 A0 CE 5C FD 00 00 00 FF FF FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5C FE 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 00 A0 CE 5C FF 00 00 00 FF FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB
                B6 4F 00 00
                           00 00
                                00 00 00 00 A0 CE
                                                 5D 00 00 00 00 00 FF FF
                                                                                       FF
                                                                      FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5D 01 00 00 00 FF FF FF FF FF FF FF FF
AC 82 F4 51 97 EB B6 4F 00 00 00 00 00 00 00 A0 CE 5D 02 00 00 00 FF FF FF FF FF FF FF FF
                                                                                       FF
                   45 00 00 00 00 00 00 00
                                                 50 03
                                                            AA
```

图 7: 全部数据

```
AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC 9D 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF 60 00 00 00 03 C5 BC 9E 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC AC 10 00 00 80 00 A9 00 A9 00 00 00 00 00
             97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC AD
                                               10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC B3 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF 00 00 00 00 03 C5 BC B4 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF 60 00 00 00 03 C5 BC C2 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC C3 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC C8 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF 60 00 00 00 03 C5 BC C9 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 FB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC D8 10 00 00 80 00 A9 00 A9 00 00 00 00 00
29994
     AC 82 F4 51 97 EB B6 4F FF FF FF 00 00 00 00 03 C5 BC D9 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC DE 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C5 BC DF
                                               10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF F0 00 00 00 00 03 C5 BC EE 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF 60 00 00 00 03 C5 BC EF 10 00 00 80 00 A9 00 A9 00 00 00 00 00
29999
     AC 82 F4 51 97 EB B6 4F FF FF FF 00 00 00 00 03 C5 BC F4 10 00 00 80 00 A9 00 A9 00 00 00 00 00
     AC 82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C6 D5 B6 10 00 00 80 00 A9 00 A9 00 00 00 00 00
       82 F4 51 97 EB B6 4F FF FF FF FF 00 00 00 00 03 C6 D5 C5 10 00 00 80 00 A9 00 A9 00 00 00 00
     AC 82 F4 51 97 FB B6 4F FF FF FF FF 00 00 00 00 03 C6 D5 CB 10 00 00 80 00 A9 00 A9 00 00 00 00 00
30004
     AC 82 F4 51 97 FB B6 4F FF FF FF FF 00 00 00 00 03 C6 D5 CC 10 00 00 80 00 A9 00 A9 00 00 00 00 00
```

图 8: 有效数据

```
sem_init(&sem_r, 0, 0);
sem_init(&sem_w, 0, SIZE_OF_BUFFER);
```

图 9: 初始化

图 10: 写

```
sem_wait(&sem_r);
while (!force_quit)
{
    fwrite(buffer[indx_write], sizeof(uint8_t), LEN_OF_BUFFER * INFO_PKG_LEN, fp);
    sem_post(&sem_w);
    indx_write++;
    if (indx_write >= SIZE_OF_BUFFER)
        indx_write = 0;
        sem_wait(&sem_r);
}
```

图 11: 读

4 完成仿真报告