# 组会报告

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# 1 本周工作内容

- 1. 尝试扩大 DPDK 的中 mbuf
- 2. 实现分块传输和流量控制问题
- 3. 处理 makefile 相关问题

# 2 尝试扩大 DPDK 的中 mbuf

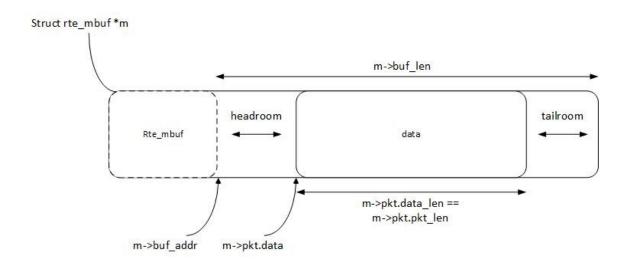


图 1: rtl\_mbuf 结构

图 2: 仅扩大 MBUF SIZE

```
🔊 🖃 📵 root@ubuntu: /home/xuyi/dataProcess
        probe driver: 8086:10fb net_ixgbe
        probe driver: 8086:10fb net ixgbe
EAL: PCI device 0000:45:00.0 on NUMA socket 1
       probe driver: 8086:10fb net_ixgbe
EAL: PCI device 0000:45:00.1 on NUMA socket 1
EAL: probe driver: 8086:10fb net_ixgbe
EAL: PCI device 0000:82:00.0 on NUMA socket 2
EAL: PCI device 0000:82:00.1 on NUMA socket 2
EAL: probe driver: 8086:10fb net_ixgbe
RTE_MBUF_DEFAULT_BUF_SIZE:2176
mempool init done
ring_send create done
ring_receive create done
number of Ethernet ports that are available:4
Initializing port 0... PMD: ixgbe_alloc_rx_queue_mbufs(): RX mbuf alloc failed q
ueue_id=0
PMD: ixgbe_dev_rx_queue_start(): Could not alloc mbuf for queue:0
PMD: ixgbe_dev_start(): Unable to start rxtx queues
PMD: ixgbe_dev_start(): failure in ixgbe_dev_start(): -1
EAL: Error - exiting with code: 1
  Cause: rte eth dev start:err=-5, port=0
root@ubuntu:/home/xuyi/dataProcess# 📗
```

图 3: 扩大 MBUF SIZE 的同时缩小 NB MBUF

#### 3 分段传输方案

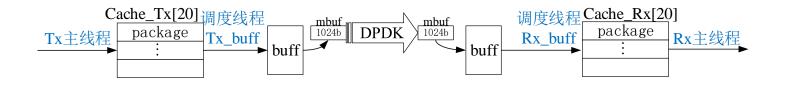


图 4: 分段传输方案系统结构

图 5: 未进行流量控制

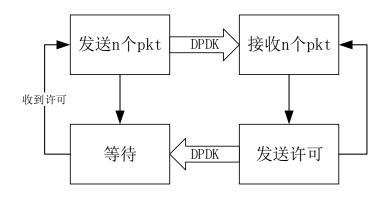


图 6: 流量控制方案

```
Port statistics =================================
                                  78471
78471
Packets received:
Packets dropped:
Aggregate statistics =========
Total packets sent:
Total packets received:
Total packets dropped:
                                  78471
root@ubuntu:/home/xuyi/traffic_control/dataProcess_send# \Box
Packets received:
                                  78471
Packets dropped:
Aggregate statistics ========
                                  78471
Total packets received:
Total packets dropped:
root@ubuntu:/home/xuyi/traffic_control/dataProcess_receive# 🗌
```

图 7: 进行流量控制后

### 4 makefile 相关问题

```
Makefile x

Makefi
```

图 8: DPDK 和编码调制系统 makefile 对比

图 9: 尝试合并的 makefile

```
root@ubuntu: /home/xuyi/dataProcess_send
/home/xuyi/data<code>Process_send/ChannelEstimator.c:413:</code> undefined reference to <code>`Dfti</code>
ComputeBackward'
/home/xuyi/dataProcess_send/ChannelEstimator.c:414: undefined reference to `Dfti
ComputeBackward'
/home/xuyi/data{\sf Process} {\sf send/ChannelEstimator.c:415:} {\sf undefined} {\sf reference} {\sf to} {\sf `Dfti}
FreeDescriptor'
ChannelEstimator LS.o: In function `Cal DCT FFT':
                                                                undefined reference to
/home/xuyi/dataProcess_send/ChannelEstimator_LS.c:224:
,
ftiCreateDescriptor_s_1d'
/home/xuyi/dataProcess_send/ChannelEstimator_LS.c:225:
                                                                 undefined reference to
ftiCommitDescriptor'
/home/xuyi/dataProcess_send/ChannelEstimator_LS.c:246:
                                                                undefined reference to
ftiComputeForward'
/home/xuyi/dataProcess_send/ChannelEstimator_LS.c:247:
                                                                undefined reference to
ftiComputeForward'
/home/xuyi/dataProcess_send/ChannelEstimator_LS.c:256:
                                                                undefined reference to
ftiFreeDescriptor<sup>1</sup>
TaskScheduler.o: In function `TaskScheduler_tx':
/home/xuyi/dataProcess_send/TaskScheduler.c:579: undefined reference to `cblas_c
collect2: 错误: ld 返回 1
make[1]: *** [dataProcess] Error 1
make: *** [all] Error 2
oot@ubuntu:/home/xuyi/dataProcess_send# 🛮
```

图 10: 编译报错

#### 临时处理方案:

将编码调制部分编译生成的.o 文件复制到 DPDK 部分进行编译。

### 5 其他改进方向

- 1. 处理内存泄露问题。
- 2. 引入进程结束信号。
- 3. 加入时延分析模块。

# 6 仍存在问题

- 1. 结构体内指针所指的堆内容无法在不同文件访问。暂时方案: 各个线程写在同一个文件中。
- 2. 实验室 PC 无法添加超过 4 个线程。 暂时方案: 使用笔记本调试代码。

## 7 下周计划

- 1. 继续完成数据处理 +DPDK 系统
- 2. 学习 LDPC 相关内容