组会报告

徐益

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1 工作内容

- 1. 使用 avx2 指令实现限幅部分;
- 2. 完成代码基于 linux 平台的调试;
- 3. 在服务器上对译码模块进行性能测试。

2 使用 avx2 指令实现限幅部分

2.1 使用 packs 相关指令

原模块:

```
for (r = 0; r < C; r++)
2
           for (n = 0; n < Nd / 8; n++)
3
                    resf = _mm256_mul_ps(*p_tabI, fact);
4
5
                    resf = _mm256_max_ps(resf, vminf);
6
                    resf = _mm256_min_ps(resf, vmaxf);
7
                    resi = _mm256_cvttps_epi32(resf);
8
                    p_tabI += 1;
9
                    for (i = 0; i < 8; i++)</pre>
10
                             ptr_llr[32 * (8 * n + i) + r] = (int8_t)p_resi[i];
           }
11
```

现模块:

```
1 for (n = 0; n < Nd; n++)
2
3
           for (i = 0; i < 4; i++)</pre>
4
5
                    vllrf = _mm256_load_ps((float *)p_tabI);
                    resf = _mm256_mul_ps(vllrf, fact);
6
                    resf = _mm256_max_ps(resf, vminf);
 7
                    resf = _mm256_min_ps(resf, vmaxf);
8
9
                    resi[i] = _mm256_cvttps_epi32(resf);
10
                    p_tabI += 1;
11
12
           vtemp16[0] = _mm256_packs_epi32(resi[0], resi[1]);
           vtemp16[1] = _mm256_packs_epi32(resi[2], resi[3]);
13
14
           vtemp8 = _mm256_packs_epi16(vtemp16[0], vtemp16[1]);
```

2.2 遇到的问题

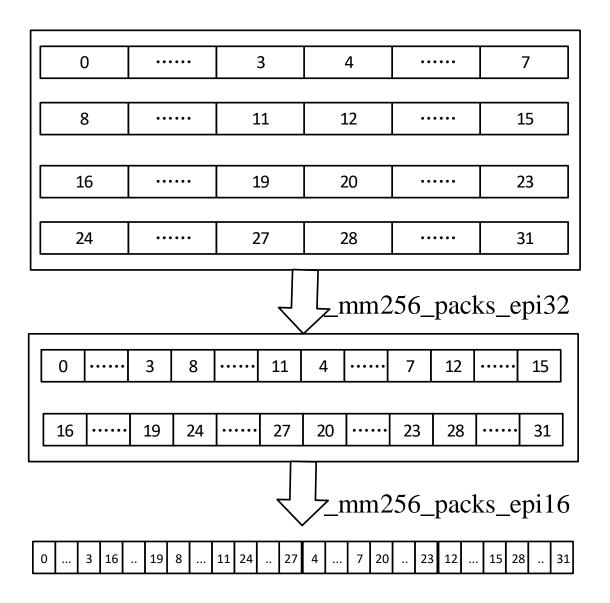


图 1: packs 相关指令的过程

(0,0)	(0,1)	•••••	(0,31)		(0,0)	(1,0)	••••	(31,0)
(1,0)	(1,1)	•••••	(1,31)		(0,1)	(1,1)	•••••	(31,1)
	:	<u> </u>	_				•	
				uchar_transpose_avx	>		•	
	·	,						
(31,0)	(31,1)	•••••	(31,31)		(0,31)	(1,31)	•••••	(31,1)

图 2: uchar_transpose_avx 函数的过程

3 代码基于 linux 平台的调试

3.1 遇到的问题

```
🔊 🖨 📵 sherlockhsu@lab: ~/Github/test_5g_simd_ldpc
qcc -o main simd ldpc.o test.o -lm -lpthread -lmkl rt -fopenmp ll.
sherlockhsu@lab:~/Github/test 5g simd ldpc$ \"/main
段错误 (核心已转储)
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make
make: 'main' is up to date.
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make clean
rm -f main simd_ldpc.o test.o
sherlockhsu@lab:~/Github/test_5g_simd_ldpc$ make
gcc -Wall -03 -march=core-avx2 -c -o simd_ldpc.o simd_ldpc.c
simd_ldpc.c: In function 'nr15_fec_ldpc_simd_decoder_avx2':
simd_ldpc.c:637:9: warning: variable 'alpha_fixed' set but not used [-Wunused-bu
t-set-variable]
  int8 t alpha fixed, beta fixed;
simd_ldpc.c: In function 'nr15_ldpc_simd_matrix_init':
simd_ldpc.c:207:4: warning: ignoring return value of 'fscanf', declared with att
ribute warn unused result [-Wunused-result]
    fscanf(fbg, "%hd", &h->H_BG[i][j]);
gcc -Wall -03 -march=core-avx2 Parchmeoetest.o testxc
gcc -o main simd_ldpc.o test.o -lm -lpthread -lmkl_rt -fopenmp -I.
sherlockhsu@lab:~/Github/test 5g simd ldpc$ ./main
段错误 (核心已转储)
sherlockhsu@lab:~/Github/test 5g simd ldpc5
```

图 3: 段错误

错误原因:

使用 malloc 函数分配空间时,未对齐寄存器变量地址。

解决方法:

使用 _mm_malloc 函数分配寄存器相关地址空间;

使用 mm free 释放相关地址空间。

3.2 编写 makefile

```
🔊 🖨 📵 makefile (~/Github/test_5g_simd_ldpc) - gedit
 打开(O) ▼
             Ħ
CC
        = icc
CFLAGS
       = -Wall -O3 -march=core-avx2
OBJS
        = simd_ldpc.o test.o
DEST
        = main
        = -lm -lpthread -lmkl_rt -fopenmp
LIBS
INCLUDE = -I.
$(DEST): $(OBJS)
        $(CC) -o $(DEST) $(OBJS) $(LIBS) $(INCLUDE)
clean:
        rm -f $(DEST) $(OBJS)
```

Makefile ▼ 制表符宽度: 8 ▼ 行 11, 列 30 ▼ 插入

图 4: makefile

4 Linux 平台上的性能测试

4.1 Linux 平台上的 VTune 测试方法

- 1. source /opt/intel/vtune_amplifier/amplxe-vars.sh
- 2. amplxe-cl -collect hotspots ./main
- 3. amplxe-cl -report hotspots r000hs

4.2 本地测试

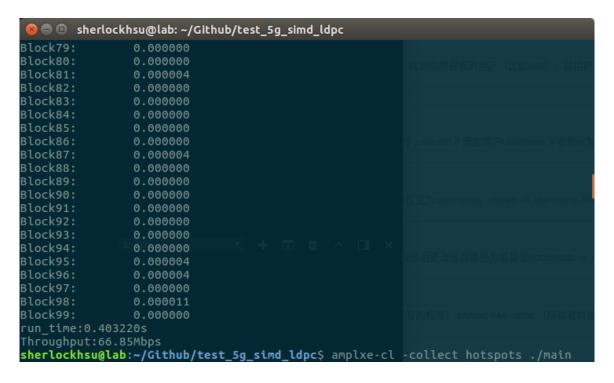


图 5: 本地运行结果

apsed Time: 1.723 used Time: 0.0									
rage CPU Utilization: 0.972									
lxe: Executing actions 100 % d t@ubuntu:/home/xuvi/test 5g si		c# amplxe-cl t-	report h	otspots -r r000hsamplx	e: Using result path `/home/xuyi/test	Sa simd ldpc/r	000hs'		
lxe: Executing actions 75 % Ge		ng a report							fective
							Time Module		
File Start Address 700									
_fec_ldpc_simd_decoder_avx2									
Os for lang sind sate matching		0.248s		main 0.248s	nr15_fec_ldpc_simd_decoder_avx2 Os	simd_ldpc.c 0.248s	0x404e30		
5_fec_ldpc_simd_rate_matching Os					nr15_fec_ldpc_simd_rate_matching	simd ldpc.c	0x40538f		
ngGaussian 706					0s	0.236s			
				0.180s		0.180s			
0s fec ldpc simd rate dematchin		0.180s		libmkl_rt.so 0.180s		[Unknown] 0.180s			
0s	9 0s			main	nr15 fec ldpc simd rate dematching	simd ldpc.c	0x4054cb		
					0.018s	0.062s			
				libc-dynamic.so					
		0.056s			0s main	0.056s test.c	0x400be0		
linux-x86-64.so.2] 715		/0.052s esf = 1		0.052s		0.052s			
						[Unknown]			
0s 718		0.032s		main 0.032s		avx2intrin.h 0.032s	0x40509c		
256_sign_epi8 Os					0s mm256 sign epi8	avx2intrin.h			
256_adds_epi8						0.032s			
shift_xor				0.028s		0.028s			
				main fix0.028s	circshift_xor 0s	simd_ldpc.c 0.028s	0x402420		
				libc.so.6		[Unknown]			
0s					_mm256_and_si256	avx2intrin.h	0x405115		
256_subs_epi8 0s		0.028s		0.028s main	0s mm256 subs epi8	0.028s avx2intrin.h			
aBernoulli Block8		0.020s		0.020s	0s	0.020s			
					viRngBernoulli				
				0.020s		0.020s			
		0.0205		libc-dynamic.so 0.020s		[Unknown] 0.020s	0x1c0d0		
				libpin3dwarf.so	operator new(unsigned long)	new.cpp			
						avx2intrin.h			
.56_abs_epi8 Block9 Os Block9					0s mm256 abs epi8	0.012s avx2intrin.h	0x4050b2		
56 unpacklo epi16 Block9		0.012s		0.012s		0.0125			
					_mm256_unpacklo_epi16				
Os Block9 256_store_si256 Block9		0.012s		libc.so.6 0.012s	func@0x9aa30 0s	[Unknown] 0.012s	0x9aa30		
0s run ti					_mm256_store_si256	avxintrin.h	0x40513f		
256_xor_si256 sherto				0.012s	0s	0.012s			
					mm256 xor si256	avx2intrin.h			

图 6: 服务器 VTune 测试结果 (错误)

	: CPU Time:Effective inverhead Time CPU Time (tion (Full)	e:Overhead Time:Creatio Source Fi	on CPU Time: le	Overhead Time:Sched	n Time CPU Ti uling CPU Tim	me:Spin 1 e:Overhea		l Spinning CP Time:Overhead	U Time:Spin Time:Lock Time:Atomics CPU Tin	c Conter ne:Overl	
15 fec ldpc simd decoder avx2											
								nr15_fec_ldpc			
15 fec ldpc simd rdm dec decbs											
									0s		
0x405e80								nr15_rec_tapc			
							s Os s libmkl intel lp64.so				
	US IS							vslNewStream			
0x400bd0											
l5_fec_ldpc_simd_cbs_enc_rm 0s											
0x405cf0 ngBernoulli	0.076s	0.076s				0.076s					
0x6bd210 BARESYSCALL DoCallAsmIntel64Lin	ux 0.060s	0.060s		0.060s							
								OS_BARESYSCAL			
.5 fec ldpc simd encoder scb	0.052s	0.052s				0.052s					
								nr15_fec_ldpc			
									0s		
							0s libc-dynamic.so				
							0s libpin3dwarf.so	operator new(Os unsigned long)		

图 7: 本地 VTune 测试结果

4.3 服务器测试

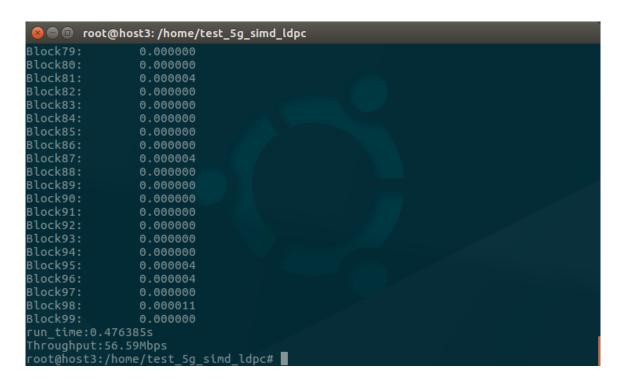


图 8: 服务器运行结果

```
| Discription |
```

图 9: 服务器 VTune 测试结果

5 仍存在的问题

```
🖯 🕕 root@ubuntu: /home/xuyi/test_5g_simd_ldpc
           0.000004
Block95:
Block96:
                   0.000004
Block97:
                   0.000000
Block98:
                  0.000011
Block99:
run_time:0.488829s
Throughput:55.15Mbps
root@ubuntu:/home/xuyi/test_5g_simd_ldpc# make
icc -Wall -03 -march=core-avx2 -c -o simd_ldpc.o simd_ldpc.c In file included from /usr/include/stdint.h(25),
                     from /usr/local/lib/gcc/x86_64-unknown-linux-gnu/5.4.0/include/
stdint.h(9),
                     from /opt/intel/compilers_and_libraries_2018.2.199/linux/compil
er/include/stdint.h(75),
from simd_ldpc.h(4),
from simd_ldpc.c(1):
/usr/include/features.h(374): catastrophic error: cannot open source file "sys/c
defs.h"
  # include <sys/cdefs.h>
compilation aborted for simd_ldpc.c (code 4)
make: *** [simd_ldpc.o] Error 4
root@ubuntu:/home/xuyi/test_5g_simd_ldpc# |
```

图 10: 更新 gcc 的服务器上遇到的 icc 编译错误问题