

EE 445L – Lab 7 (PCB Layout Lab)
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1.0 OBJECTIVES

See the requirements document.

2.0 HARDWARE DESIGN

See the schematic and PCB files

3.0 SOFTWARE DESIGN

None

4.0 MEASUREMENT DATA

Total estimated EE 445L cost: \$27.02

Total estimated real people cost: \$155.58

Max estimated current: 255 mA

5.0 ANALYSIS AND DISCUSSION

None