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Portfolio 1

Part 1:
Config 1: (460 * 360)pixels * 4 bits * 3 colors / (1byte/8bits) = 248,400 bytes
Config 2: (720 * 480)pixels * 4 bits * 3 colors / (1byte/8bits) = 518,400 bytes

datarate: 8frames/second * 256KB = 2048KB/second = 0.002048GB/second
Total hours recording: 64GB / 0.002048GB/sec * (1min/60sec) * (1h/60min) = approx 8.68055556 hours

datarate: 4proc * 128KB/(sec*proc) = 512KB /sec
required capacity: (512KB / sec) * (60sec / min) * (60min / h) * (24h / day) * 3day = 132,710,400 KB

2 month backup: 6centers * 64servers * 128nodes * 32TB = 1,572,864 TB
2 years worth of 2 month backups: 1,572,864 TB * 6backups/year * 2 years = 18,874,368 TB

Part2 How processor and memory interact:
Accessing data: In the event of a processor requesting data from memory, the processor will set the address via the address bus which will cause the memory to return the data in that address to the processor via the data bus.
Storing Data: In the event of a processor writing data to the memory, the processor will set the address bus as well as the data bus, causing the memory to store the data on the data bus at the address location specified on the address bus.
Adding Data: If a processor needs to add two values, it will access two separate memory locations to get their data and then the processor will add the data before storing it back in memory or performing another operation on the result.

Part 3: