

# Arithmetic Circuit Complexity: NPTEL Course

– Nitin Saxena

Soham Chatterjee

# Chapter 1

## Introduction

### 1.1 Turing Machines

Classically, computation is modeled using Turing Machine i.e. a computer program is seen as  $TM$  description

**Turing Machine ( $TM$ ):**  $M = (\Gamma, Q, \delta)$  where

- $\Gamma$  = Set of alphabets, say 0,1,  $\triangleright$  (start),  $\square$  (blank)
- $Q$  = Set of states (at least it has start state =  $q_s$ , final state =  $q_f$ )  
[whenever we say computation, we mean  $q_s$  to  $q_f$  finitely many steps are taken and whatever is there in tape is considered as output.]
- $\delta$  = transition function

$$\delta : Q \times \underbrace{\Gamma^2}_{\substack{\text{(Assuming 2} \\ \text{tapes one for} \\ \text{input bit and} \\ \text{other for work} \\ \text{tape for reading} \\ \text{bit at current} \\ \text{work tape head)}}} \longrightarrow Q \times \Gamma^2 \times \underbrace{\left\{ \overset{\text{Stay}}{S}, \overset{\text{Left}}{L}, \overset{\text{Right}}{R} \right\}}_{\text{head movement}}$$

[you can think  $\delta$  as your  $C$  program or computer program]

Since work tape is infinite you don't know how many steps will be taken.  $TM$  abstracts every possible device

#### Definition 1.1.1: Time and Space of $TM$

- **Time** is the number of steps for a given input  $x$ .
- **Space** is the number of worktape-cells used by  $TM$  on  $x$

### 1.2 Complexity Classes

#### Definition 1.2.1: $Dtime(f(n))$ and $Space(f(n))$

For a function  $f : \mathbb{N} \rightarrow \mathbb{R}_{>0}$  we can define complexity classes

- $Dtime(\mathbf{f}(\mathbf{n}))$ : { Set of all those problems that can be solved on a  $TM$  in time  $O(f(n))$  }
- $Space(\mathbf{f}(\mathbf{n}))$ : { Set of all those problems that can be solved on a  $TM$  in work space  $O(f(n))$  }

This leads to a zoo of complexity classes

**Definition 1.2.2:**  $P$ ,  $PSPACE$ ,  $NP$ ,  $L$ ,  $EXP$

- $P := \bigcup_{c>0} Dtime(n^c)$
- $PSPACE := \bigcup_{c>0} Space(n^c)$
- $NP := \bigcup_{c>0} Ntime(n^c)$   
 $\downarrow$   
on a non-deterministic  $TM$
- $L := Space(\log n)$
- $EXP := \{ \text{Problems that can be solved in time } 2^{n^c} \}$

**Note:-**

$$L \subseteq P \subseteq NP \subseteq PSPACE \subseteq EXP \subseteq EXPSPACE \subseteq EEXP \subseteq \dots$$

There are **randomized versions** (using probabilistic  $TM$ )

$$\begin{array}{ccccccc} ZPP & \subseteq & RP & \subseteq & BPP & \subseteq & PP \subseteq PSPACE \\ \text{zero error} & & \text{one-sided} & & \text{both-sided} & & \text{Probabilistic} \\ \text{probabilistic} & & \text{error} & & \text{error} & & \text{poly error} = \frac{1}{2} \\ \text{poly-time} & & & & \text{(Bounded error} & & \text{both sided)} \\ \text{(Las-Vegas} & & & & \text{Probabilistic} & & \\ \text{Algorithms)} & & & & \text{poly-time)} & & \end{array}$$

**Oracle-based complexity classes:**

$$\begin{array}{ccccccc} P & \subseteq & NP & \subseteq & NP^{NP} & \subseteq & NP^{\Sigma_2} \subseteq NP^{\Sigma_3} \subseteq \dots \subseteq PH \subseteq PSPACE \\ \parallel & & \parallel & & \parallel & & \parallel \\ \Sigma_0 & & \Sigma_1 & & \Sigma_2 & & \Sigma_3 & & \Sigma_4 \end{array}$$

This hierarchy is called Polynomial Hierarchy. Union of all of these is called  $PH$

This course will take a different route to build a zoo of complexity classes

## 1.3 Arithmetic Circuits

Instead of seeing computation as a sequence of very simple steps (that's what  $TM$  does. At each step transition is trivial but in the end something highly non trivial happens.) We'll review it as an algebraic expression

**Definition 1.3.1: Arithmetic Circuits**

An arithmetic circuit  $C$ , over a field  $\mathbb{F}[\bar{x}]$ , is a rooted  $DAG$  as follows

- The **leaves** are the variables  $x_1, x_2, \dots, x_n$  or field constant
- The **root** outputs a polynomial  $C(\bar{x}) \in \mathbb{F}[\bar{x}]$  (input)
- The **Internal vertices** are gates that compute  $(\times)$  or  $(+)$  in  $\mathbb{F}[\bar{x}]$
- The **edges** are called wires and they have constant labels to do scalar multiplication.

**Theorem 1.3.1**

Any polynomial has a depth-2 circuit

*Proof.* In first layer you have addition and in the bottom layer you have multiplication □

### Definition 1.3.2: Size, Depth, Degree

- **Size:** The size of the DAG (# of wires) is the size of the circuit size ( $c$ ). Sometimes we include the bit size of the constants on the wires
- **Depth:** A Max-path from a leaf to the root determines the depth of the circuit.
- **Degree:** Degree of  $c$  is the degree of intermediate polynomials computed in  $c$

### Question 1

How many monomials are there in  $n$  variable  $d$  degree polynomial ?

**Solution:**  $\binom{n+d}{d} \approx \left(\frac{n}{d}\right)^d, \left(\frac{d}{n}\right)^n$

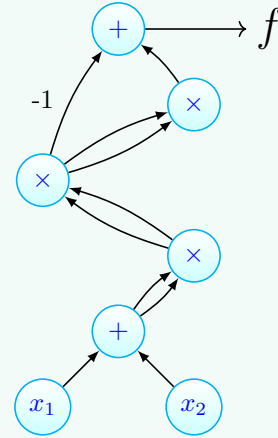
□

### Example 1.3.1

$$f(x_1, x_2) = (x_1 + x_2)^8 - (x_1 + x_2)^4.$$

The circuit size is small because of repeated squaring

Another example for repeated squaring is  $(1 + x)^{2^n}$



### Question 2: Foundational Question in this area

When a polynomial is not possible to compress by circuit representation and only way is depth-2 (worst possible way)

### Definition 1.3.3: Fanin, Fanout, Formula, Family of Circuits

- **Fanin:** Maximum in-degree
- **Fanout:** Maximum out-degree
- **Formula:** A circuit with fanout=1 is called formula

### Definition 1.3.4: Family of Circuits

Suppose  $\mathcal{F} := \{f_1(x_1, \dots, x_i) \mid n \geq 1\}$  is a family of polynomials (call it a problem). A family of circuits  $\mathcal{C} := \{C_i(x_1, \dots, x_n) \mid i \geq 1\}$  solves  $\mathcal{F} \forall i, C_i = f_i$

In this case, we say that  $\mathcal{F}$  can be solved in size bounded by  $size(C_n)$  and depth bounded by  $depth(C_n)$ . These two functions basically tell you the circuit complexity of the set of polynomials  $\mathcal{F}$

**Note:-**

Depth can be thought of as time (In parallel algorithm). Size can be thought of as space.  
This gives us a new way to measure the complexity of polynomials (or problems)

## 1.4 Arithmetic Complexity Classes

Arithmetic Complexity Classes were first defined by Valiant (1979). In particular, the arithmetic analogues of  $P$  and  $NP$

**Definition 1.4.1:  $VP$  (Valiant's  $P$ )**

$VP$  consists of polynomial families say  $\{f_n\}_n$  [ $f_n$  is a  $n$  variate polynomial] that can be solved or computed by Arithmetic Circuits of size- $\text{poly}(n)$  and degree- $\text{poly}(n)$

**Question 3**

Why degree- $\text{poly}(n)$  condition?

**Solution:** You want it to be practically implementable i.e. every aspect of the computation you would ideal want to be efficiently implementable on a turing machine. One necessary condition is degree should not blow p. We don't want circuit at a point to become too large.

The family  $\{x^{2^n}\}_n \notin VP$ . Though it is computable by  $O(n)$  size arithmetic circuits, its degree is not  $\text{poly}(n)$

□

We keep some field  $\mathbb{F}$  in mind while defining  $VP$ .

**Note:-**

Constants don't contribute to size or degree

An interesting polynomial (family) in  $VP$  is the **Determinant**.

$$\det_n(X_{n \times n}) := \sum_{\pi \in \text{Sym}(n)} \text{sign}(\pi) \prod_{i=1}^n x_{i, \pi(i)}$$

$\begin{matrix} \frac{n}{2} \approx n! \\ \text{such monomials} \\ \uparrow \\ \boxed{\prod_{i=1}^n x_{i, \pi(i)}} \\ \downarrow \\ \text{Don't have same row or column} \end{matrix}$

**Theorem 1.4.1**

Given a specialized  $X$  we can compute  $\det_n(X)$  in  $P$  (nearly quadratic time!).

*Proof.* Use Gaussian Elimination

□

**Note:-**

This does not give  $\{\det_n\}_n \in VP$  because, the above algorithm uses division, if-then-else, permutation etc.

What is the analogue of  $NP$  (non-determinism)? Do a large sum

**Definition 1.4.2:  $VNP$  (Valiant's  $NP$ )**

Polynomial family  $\{f_n\}_n \in VNP$  of

$$f_n(\bar{x}) = \sum_{\substack{\bar{w} \in \{0,1\}^{t(n)} \\ \downarrow \\ \text{witness}}} g(\bar{x}, \bar{w}) \quad \begin{array}{c} \downarrow \\ \text{verifier} \end{array}$$

where

$$t(n) = \text{poly}(n), \quad \{g_n\}_n \in VP$$

So a polynomial family  $\{f_n\} \in VNP$  if  $f_n$  can be written as a sum over witnesses with verifier evaluated. Replace  $\sum$  with  $\vee$  (in the boolean world) then you can recover definition of  $NP$  right in the boolean case because  $g$  = verifier algorithm, poly-time algorithm and it is just going over all possible certificates.

A standard problem in  $VNP$  is **Permanent**

$$\text{per}_n(X_{n \times n}) = \sum_{\pi \in \text{Sym}(n)} \prod_{i=1}^n x_{i, \pi(i)}$$

**Question 4**

Why Gaussian Elimination would fail on this?

**Solution:** Adding 2 rows or columns that does not change det but you don't know for per. In general it change.

□

**Note:-**

So per is the hardest problem, even harder than  $SAT$  in a way