Yuhei Morishita

	Cursor = 20,000ns Baseline = 0 Cursor-Baseline = 20,000ns		Baseline = 0	100ns	200ns	300ns	400ns	500ns	600ns	700ns	800ns	900ns
	ALUB[31:0]	'h00000x0x	▶ 00000x00	00000000	00005040	0000501C	00005048	00005044	00005020	00000007	00000000	
⊕ 🗁	ALUOp[1:0]	'hx	х	2	χ ο					1	χ ο	
	ALUSrc	х										
	ALU_out[31:0]	'h00000000	▶ (00000000		00005040	0000501C	00005048	00005044	00005020	FFFFFFF9	X 00005000	X 00005024
	ALUcontrol_out[3:0]	'hx	х		X 2					X 6	X 2	
	Branch	х										
	Instruction[31:0]	'h0800040D	08000400	X 00004025	X 8C095040	X 8C0A501C	X 8C0B5048	X 8C0C5044	X 8C0D5020	X 110D0006	X 8D4E0000	AD6E0000
	Jump	1	Ц									
	¶X_WitePegister_out[4:0]	'h00		_X_08	X 09	V OA	Х ов	<u>χος</u>	X OD	0x	OE.	0x
	MemRead	0										
	MemWrite	0		<u></u>								
	MemtoReg	x										
	Out4[31:0]	'h00000004	0000004									
⊕ (_	PC_in[31:0]	'h00001034	00001000	00001004	00001008	0000100C	00001010	00001014	00001018	0000101C	00001020	00001024
⊕ (_	PC_out[31:0]	'h00001034	0000000	00001000	00001004	00001008	0000100C	00001010	00001014	00001018	0000101C	00001020
⊕ (ReadData1_out[31:0]	'h00000000	▶ (00000000									00005024
⊞ Ѿ	ReadData2_out[31:0]	'h00000000	▶ (00000000			XX 00000000	XX 00000000	XX 00000000	XX 00000000	00000007	00000000	0000000C
	ReadData_out[31:0]	'hxxxxxxxx	xxxxxxx		00000001	00005000	00005024	00000004	00000007	xxxxxxx	00000000	xxxxxxx
	RegDst	х										
	RegWrite	0										
	WriteData_in[31:0]	'hxxxxxxxx	xxxxxxx	00000000	00000001	00005000	00005024	00000004	00000007	xxxxxxx	0000000C	xxxxxxx