Yuhei Morishita

	Cursor = 20,000ns Baseline = 0 Cursor-Baseline = 20,000ns		Baseline = 0	100ns	200ns	300ns	400ns	500ns	600ns	700ns	800ns	900ns
	•	'h00000x0x	▶ 00000x00	(00000000	X 00005020	X 00005030	X 00005024	X 0000502C	W 00000000	<u>γ</u> οοοοοοο	X 00000000	Υ 0000003A
			P 00000000			V 00003030	1 00003024		Y 2	V 1	\prec>====	Y 2
		'hx	X	2	_X				_\		_X	_\
	ALUSrc	х	. /									
□ <u>□</u>	₫'	'h00000000	> (00000000		X 00005020	X 00005030	X 00005024	X 0000502C	<u> </u>	X FFFFFFF8	XX 00005000	X 0000003A
	ALUcontrol_out[3:0]	'hx	х		X_2				X_1	_X_6	X 2	
	Branch	х										
	Instruction[31:0]	'h0800040D	08000400	00004025	X 8C095020	8C0A5030	X 8C0B5024	8C0C502C	00006825	110B0005	X 8D2E0000	01AE6820
	Jump	1										
	¶X_WiteRegister_out[4:0]	'h00	00	X 08	X 09	V OA	X 0B) 0C	V OD	0X	0E	V OD
	MemRead	0										
	MemWrite	0	П									
	MemtoReg	x										
	Out4[31:0]	'h00000004	00000004									
	PC in[31:0]	'h00001034	00001000	X 00001004	X 00001008	X 0000100C	X 00001010	X 00001014	X 00001018	W 0000101C	X 00001020	X 00001024
	PC_out[31:0]	'h00001034	Y 00000000	X 00001000	X 00001004	X 00001008	X 0000100C	X 00001010	X 00001014	X 00001018	X 0000101C	X 00001020
	ReadData1 out[31:0]	'h00000000	↑ 000000000									
		'h00000000	• (00000000			XX 00000000	XX 00000000	XX 00000000	XX 00000000	Y 00000008	X 00000000	X 0000003A
	ReadData2_out[31:0]	'hxxxxxxx			(00005000	00000004	X 00000008	X 00000001			00000000	
⊕ ५ <u>६</u>	ReadData_out[31:0]		xxxxxxx			X 00000004	V 00000008		xxxxxxx			xxxxxxxx
<u> </u>	RegDst	х	<u> </u>									
Ļ	RegWrite	0										
	WriteData_in[31:0]	'hxxxxxxxx	xxxxxxx	00000000	00005000	X 00000004	X 00000008	00000001	X 00000000	xxxxxxx	0000003A	X 0000003A