**Microprocessor Systems Lab 4**

Analog Conversion and Mac

Shane Conaboy

Justin Yost

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**Introduction**

Lab 4 investigated the use of the 8051 Microcontroller’s analog-to-digital converter (ADC) and digital-to-analog converter (DAC), as well as the multiply and accumulate (MAC) engine. The ADC was used to make a digital voltmeter that would measure the applied voltage and display it on the terminal screen. The DAC was then incorporated to convert a digital signal coming from the ADC back to analog and send it to an oscilloscope to compare the original signal to the processed one. Lastly, the MAC was used to create a notch filter that was observable through use of the oscilloscope and changing the frequency of the input signal.

**Procedure**

The first part of the lab was to set up and use the 8051’s ADC. The ADC was set for single-ended mode, the input port on AIN0.0, the clock rate less than 2.5MHz, as instructed by the reference manual, and an internal reference voltage of 2.4V by using the internal bias generator and buffer. A push-button was wired as an external interrupt to trigger an analog-to-digital conversion. If an external interrupt from the button was detected, the program would clear the “conversion done” flag, AD0INT, and set the AD0BUSY flag to 1 to begin the conversion. The program would then wait for AD0INT to be set high before reading the data from the ADC0L and ADC0H buffers. The input voltage was output to an accuracy of six decimal places. The program also recorded the highest and lowest voltage readings and computed the average of the last sixteen voltages. The high, low, and average numbers were output in hexadecimal format.

After the ADC was functioning, the DAC was incorporated next. There was almost no initial setup needed for the DAC; it was set to update the output on writes to DAC0H and then enabled. It was important that DAC0L be set before DAC0H in the code since the output updated upon setting DAC0H. The 8051 was programmed to read analog values using the ADC and convert them to digital values, and then using the DAC to convert them back to analog and update the output. A function generator was used to generate a sine wave as the input, and an oscilloscope was used to monitor both the input and output waveforms. Since the sine wave had both positive and negative values, and the ADC that was set up did not support negative voltages, the function generator was wired through a variable voltage source to add a DC offset to the signal and keep it positive.

With both the ADC and DAC working, the notch filter was then implemented using both a software implementation and a MAC implementation. Implementing the filter within the code was straightforward and allowed the notch to be viewed on the oscilloscope before using the MAC. The MAC was then setup in multiply and accumulate mode, fractional mode, non-saturation mode, and to right-shift the results. Using fractional mode, the MAC represents numbers using 2’s compliment as shown in Figure 1 below. Using this mode, the coefficients in the filter equation needed to be converted to 2’s compliment form. The hexadecimal 2’s compliment representation for the numbers are shown below. Once the MAC was set up, the procedure given in the lab was followed, loading the MAC with the appropriate values and reading from the rounding engine, before outputting the resulting signal through the DAC. The oscilloscope was used again to monitor both input and output signals. Once the MAC was working, the frequency of the input signal was changed until the notch frequency was found.



Figure : MAC Fractional Mode Data Representation

**Results**

All parts of the lab were successfully implemented. For the voltmeter, calculating the accuracy to 6 decimal places and avoiding overflow was a small issue. Since the printf command to interface the ANSI terminal does not output decimal values, the strategy was to multiple by 10N, where N is the number of places to shift the decimal over (6 in this case), and then convert the value to and integer. An unsigned integer is 16 bits long, representing numbers up to 65535, which is less than 106 and would cause overflow. To remedy this, the first three decimal places were obtained first using the above method, the integer value was subtracted off, and then the next three decimal places were obtained from that number. After the overflow issue was resolved, the voltmeter worked as expected and was verified using an actual multimeter.

There were little problems with setting up the DAC. The most time-consuming portion was setting up the function generator, oscilloscope, and DC offset. Once everything was wired appropriately, the input and output waveforms were visible on the oscilloscope and matched as expected.

Getting the notch filter set up also posed little problems. First, it was implemented in software and observed on the oscilloscope. It took a class period to figure out what was supposed to be seen since the lab did not mention if there was a target frequency to achieve and the oscilloscope was not displaying the output waveform in an easily-viewable manner. After everything was understood and the MAC was set up in fractional mode with the appropriate 2’s compliment values for the coefficients, the filter was implemented successfully, matching what the software implementation showed but at a higher frequency due to the increased running speed from using the MAC. The software and MAC notch filter frequencies were 1.55kHz and approximately 24kHz respectively. The MAC-implemented notch frequency is shown in Figure 2 below.

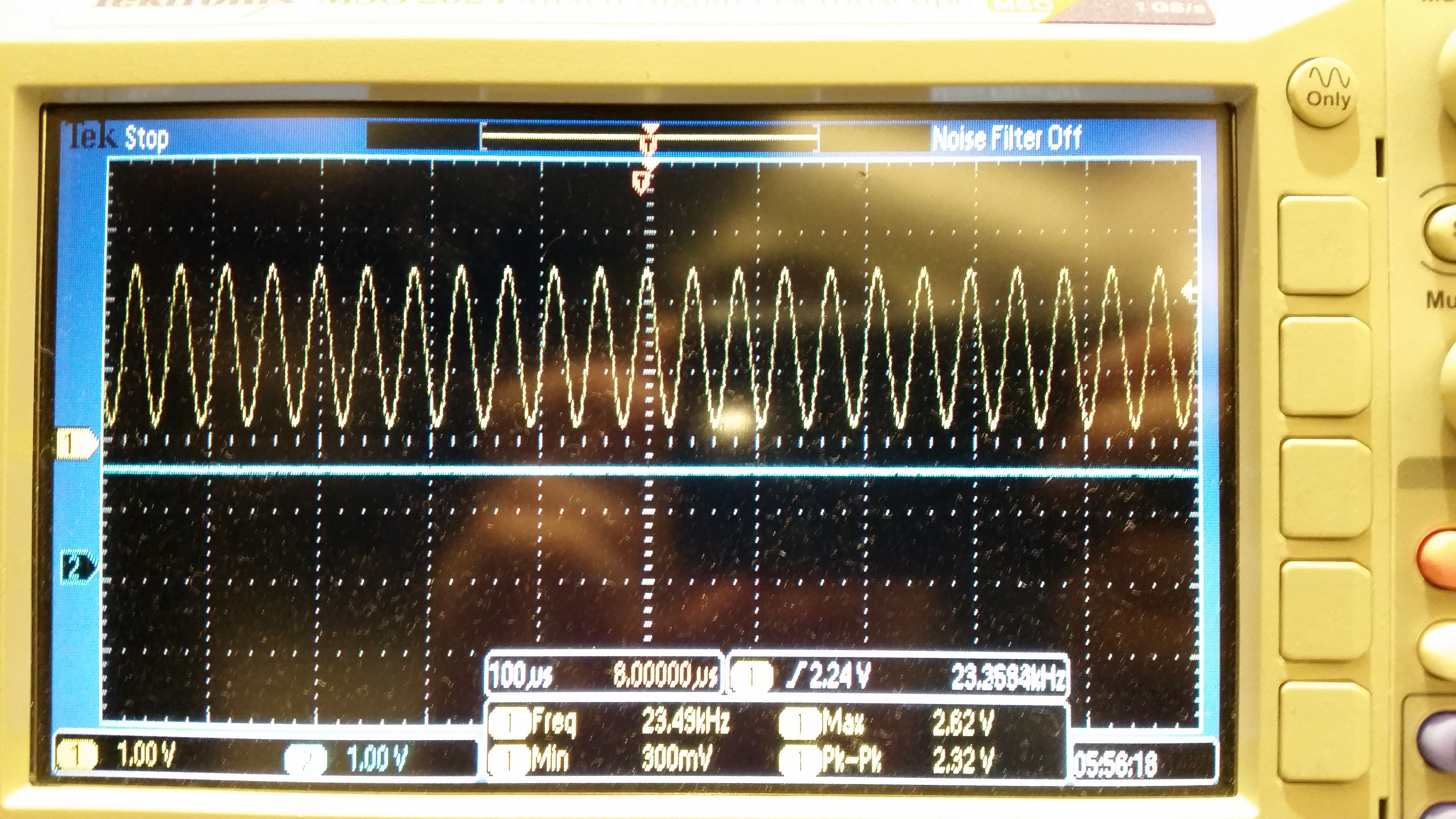


Figure : MAC-Implemented Notch Filter

**Conclusion**

The lab investigated the use of the 8051’s analog-to-digital and digital-to-analog converters, as well as the multiply and accumulate engine. Several different reference voltages, both internal and external, were applied to the ADC and DAC, and how the reference affected the output values was tested. In the last part of the lab, the speed at which the software runs was directly observed by comparing the notch frequencies of the software- and MAC-implemented filter. This lab demonstrated how to leverage different hardware components of the 8051 microcontroller and how using the built-in components can dramatically increase the running speed of the program.

**Appendix A**

**Appendix B: Circuit Schematic**

**Appendix C: Code**