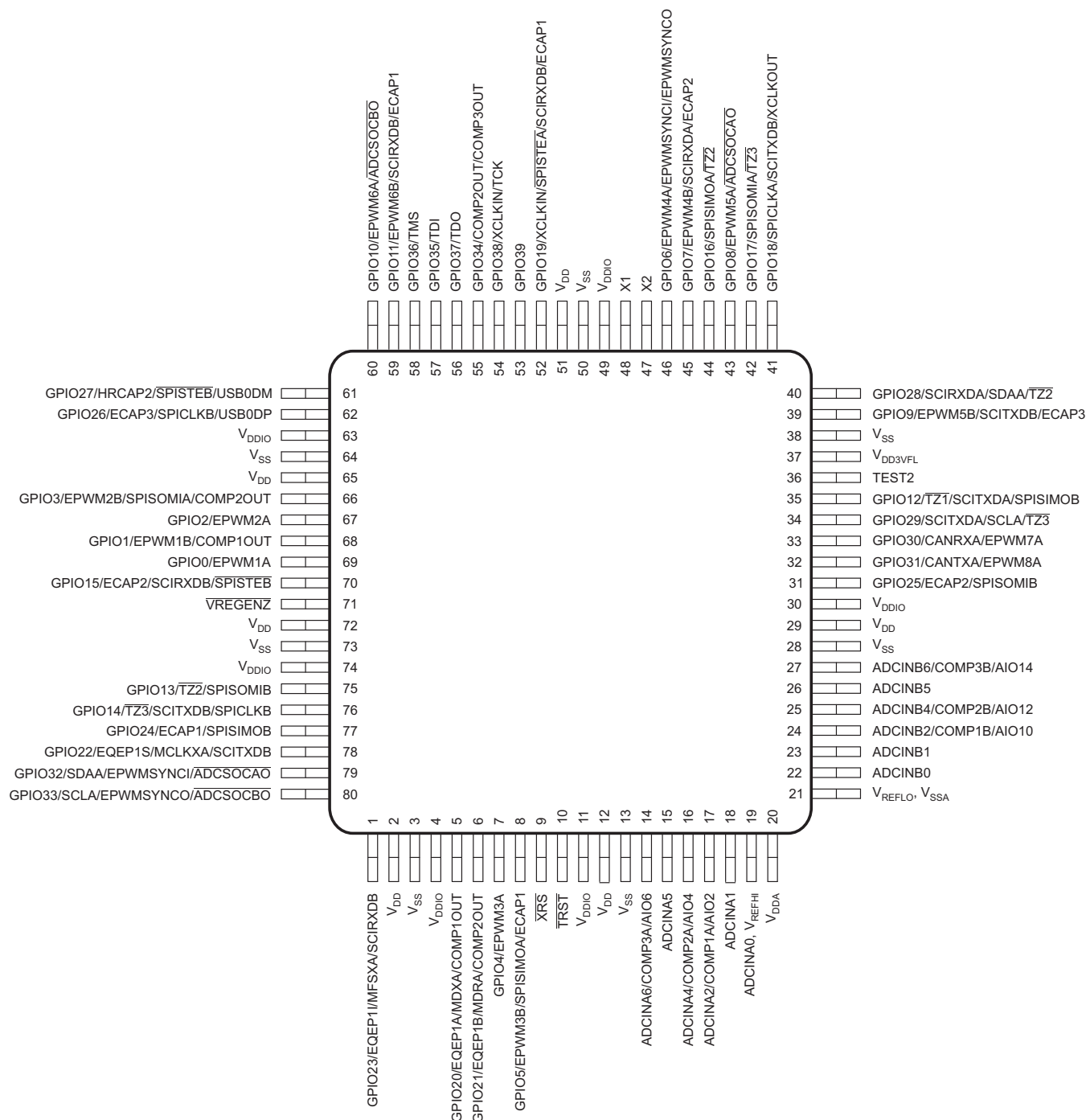


**F28069 USB controlSTICK PIN-OUT TABLE**

<b>1</b> ADC-A6 COMP3 (+VE)	<b>2</b> ADC-A2 COMP1 (+VE)	<b>3</b> ADC-A0	<b>4</b> 3V3
<b>5</b> ADC-A4 COMP2 (+VE)	<b>6</b> ADC-B1	<b>7</b> EPWM-4B GPIO-07	<b>8</b> TZ1 GPIO-12
<b>9</b> SCL-A GPIO-33	<b>10</b> ADC-B6 COMP3 (-VE)	<b>11</b> EPWM-4A GPIO-06	<b>12</b> ADC-A1
<b>13</b> SDA-A GPIO-32	<b>14</b> ADC-B0	<b>15</b> EPWM-3B GPIO-05	<b>16</b> 5V0 (Disabled by Default)
<b>17</b> EPWM-1A GPIO-00	<b>18</b> ADC-B4 COMP2 (-VE)	<b>19</b> EPWM-3A GPIO-04	<b>20</b> SPISOMI-A GPIO-17
<b>21</b> EPWM-1B GPIO-01	<b>22</b> ADC-A5	<b>23</b> EPWM-2B GPIO-03	<b>24</b> SPISIMO-A GPIO-16
<b>25</b> SPISTE-A GPIO-19	<b>26</b> ADC-B2 COMP1 (-VE)	<b>27</b> EPWM-2A GPIO-02	<b>28</b> GND
<b>29</b> SPICLK-A GPIO-18	<b>30</b> GPIO-34 (LED)	<b>31</b> PWM1A-DAC (Filtered)	<b>32</b> GND

## 2.3 Pin Assignments

Figure 2-8 shows the 80-pin PN/PFP pin assignments. Figure 2-9 shows the 100-pin PZ/PZP pin assignments.



- A. Pin 19: V<sub>REFHI</sub> and ADCINA0 share the same pin on the 80-pin PN/PFP device and their use is mutually exclusive to one another.  
Pin 21: V<sub>REFLO</sub> is always connected to V<sub>SSA</sub> on the 80-pin PN/PFP device.

Figure 2-8. 80-Pin PN/PFP (Top View)