

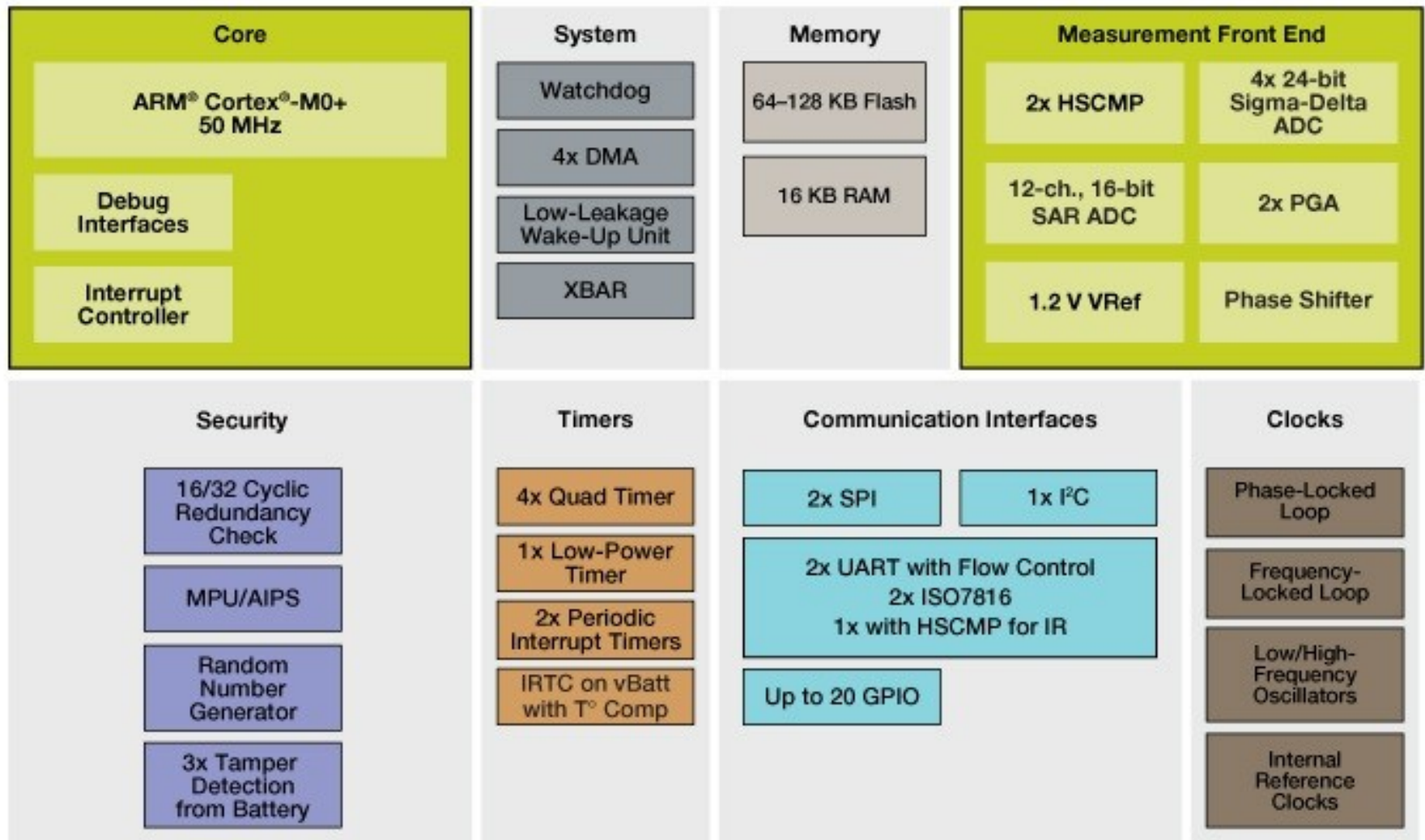
# **Conceptos de Arquitectura de Computadoras**

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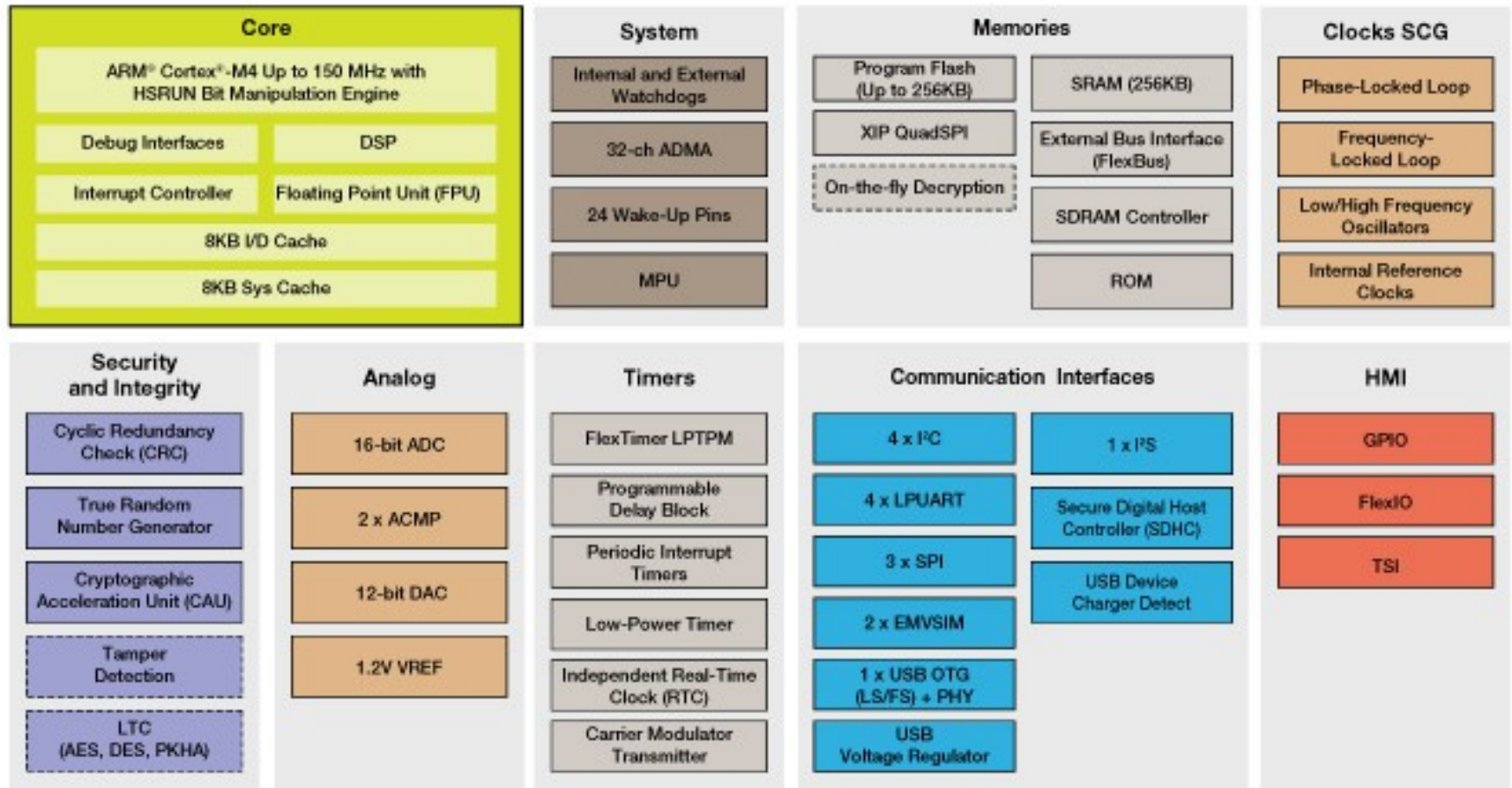
## **Anexo Clase 10**

### **Ejemplos comerciales**

## Kinetis KM1x Metering MCUs Block Diagram

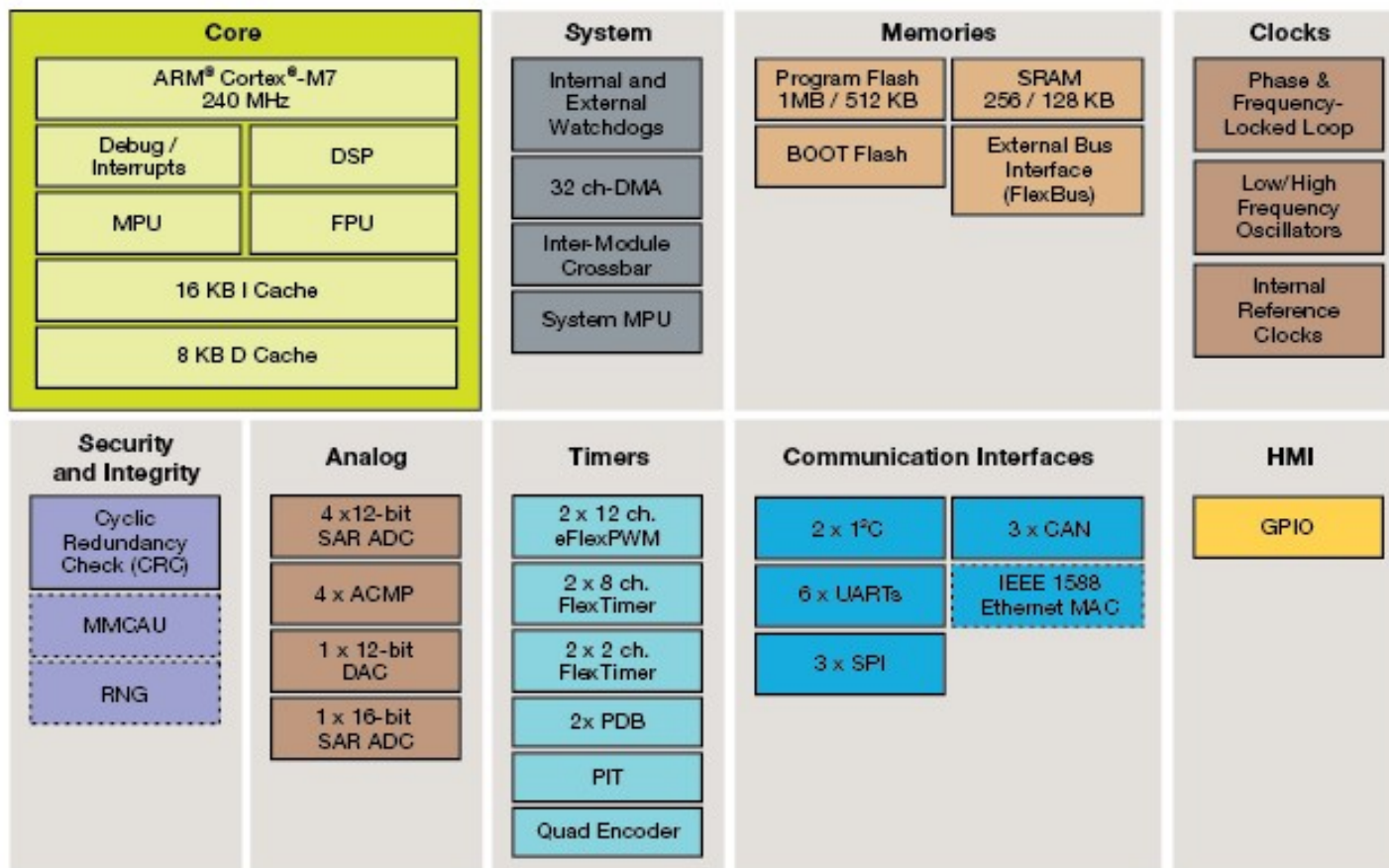


# Kinetis K8x\_150 MCU Family Block Diagram



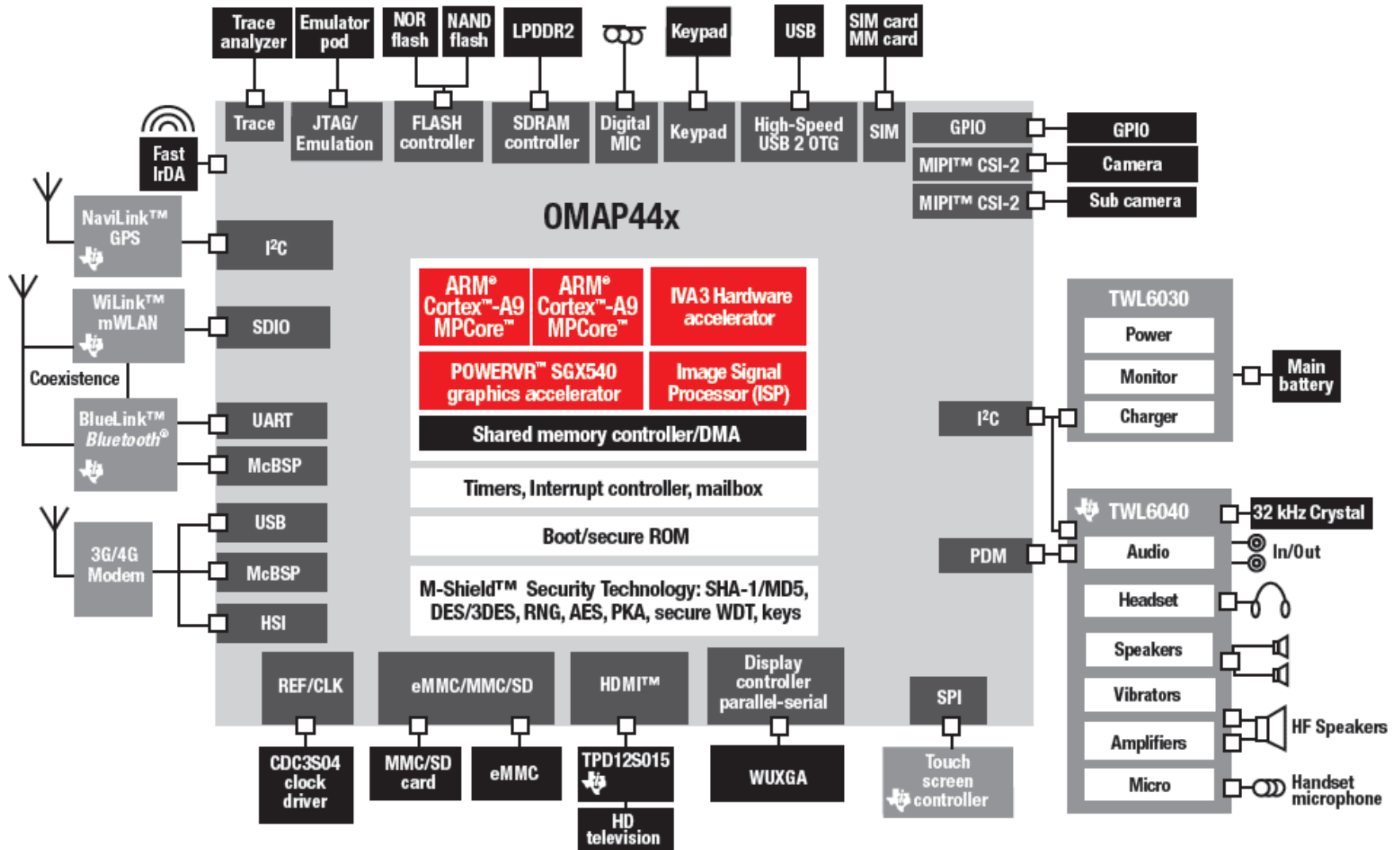
 Optional

# Kinetis KV5x MCU Family Block Diagram



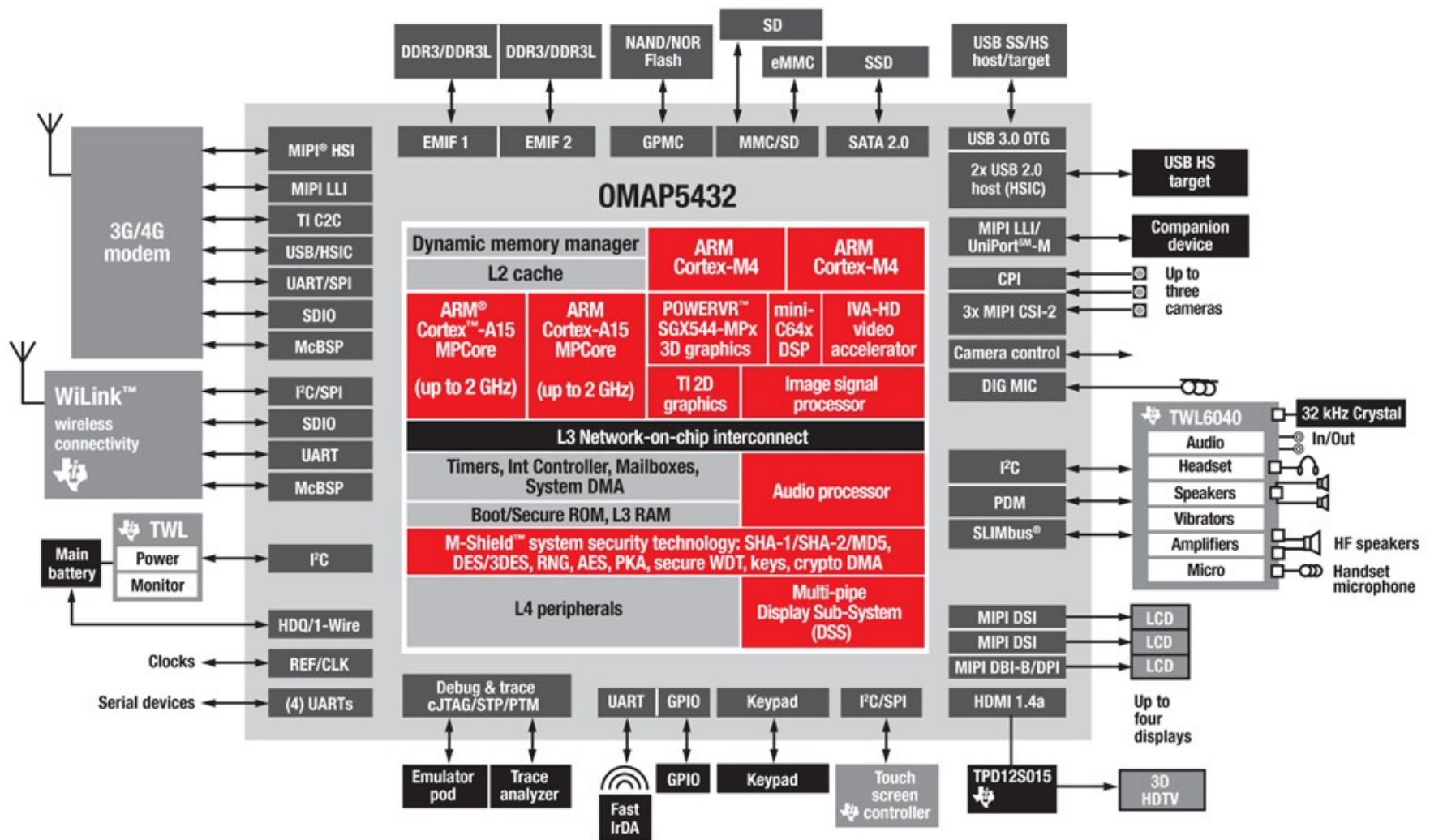
 Optional

# SoC con memoria compartida



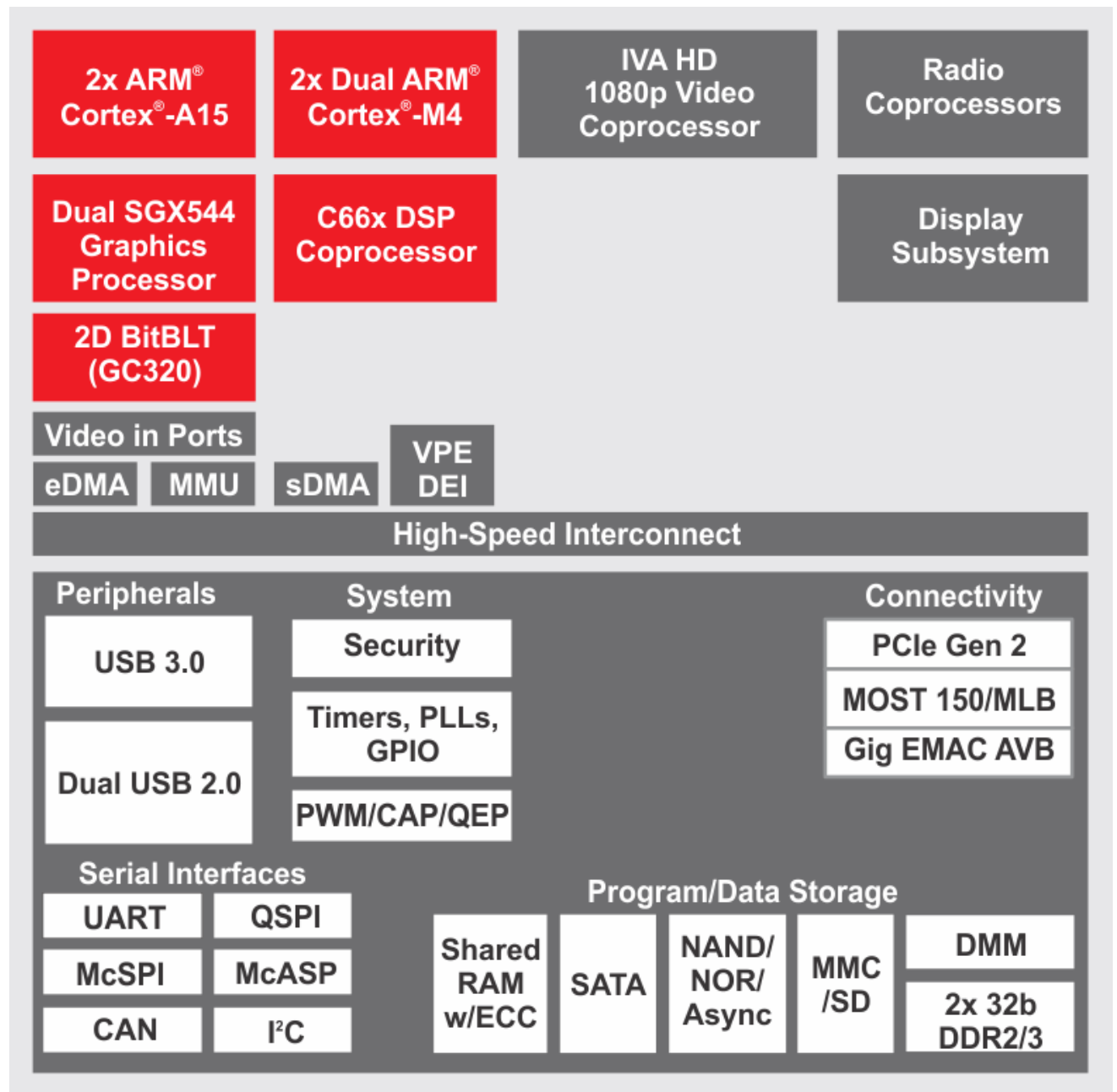


# NoC

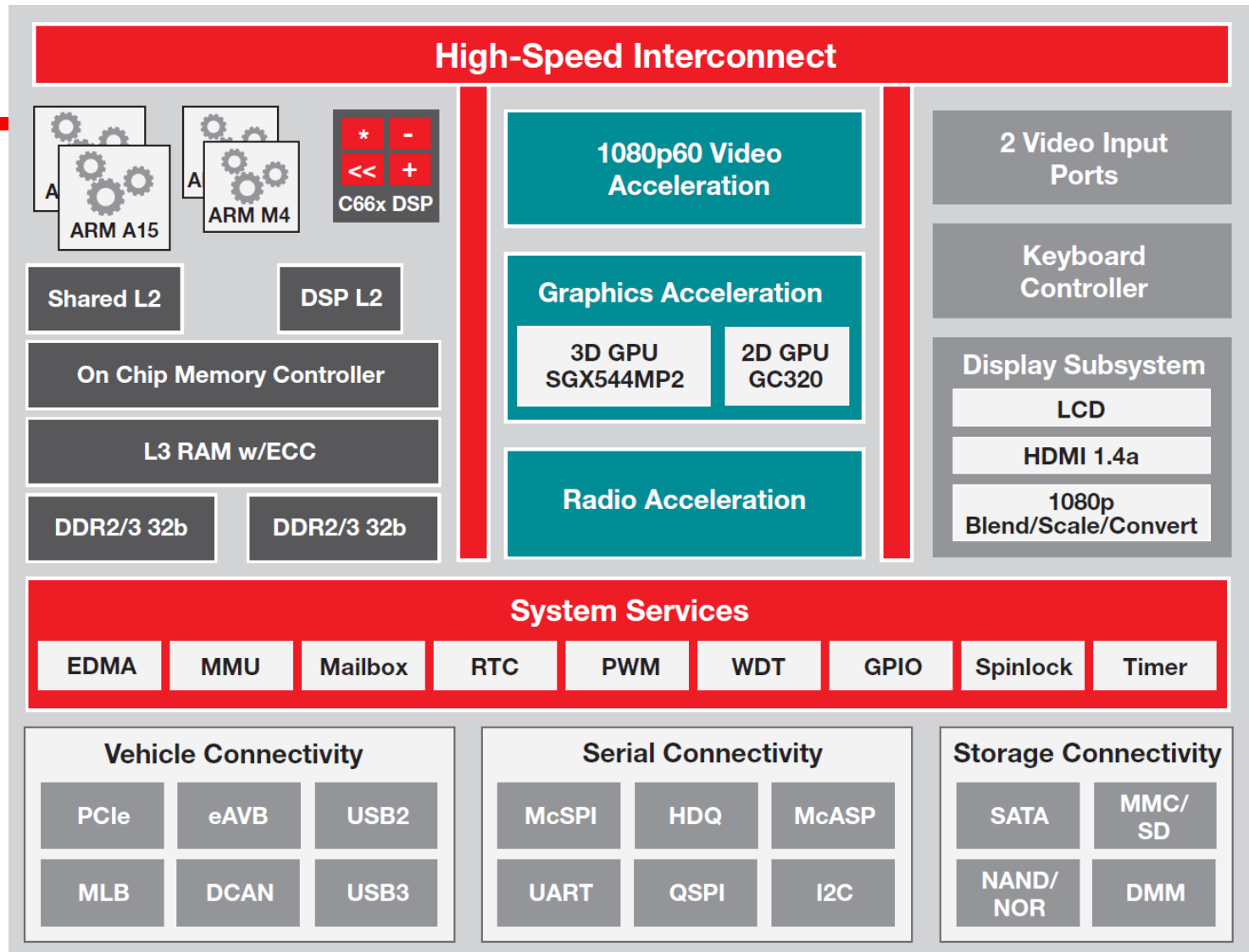


# DRA746 SoC

## Jacinto 6

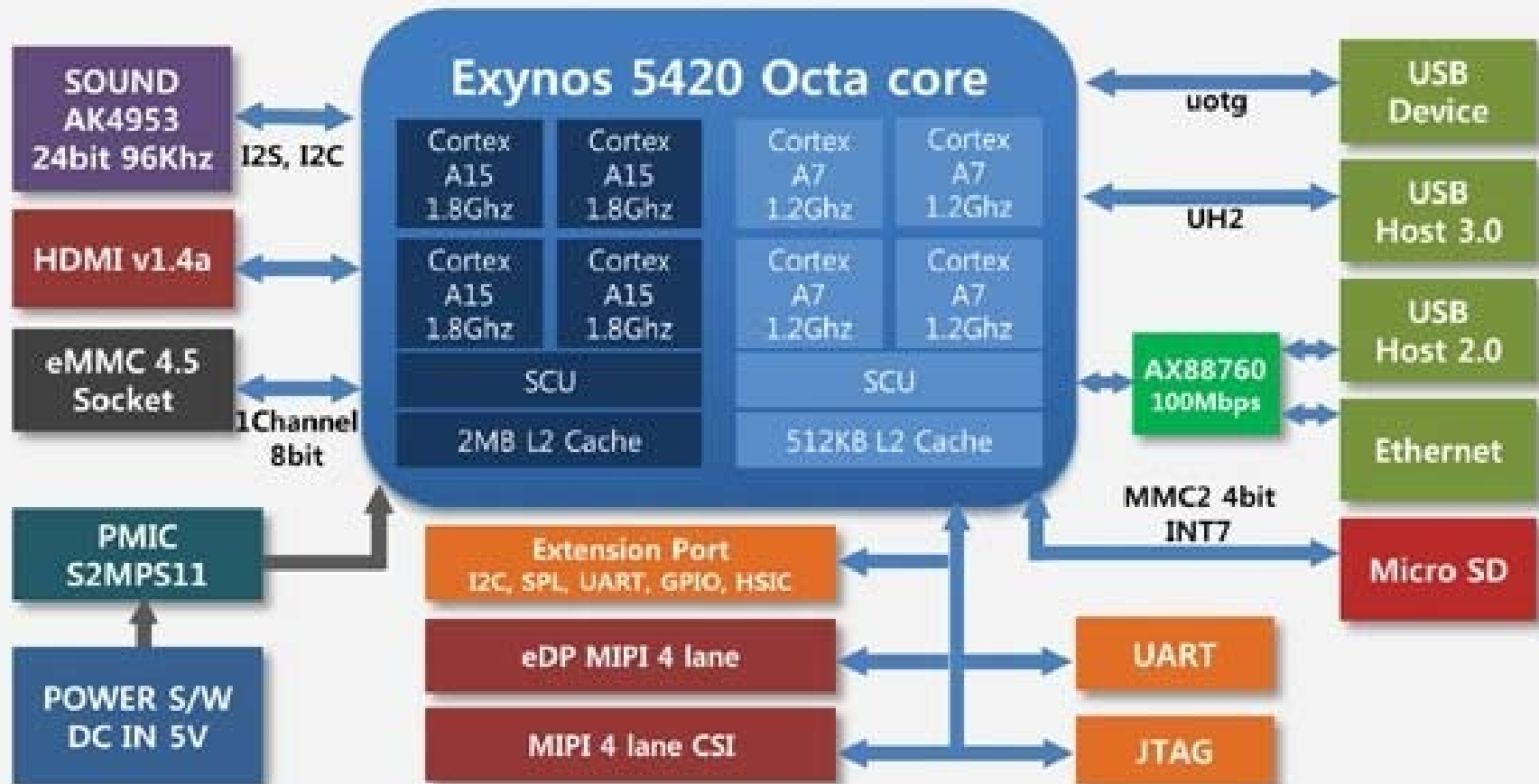


# Jacinto 6 en bloques

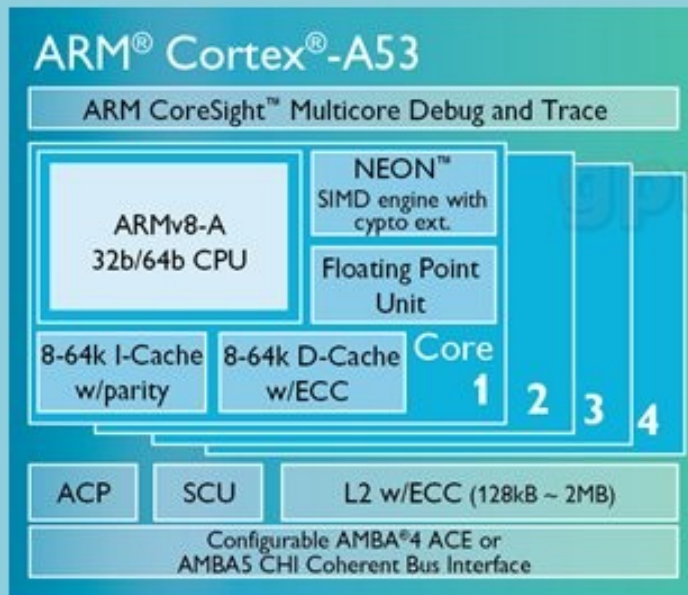




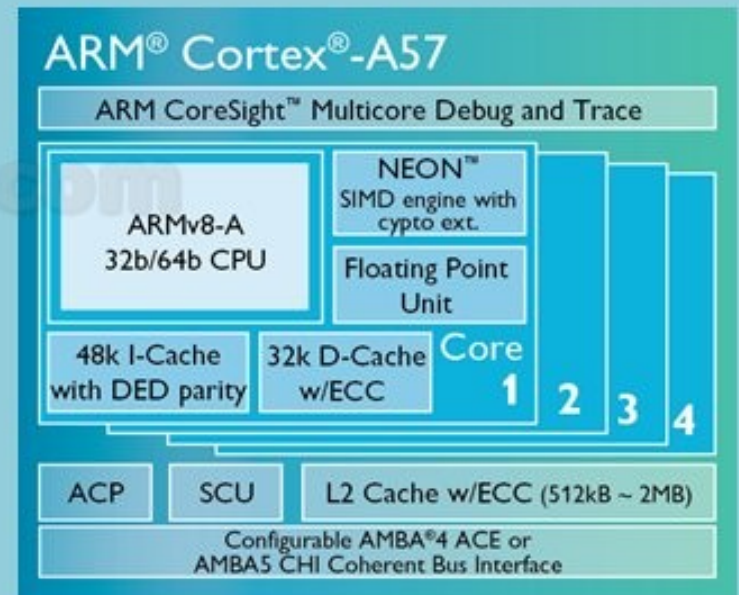
# NoC Exynos 5420



# Samsung Exynos 5 Octa (Exynos 5433)

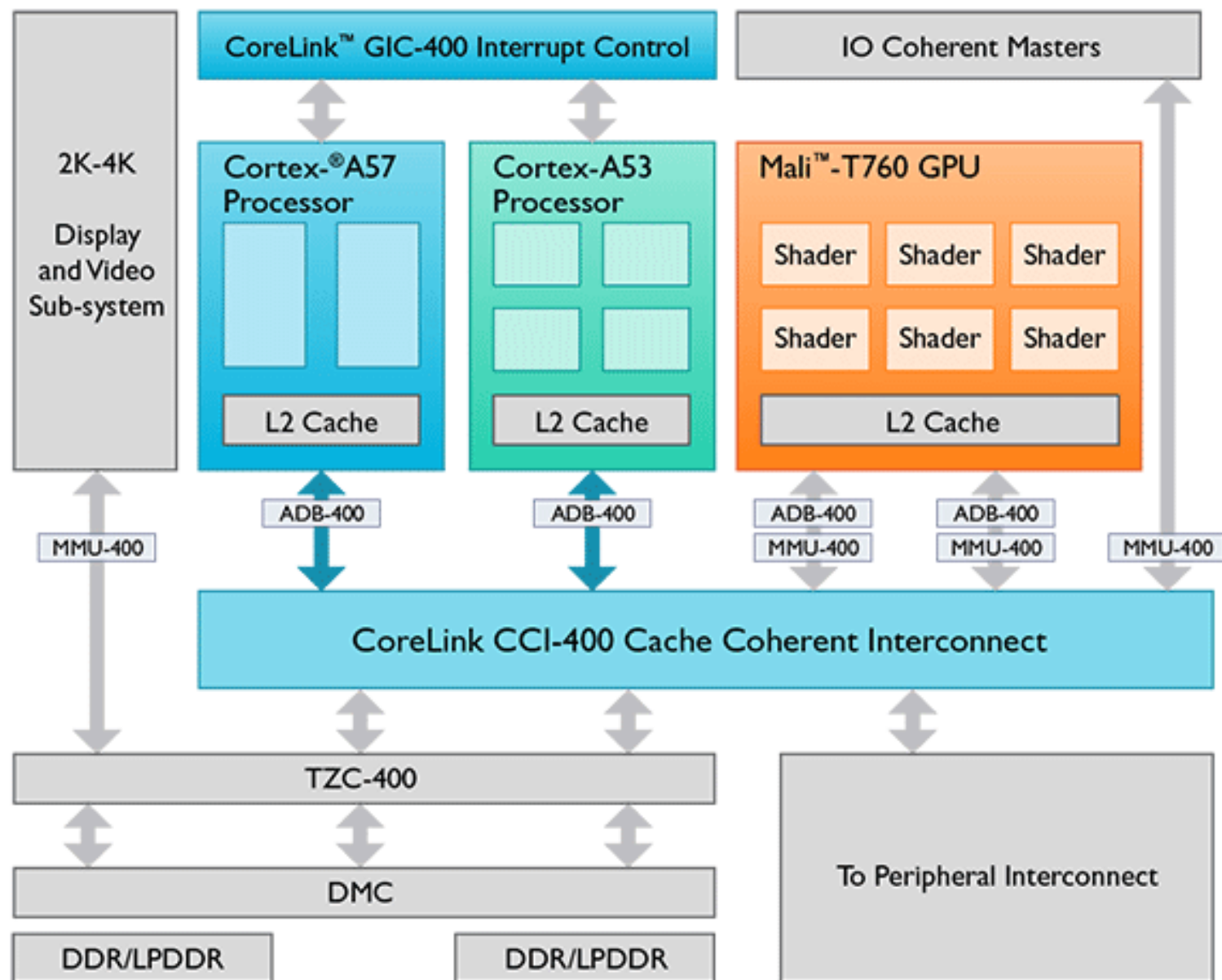


+



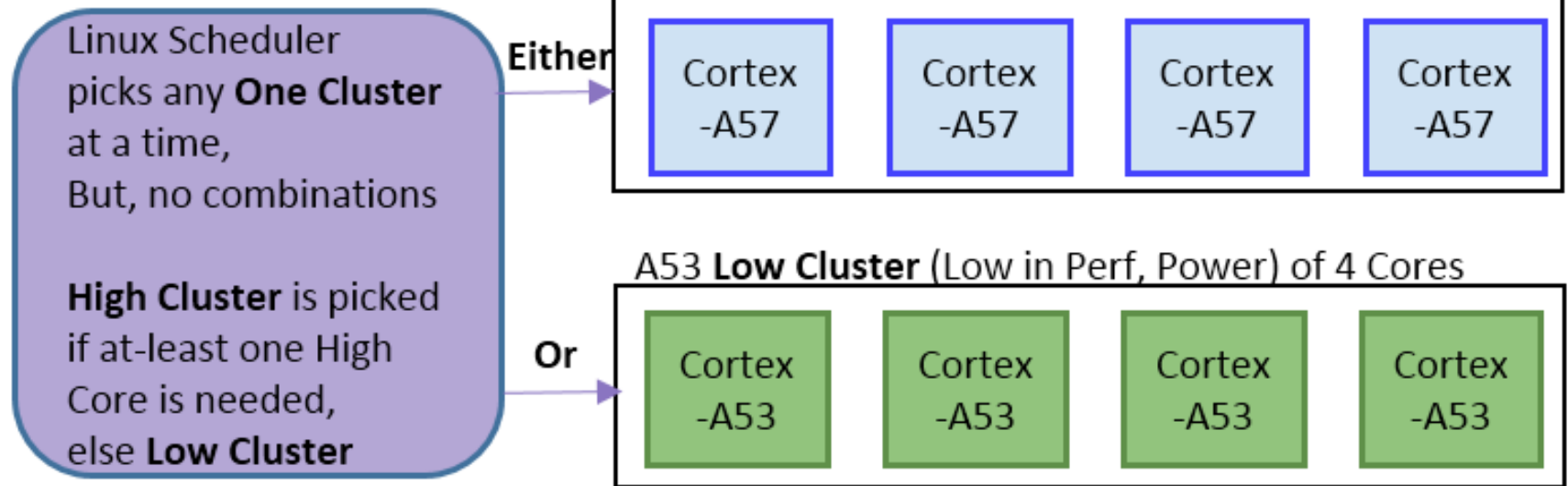
gpucpu.com

# **Samsung Galaxy S6**

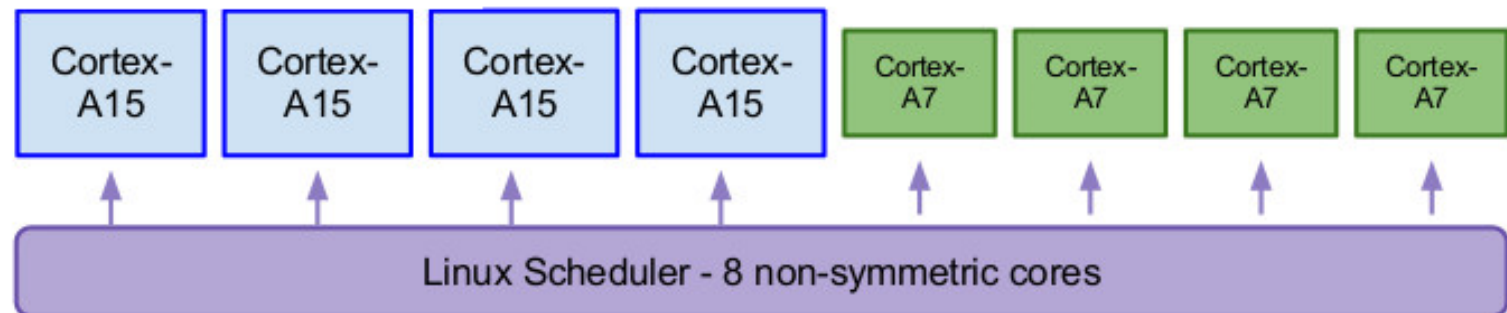


# Exynos octa 7420

## Clustered Switching:

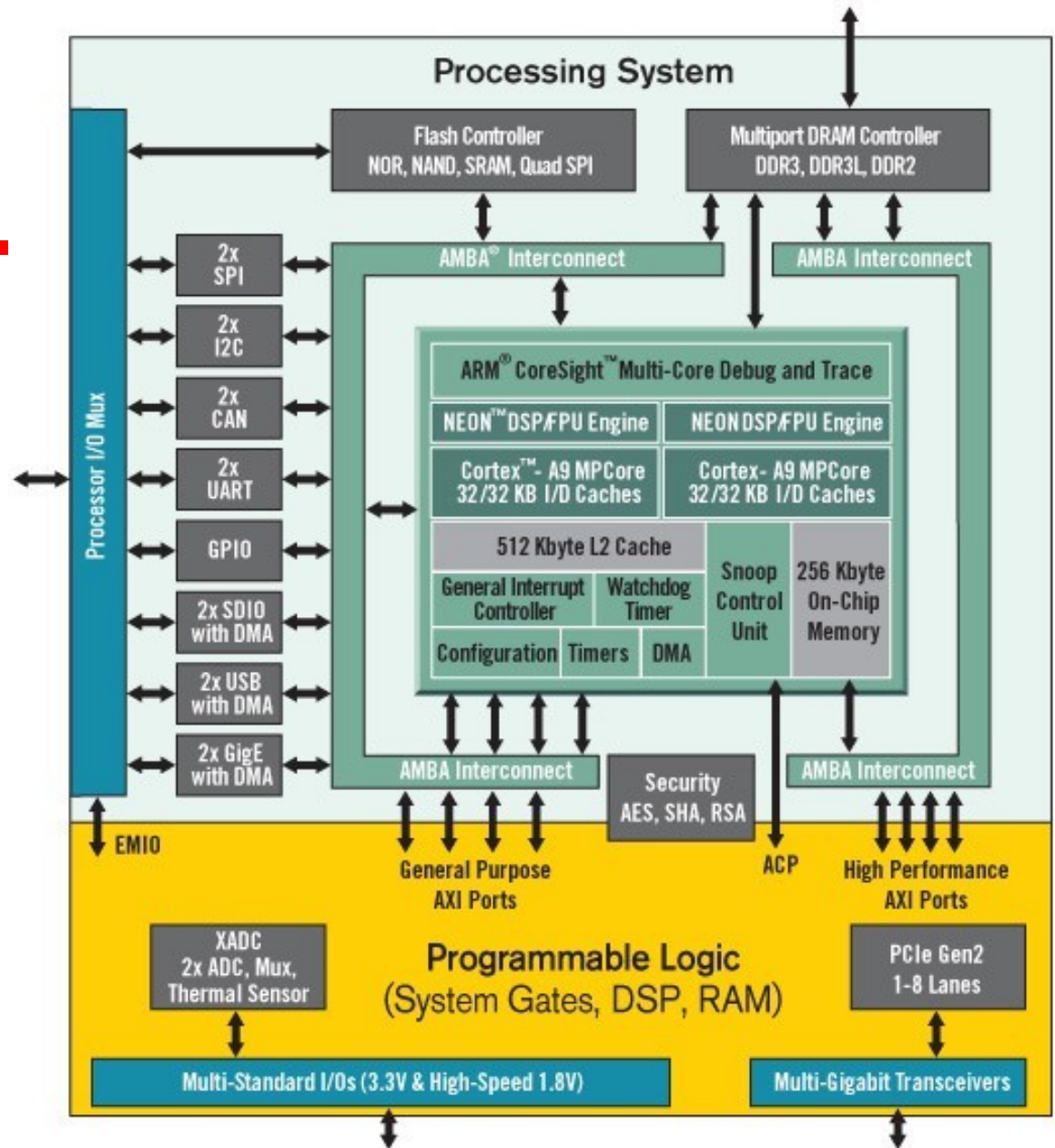


## Global Task Scheduling:



# Zynq-7000

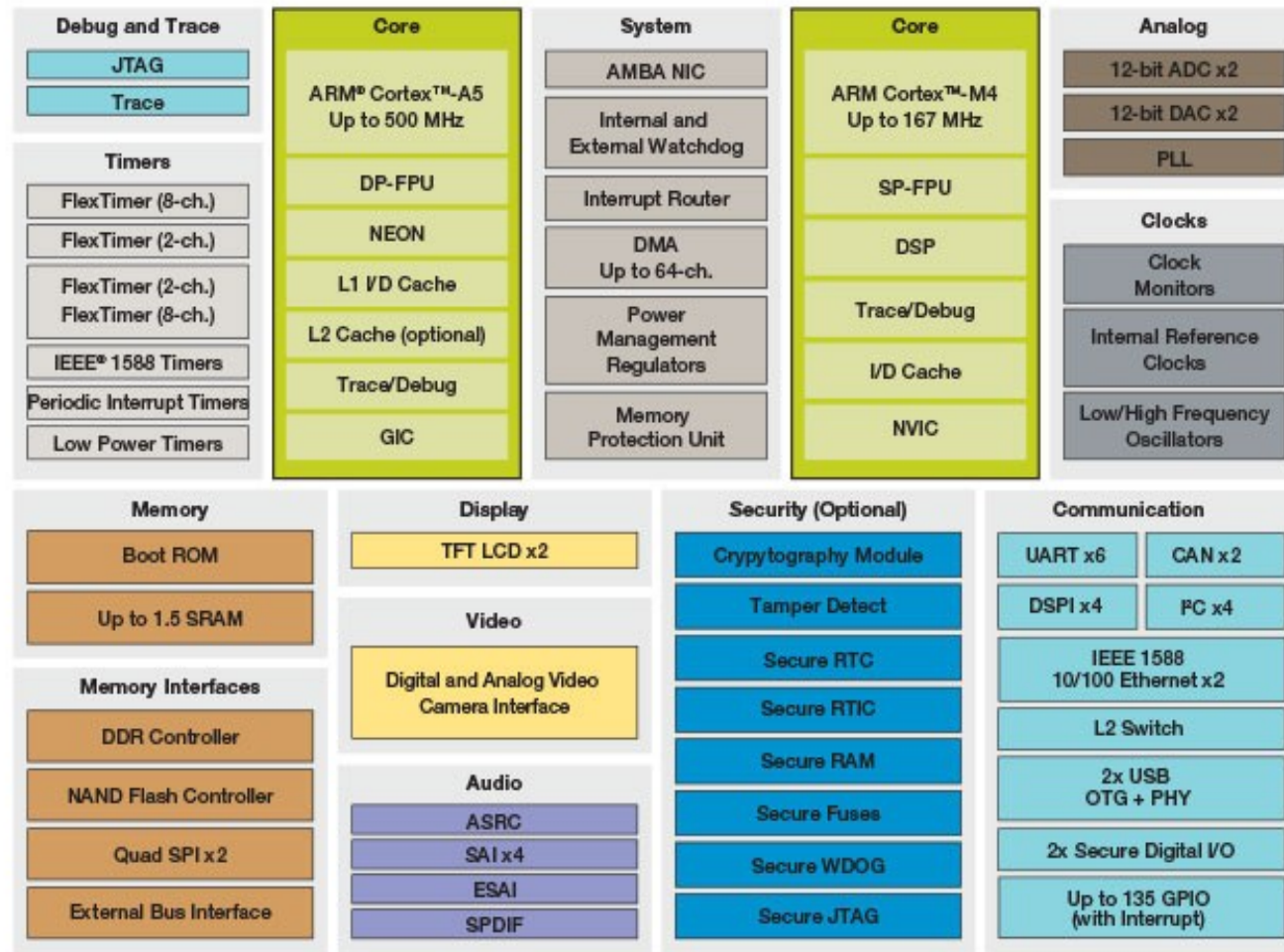
Xilinx





# Vybrid Freescale

## Vybrid VF6xx Block Diagram

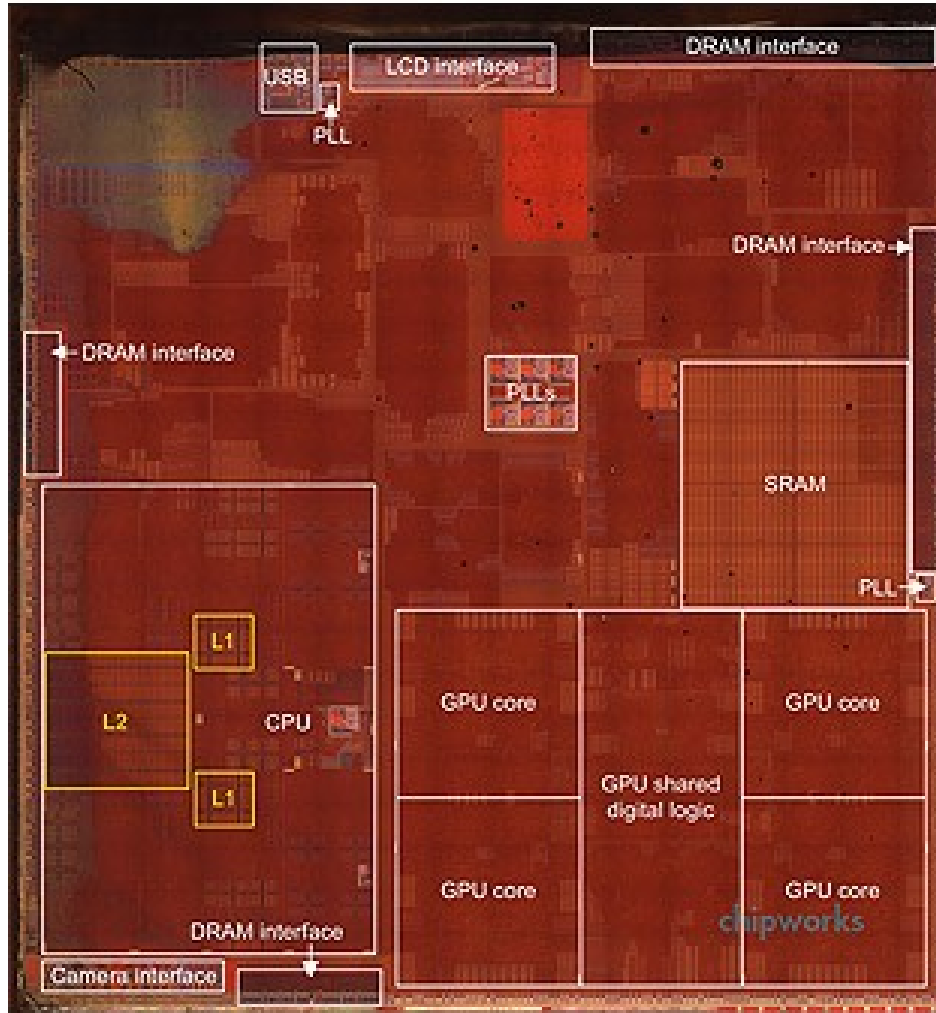




# Apple A8 SoC

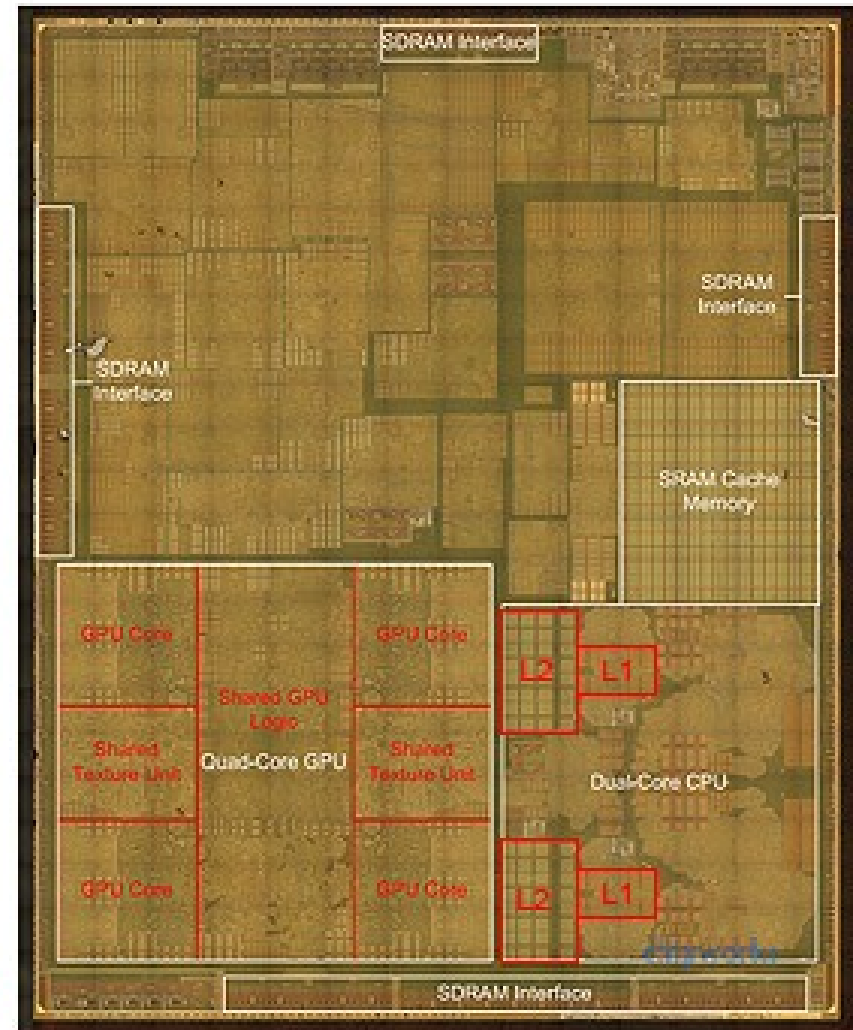
**A7**

9.83 x 10.45 mm



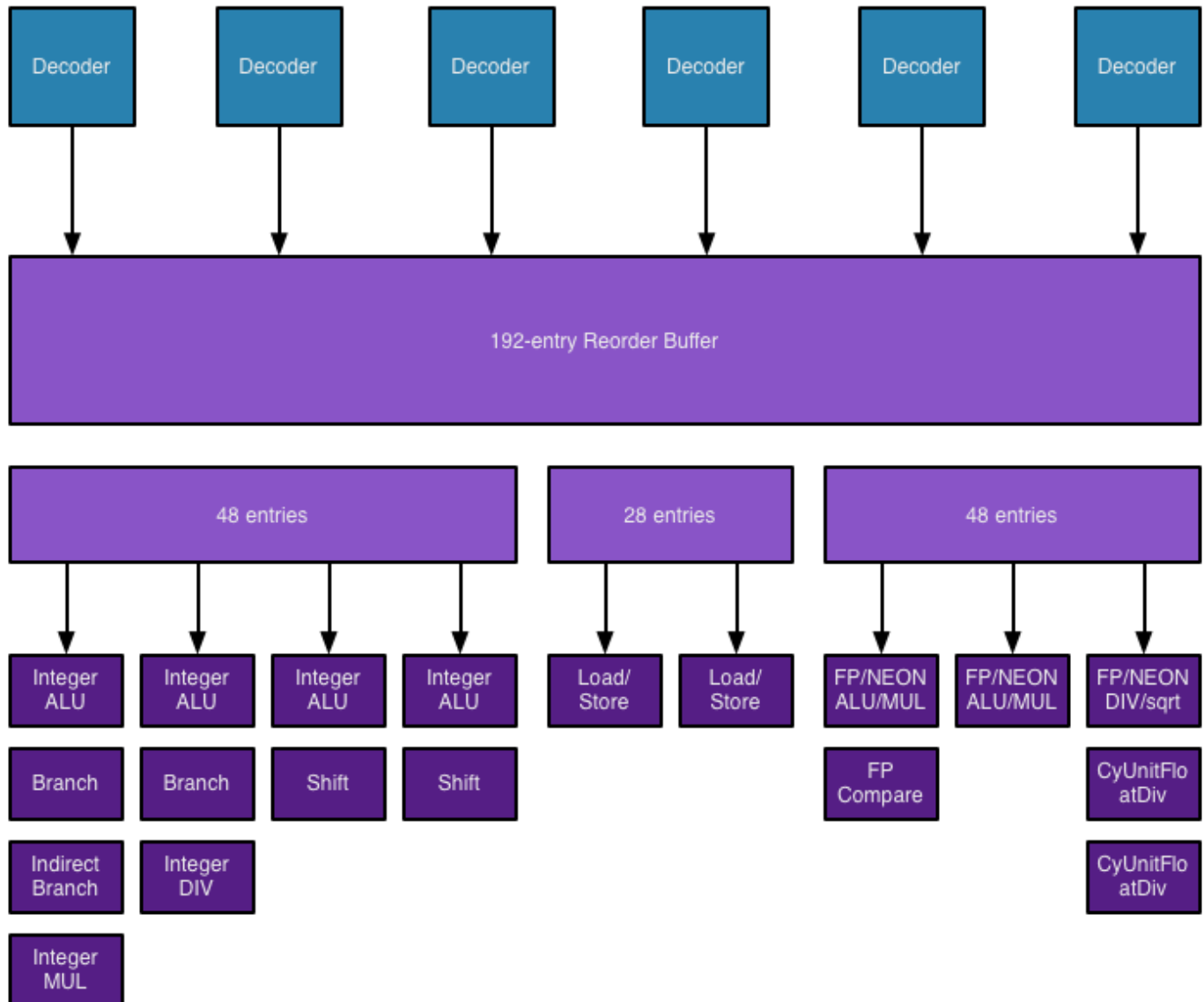
**A8**

8.47 x 10.50 mm

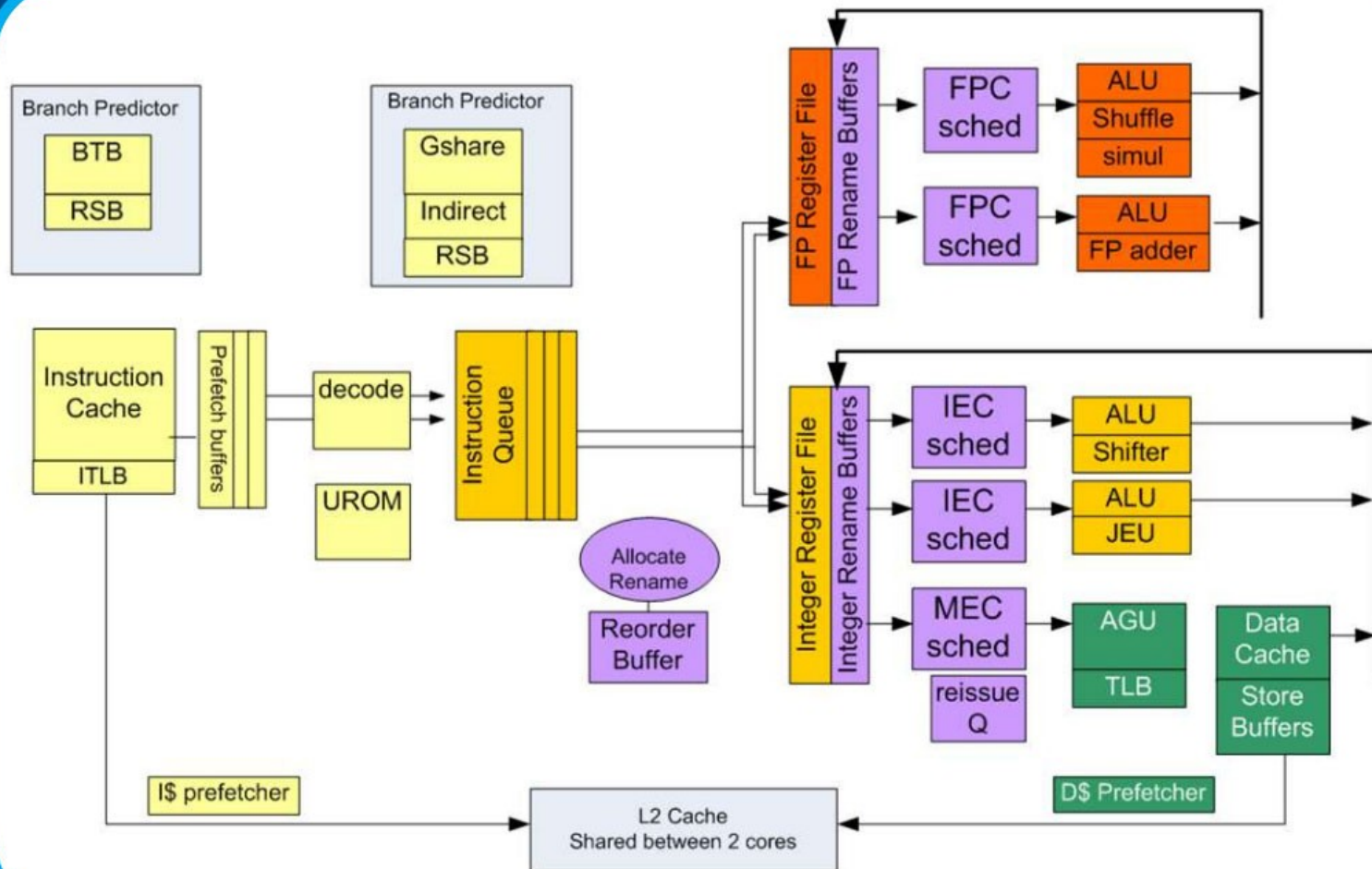


# iPhone 6

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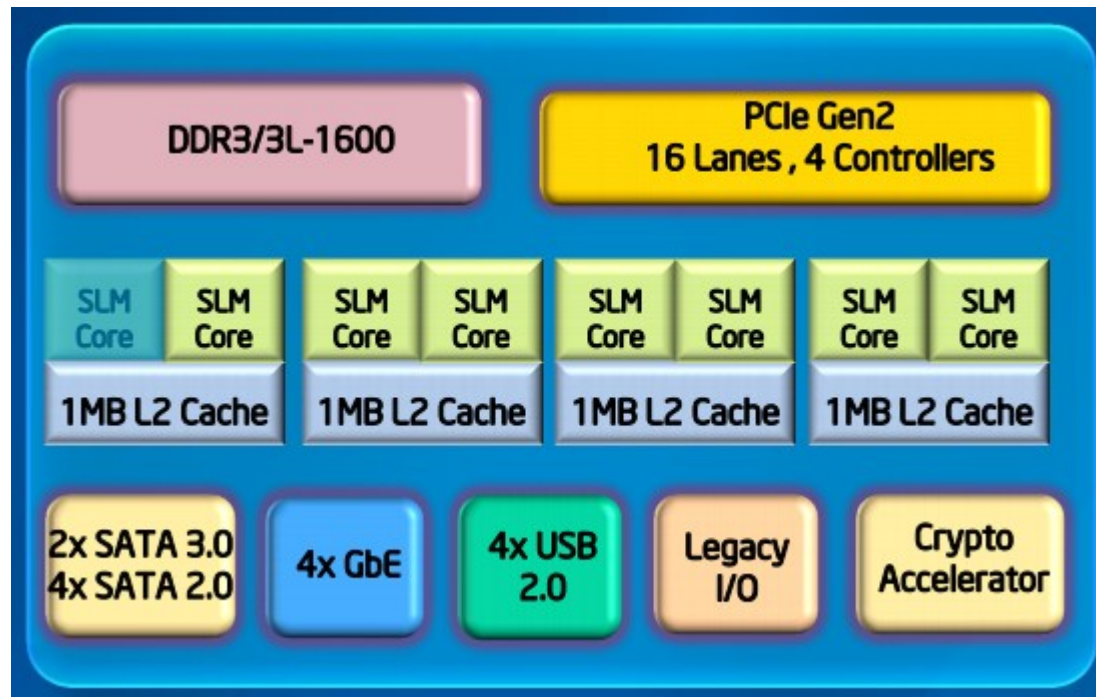


# Core Block Diagram



# Intel Atom C2000 family

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# Lecturas recomendadas

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- Fabricantes
  - [www.arm.com](http://www.arm.com)
  - [www.ti.com](http://www.ti.com)
  - [www.xilinx.com](http://www.xilinx.com)
  - [www.altera.com](http://www.altera.com)
  - [www.freescale.com](http://www.freescale.com)
  - [www.intel.com](http://www.intel.com)