

# Juyang Bai



(773) 754-6670

jbai23@jh.edu

juyangbai.github.io

## EDUCATION

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<b>Johns Hopkins University</b> , Baltimore, MD	<i>Aug 2023 - Present</i>
Ph.D. in Electrical and Computer Engineering	
<b>Northwestern University</b> , Evanston, IL	<i>Sep 2021 - Jun 2023</i>
M.S. in Electrical and Computer Engineering, GPA: 4.0/4.0	
<b>Zhejiang Sci-Tech University</b> , Hangzhou, Zhejiang	<i>Sep 2017 - Jul 2021</i>
B.S. in Electrical Information Engineering, Rank: 1/129, GPA: 3.63/5.0 (86/100)	

## EXPERIENCE

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<b>Professor Tran Research Group</b> , Baltimore, MD	<i>Jun. 2025 - Now</i>
Research Assistant	
Supervisor: Trac Duy Tran	
Research focus:	

- Explore multiple pruning methodologies for Vision Transformer (ViT) architectures applied to sparse image reconstruction, identifying optimal pruning techniques to retain model performance in sparse image recovery.
- Conduct a comprehensive analysis of pruning strategy impacts on computational efficiency, quantifying improvements in inference latency and energy consumption metrics when deployed on the Nvidia Jetson Nano platform.
- Work on designing and implementing an optimization framework that systematically balances model accuracy, inference speed, and power consumption for deployment on resource-constrained devices.

<b>Efficient, Secure and Intelligent Computing Laboratory</b> , Baltimore, MD	<i>Aug 2023 - Dec 2024</i>
Research Assistant	
Supervisor: Deliang Fan	
Research focus:	

- Propose a novel privacy-preserving DNN model obfuscation framework to obfuscate both model architecture and weights through a reinforcement learning (RL) based searching algorithm.
- Deploy the obfuscated DNN model in real TEE/GPU systems, where only the authorized user with keys could achieve full model functionality and accuracy.
- Explore one-bit flip attacks on soft-IP (AMD-Xilinx DPU) to reveal how malicious tenants can expose soft-IP instructions and mechanisms in both multi-tenant and single-tenant FPGA environments.
- Develop two defense mechanisms against one-bit flip attacks on soft-IP: a rule-based invalid instruction scanning method and a time series-based machine learning approach.

<b>Design Automation of Intelligent Systems Lab</b> , Evanston, IL	<i>May 2022 - May 2023</i>
Research Assistant	
Supervisor: Qi Zhu	
Research focus:	

- Design supervised contrastive learning and unsupervised semantics-guided reconstruction methods for vehicle trajectory anomaly detection, demonstrating their effectiveness across various settings.
- Explore and compare various representations and architectures for anomalous trajectory detection in supervised and unsupervised settings, demonstrating the algorithms' ability to generalize to unseen anomaly patterns and analyzing the effectiveness of different modules in the proposed methods.

<b>Ka Moamoa Lab</b> , Evanston, IL	<i>May 2022 - May 2023</i>
Research Assistant	
Supervisor: Josiah Hester	
Research focus:	

- Develop an external wearable hub that collects and processes data from multiple sensors (IMU, PPG, and ECG) to analyze human activities.
- Implement a Pulse Transit Time (PTT) algorithm to extract heart rate measurements from combined PPG and ECG signals.

<b>Image and Video Processing Lab</b> , Evanston, IL	<i>Sep 2022 - May 2023</i>
Research Assistant	

Supervisor: Aggelos Katsaggelos

Research focus:

- Develop a preprocessing algorithm to create a matched dataset of cell images from partial-wave spectroscopic (PWS) and confocal microscopy, aligning shape and rotation.
- Develop a UNeXt-based model to translate PWS cell images to their confocal microscopy equivalents, enabling cross-modality image synthesis.

**Meng's Lab**, Hangzhou, Zhejiang

*Mar 2019 - Jun 2021*

Research Assistant

Supervisor: Meng Li

Research focus:

- Develop a line-following Unmanned Ground Vehicle (UGV) for indoor inspections, integrating custom-designed driver boards, infrared tracking modules, PID control for navigation, and Tiny-YOLOv3 for real-time environmental monitoring.
- Propose and implement a DCNN-based fish classification system for challenging underwater environments, utilizing transfer learning and image augmentation to achieve 89% accuracy with limited data and computational resources.
- Develop a line patrol drone for electrical transmission inspection, incorporating Mahony complementary filtering for attitude adjustment, Kalman filtering for multi-sensor data fusion, and cascade PID control for precise navigation.

## PUBLICATIONS

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Phantom: Privacy-Preserving Deep Neural Network Model Obfuscation in Heterogeneous TEE and GPU Systems  
*USENIX Security 2025*

**Juyang Bai**, Md Hafizul Islam Chowdhuryy, Jingtao Li, Fan Yao, Chaitali Chakrabarti, Deliang Fan

Microarchitectural Shadows: A Case Study on the Unseen Security Risks of FPGA Virtualization in the Cloud  
*IEEE S&P 2026 (Under Review)*

Yukui Luo\*, **Juyang Bai**\*, Sabbir Ahmed, Adnan Siraj Rakin, Deliang Fan, Xiaolin Xu

Learning Representation for Anomaly Detection of Vehicle Trajectories

*IROS 2023*

Ruochen Jiao, **Juyang Bai**, Xiangguo Liu, Takami Sato, Xiaowei Yuan, Qi Alfred Chen, Qi Zhu

Towards a Toolkit for Free Living Wearable Development

*HASCA, 2022*

Blaine Rothrock, Alexander Curtiss, **Juyang Bai**, Josiah Hester

Fish Image Classification Using Deep Convolutional Neural Network

*CIPAE 2020*

Xiaojuan Lan, **Juyang Bai**, Meng Li, Jiajun Li.

## HONORS And AWARDS

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Government Scholarship in Zhejiang Province	<i>Nov 2020</i>
First-class Scholarship	<i>Oct 2020</i>
<b>3<sup>rd</sup> Place</b> China Collegiate Computing Contest-Network Technology Challenge	<i>Oct 2020</i>
Second-class Scholarship & Merit Student	<i>Nov 2019</i>
<b>1<sup>st</sup> Place</b> Softbank Robot Cup Wheeled Robot Sprint Group	<i>Nov 2019</i>
<b>3<sup>rd</sup> Place</b> Softbank Robot Cup Biped Robot Dance Group	<i>Nov 2019</i>
First-class Scholarship for Freshman	<i>Sep 2017</i>

## PROFESSIONAL SERVICE

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### Reviewer

International Conference on Computer-Aided Design (ICCAD), 2025

Design, Automation & Test in Europe Conference (DATE), 2025

IEEE/RSJ International Conference on Intelligent Robots and Systems (IROS), 2024

### Artifact Evaluation Committee

IEEE Symposium on Security and Privacy (S&P), 2026

### Web Chair

Top Picks in Hardware and Embedded Security (Workshop collocated with ICCAD 2025)

## TEACHING EXPERIENCE

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### **EN.520.344 Introduction to Digital Signal Processing**

*Fall 2025*

Role: Teaching Assistant

Instructor: Berrak Sisman

### **EN.520.230/231 Mastering Electronics II + Lab**

*Spring 2025*

Role: Teaching Assistant

Instructor: Amy Foster and Lucas Buccafusca

### **EN.520.231 - Mastering Electronics Lab**

*Fall 2024*

Role: Teaching Assistant

Instructor: Sathappan Ramesh

## TECHNICAL SKILLS

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**Programming Skills:** Python, C/C++, CUDA, Matlab, L<sup>A</sup>T<sub>E</sub>X

**Deep Learning Tools:** Pytorch, Tensorflow