For memory map details check in the reference manual:

A memory map with text and images

AI-generated content may be incorrect.

A close-up of a table

AI-generated content may be incorrect.

Bus domains:

* There are 4 peripheral busses: APB1, APB2, AHB1, AHB2. They have their own base addresses
* AHB is used for peripherals that need high speed data communication
* APB is used for peripherals that need low speed communication

To reset all the peripheral registers in one shot, use the following register:

A computer error message

AI-generated content may be incorrect.