

4-Output Part

Overview

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Part: Si5338
 Created By: ClockBuilder Pro v2.43 [2020-03-31]
 Timestamp: 2020-11-06 10:53:20 GMT-05:00

Design Rule Check

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Errors:

- No errors

Warnings:

- No warnings

Design

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Universal Hardware Input Pins:

P3: CLKIN
 P4: I2C_LSB

Inputs:

IN3: 25.0005 MHz Single-Ended
 [25 MHz + 20 ppm]

Outputs:

CLK0: 240 MHz LVDS 2.5 V
 CLK1: 125 MHz LVDS 2.5 V
 CLK2: 62.5 MHz LVDS 2.5 V
 CLK3: 25 MHz LVCMOS Single (+) 2.5 V

Device Pinout

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Pin #	Description
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1	CLKIN/XA
2	CLKINB/XB
3	CLKIN
4	I2C_LSB
5	FDBK
6	FDBKB
7	VDD
8	INTR
9	CLK3B
10	CLK3A
11	VDDO3
12	SCL
13	CLK2B
14	CLK2A
15	VDDO2
16	VDDO1
17	CLK1B
18	CLK1A
19	SDA
20	VDDO0
21	CLK0B
22	CLK0A
23	GND
24	VDD
PAD	GND (Must be grounded for proper device operation)



Frequency Plan

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Fpfd = 25.0005 MHz
Fvco = 2.4 GHz

P dividers:

P0 = 1
P1 = Unused

M = 95.998080038399232... [95 + 16635/16667]

N dividers:

N0:

Value: 10
CLK0: 240 MHz, Error: 0 ppm

N1:

Value: 19.2
CLK1: 125 MHz, Error: 0 ppm

N2:

Value: 38.4
CLK2: 62.5 MHz [62 + 1/2 MHz], Error: 0 ppm

N3:

Value: 96
CLK3: 25 MHz, Error: 0 ppm

R dividers:

R0 = 1
R1 = 1
R2 = 1
R3 = 1

This datasheet addendum is provided as supplemental information to the Si5338X datasheet, located at www.silabs.com/timing. You can search for and download any datasheet addendum for ClockBuilder Pro generated custom part numbers. Go to <http://www.silabs.com/custom-timing> for more information.

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