Data acquisition system for use with soft elastomeric capacitor

strain sensors for the early detection of cracks in steel beams

by

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A thesis submitted to the graduate faculty

in partial fulfillment of the requirements for the degree of

MASTER OF SCIENCE

Major: Electrical and Computer Engineering

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Ames, Iowa

2015

DEDICATION

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ACKNOWLEDGEMENTS

ABSTRACT

TODO: This is the text of my abstract that is part of the thesis itself. The abstract describes the work in general and the heading and style match the rest of the document.

CHAPTER 1. OVERVIEW

The ever increasing availability of smaller, cheaper, and more power efficient electronic devices mixed with a new type of strain skin sensor makes possible new methods of infrastructure health monitoring of steel beams in bridges. This new system of custom skins sensors and controlling electronics can assist in the detection of fatigue cracks forming on bridges and other steel infrastructure.

1.1 Introduction

Iowa State University's Civil, Construction, and Environmental Engineering department has developed a new type of strain skin sensor. To enable the use of this new type of strain sensor requires a new high sensitivity data acquisition and logging system. This system must be small, energy efficient, and capable of measuring small changes in capacitance in a reasonably short amount of time. This data acquisition and logging circuitry will ultimately be networked by a wireless sensor network in order to detect fatigue cracks forming on a bridge and assessing the overall health of the bridge during phase two of the project.

1.1.1 Capacitive Skin Sensor

This project revolves around a capacitive strain skin sensor develop by the Iowa State University department of Civil, Construction, and Environmental Engineering under the direction of Dr. Simon Laflamme. This sensor changes its capacitive relative to the applied strain on the sensor. This sensor is constructed in such a way that it can be attached to steel beams on bridges to detect cracks forming.

1.1.2 Capacitive Measurement

A large aspect of this project is to measure the capacitive change of the capacitive sensors to detect the formations of cracks. The sensors alone have a very small sensitivity to the change in strain making these measurements very difficult. Therefore a new method of measuring small changes in capacitance was developed which can greatly increase the sensitivity of the overall system. This new capacitive measurement system has been designed and is undergoing testing presently.

1.1.3 Data Acquisition

The final core part of this project is the ability to acquire and log the measured strain over time. This all together is the data acquisition and forms the entire system designed. The data acquisition also has self calibration features to counter the effects of using different capacitive sensors. The acquisition system connects to a computer via a USB connections and can log the strain data in MATLAB for analysis and post processing to detect the formation of cracks.

1.2 Design Requirements

This project was completed in collaboration with a parallel project to develop a strain sensor by the Department of Civil, Construction, and Environment Engineering at Iowa State University. The design requirements were set by that project and are given here.

1.2.1 Measured Parameter

The purpose of this project is to measure strain using the capacitive strain sensor designed in a parallel project. The project requirement is to measure strain at a resolution of one micro-strain over a range of minus five-hundred to plus five-hundred micro-strain from the unstrained position. The data acquisition system must be self calibrating and able to quickly recalibrate in the field.

1.2.2 Output Format

The output from the data acquisition circuit must be in a format that can be accurately measured by a data logging circuit such as an Intel iMote2. In addition a custom logging circuit was designed which interfaces with the data acquisition circuit and reports the results to a computer over a USB connection.

1.2.3 Power Requirements

In a future phase of this project the sensors and data acquisition system will be deployed onto remote bridges in a sensor network scheme. After deployment the entire system will need to run using energy harvested from the surrounding environment. Due to this restriction care must be taken throughout the design to ensure the minimum energy is required to measure the strain.

CHAPTER 2.   METHODS AND PROCEDURES

2.1   Capacitive Skin Sensor

The strain sensor used in this project is a soft elastomeric capacitor (SEC) developed by the Department of Civil, Construction, and Environmental Engineering at Iowa State University. The SEC is composed of a nanocomposite mix of poly-styrene-co-ethylene-co-butylene-co-styrene (SEBS) (Dryfelx 500120) doped with rutile TiO_2 (Sachtleben R 320 D) serving as a dielectric for the capacitor?).

2.1.1   Capacitive properties

The strain to capacitance properties were shown by Dr. Laflamme et al. to be the form shown in Equation 2.1. The parameter C_0 is the nominal capacitance of the sensor and is estimated to be in the range of 500pF. The value of the gain factor λ is dependent on the geometry of the sensor; it is 2 for the capacitive skin sensors used in this study. The value of ϵ_s is the strain seen by the sensor and ΔC is the change in the capacitance of the sensor due to that strain. This can be rewritten in the form of Equation 2.2

$$\frac{\Delta C}{C_0} = \lambda \epsilon_s \tag{2.1}$$

$$\Delta C = \lambda C_0 \epsilon_s \tag{2.2}$$

The overall capacitance of the strain sensor is the nominal capacitance plus the capacitance due to an applied strain. This capacitance C_{sens} is shown in Equation 2.3. The capacitance due to strain is much smaller than the nominal capacitance; it is on the order of femtofarads for an applied strain of one micro strain. The typical values for the parameters of a capacitive skin sensor are shown in Table 2.1.

Parameter	Typical Value
C_0	500pF
λ	2

Table 2.1   Typical capacitive strain gauge parameters

$$C_{sens} = C_0 + \Delta C = C_0 + \lambda C_0 \epsilon_s = C_0(1 + \lambda \epsilon_s) \tag{2.3}$$

2.2   Capacitive Measurement Circuit

2.2.1   Challenges

The capacitance change that needed to be measured was on the order of $1fF$ on a capacitor with a nominal capacitance of $500pF$. This is a change on the order of one part per 500,000. Using traditional methods of capacitance measurement is unable to measure at this required sensitivity.

2.2.1.1   Traditional Methods of Capacitance Measurement

There are many traditional methods for measuring capacitance which were explored as possible solutions and determined to be ineffective for solving this problem. To accurately measure these small changes in capacitance a circuit with a high sensitivity to capacitance change is required. Sensitivity is the measure of how much a output parameter changes due to a change of an input parameter. In general the equation for sensitivity is given in Equation 2.4 where x is the input parameter and y is the output parameter.

$$S = \lim_{\Delta x \to 0} \frac{\Delta y/y}{\Delta x/x} = \frac{x}{y}\frac{\partial y}{\partial x} \tag{2.4}$$

In the context of measuring the capacitance of the stain sensor where the base capacitance is C_0 and the change in capacitance is ΔC define the output parameter as the frequency at capacitance C is define as $f(C)$, then the sensitivity equation becomes the form shown in Equation 2.5 or Equation 2.6.

$$S \quad = \quad \lim_{\Delta C \to 0} \frac{(f(C_0 + \Delta C) - f(C_0)) C_0}{f(C_0)\Delta C} \tag{2.5}$$

$$= \quad \frac{C_0}{f(C_0)} \left[\frac{\partial f(C)}{\partial C} \right]_{C=C_0} \tag{2.6}$$

LCR oscillator circuits are circuits that oscillate at a frequency proportional to the capacitance of the sensor and fixed inductor value. The frequency of these circuits is proportional to $1/\sqrt{C}$. Given the sensitivity equation in Equation 2.6 the sensitivity of a LCR oscillator circuit can be calculated in Equation 2.8.

$$f \quad := \quad \frac{\alpha}{\sqrt{(C)}} \tag{2.7}$$

$$S \quad = \quad \frac{C_0 \sqrt{C_0}}{\alpha} \left[\frac{-\alpha}{2C\sqrt{C}} \right]_{C=C_0} = -1/2 \tag{2.8}$$

Therefore this circuit has a very small sensitivity to changes in capacitance is not a valid solution for the problem.

Charge time measurement is a method of measuring the capacitance of a circuit by measuring the time for that circuit to charge to a set amount. This method involves charging the capacitor with a constant current source and measuring the time to charge to one time constant. The voltage through a capacitor which is charging by a constant current source I is $V(t) = \frac{I}{C}t$. Therefore if the measured parameter is the time t to charge to a given voltage V the time to charge is given in Equation 2.9, then from Equation 2.4 the sensitivity is given in Equation 2.10

$$t = \frac{C}{I}V \tag{2.9}$$

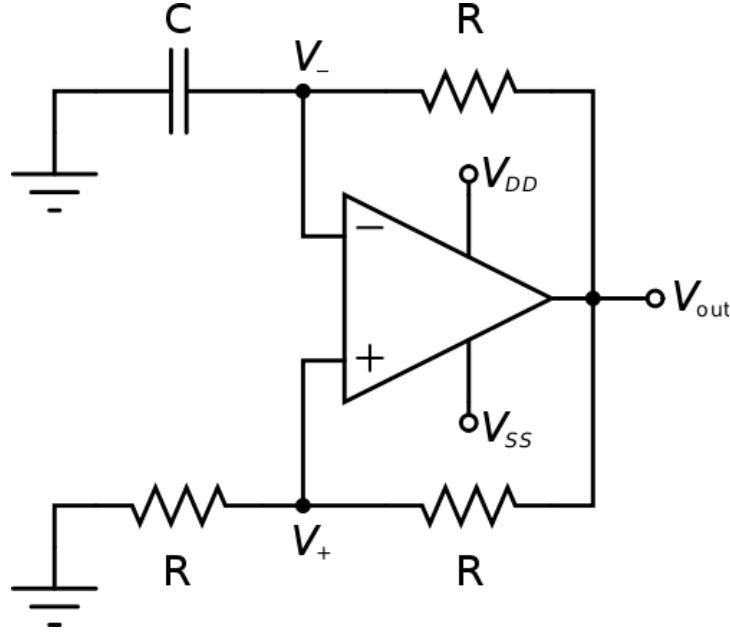$$S = \frac{C_0}{C_0 V/I} \frac{V}{I} = 1 \tag{2.10}$$

Figure 2.1   Simple relaxation oscillator circuit.

A relaxation oscillator is a simple type of oscillator which produces a square wave at a frequency proportional to the capacitor value. The circuit for a basic relaxation oscillator is shown in Figure 2.1. This circuit oscillates at a frequency f show in Equation 2.11.

$$f = \frac{1}{2\ln(3)RC} \tag{2.11}$$

Therefor the sensitivity can be calculated using Equation 2.6 and is shown in Equation 2.12.

$$S = \frac{C_0}{1/(2\ln(3)RC_0)} \frac{-1}{2\ln(3)RC_0^2} = -1 \tag{2.12}$$

2.2.2   Design

None of the traditional methods of capacitance measurements explored are capable of achieving the sensitivity required to accurately measure the small changes of capacitance produced by the strain sensor. For example, if the relaxation oscillator was used to measure a $1fF$ change in a $500pF$ capacitor assuming $R = 910\Omega$ yields the frequencies shown in Equations 2.13 and 2.14.

$$f_{\text{unstrained}} \quad = \quad \frac{1}{1820\Omega \ln(3)(500\text{pF})} = 1.0002628864\text{MHz} \tag{2.13}$$

$$f_{\text{strained}} \quad = \quad \frac{1}{1820\Omega \ln(3)(500\text{pF} + 1\text{fF})} = 1.00026088588\text{MHz} \tag{2.14}$$

This corresponds to a change in frequency of approximately 2Hz; an effective change of two parts per million. Clearly it is impractical to measure this value in a real system. A system had to be designed to greatly increase the sensitivity of the data acquisition circuit to the expected small changes in capacitance.

The problem with the other capacitance measurement techniques is the relatively large nominal capacitance of the skin sensor to its small change in capacitance due to an applied strain. The approximate expected nominal capacitance is 500pF with a change of capacitance on the order of 1fF; this is a change of one part per five-hundred thousand. The overall range of capacitance change expected is relatively small in comparison to the nominal capacitance; in the case of a sensor measuring 500 micro-strain can expect a capacitance change of 500fF or 0.5pF. Define the variable C_{max} as this maximum expected capacitance change. The solution is therefore to cancel out the nominal capacitance such that the only capacitance contributing to the measured frequency is the capacitance due to strain.

The equivalent capacitance of capacitors in parallel is the sum of the capacitance of each capacitor. Therefore connecting the strain sensor in parallel with a capacitor of C shown in Equation 2.15 will yield an equivalent capacitance of C_1. By designing the system such that $C_1 = C_{max} + \epsilon$ the capacitance over the range of ± 500 micro-strain will be $\epsilon \leq C_{eq} \leq C_{max} + \epsilon$. This capacitance C_{eq} can be used as an input to one of the traditional capacitance measurement techniques.

$$C = C_1 - C_0 \tag{2.15}$$

The relaxation oscillator circuit was chosen due to its higher sensitivity, relatively cheaper cost, and lower energy requirements compared to the other measurement techniques.
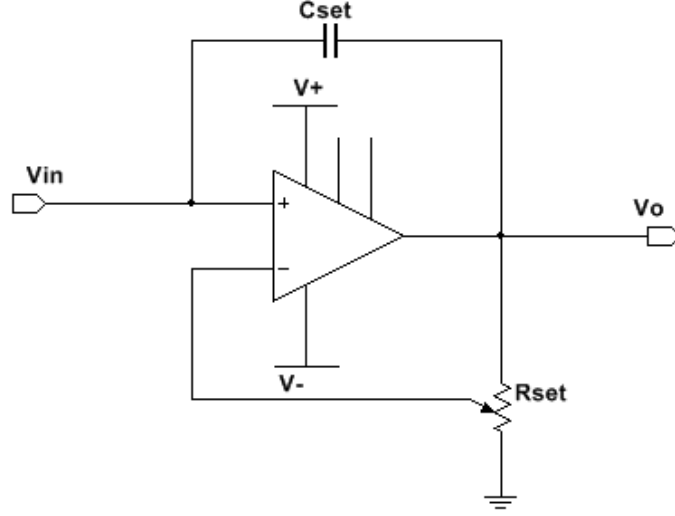
Figure 2.2   Negative capacitance circuit schematic

2.2.2.1   Negative capacitive circuit

The basic negative capacitor circuit used for this project is shown in Figure 2.2. This is a well established and common configuration used to create negative impedance circuits. It has many useful features including the ease of setting the impedance by changing the value of a single potentiometer.

The negative feedback   circuit forms a basic voltage divider circuit between the Op-Amp output and ground. Therefore the Op-Amp output voltage V_o is given in Equation 2.16 where $x \in [0, 1]$ is the position of the potentiometer.

$$V_o = \frac{1}{x}V_{in} \geq V_{in} \tag{2.16}$$

The positive feedback   circuit has an impedance $Z_{set} = \frac{1}{sC_{set}}$ where s is the Laplace operator. The input current I_{in} can be calculated using Equation 2.17. This represents the current sunk or sourced at the input to the negative impedance circuit.

$$I_{in} = \frac{V_{in} - V_o}{Z_{set}} \tag{2.17}$$

The circuit impedance of the negative impedance circuit is given by Ohms law as $V_{in} = ZI_{in}$. Using the calculated V_o from the negative feedback circuit and using that into the positive feedback circuit calculation the impedance of the circuit is calculated in Equation 2.20. Using the fact that $Z = \frac{1}{sC_{set}}$ the equivalent capacitance of the circuit is shown in Equation 2.21.

$$I_{in} = \frac{V_{in} - \frac{1}{x}V_{in}}{Z_{set}} \tag{2.18}$$

$$= \frac{\frac{x-1}{x}V_{in}}{Z_{set}} \tag{2.19}$$

$$\Downarrow$$

$$Z_{eq} = \frac{V_{in}}{I_{in}} = \frac{x}{x-1}Z_{set} \leq 0 \tag{2.20}$$

$$Z_{eq} = \frac{1}{sC_{eq}} = \frac{x}{sC_{set}(x-2)} \Rightarrow C_{eq} = \frac{x-1}{x}C_{set} \tag{2.21}$$

In the implementation the potentiometer is digitally controlled to allow real time calibration of the system. The potentiometer chosen was the CAT5259 manufactured by ON Semiconductors. This potentiometer divides the range linearly into eight bits of resolution. Therefore the x in Equation 2.21 must be chosen such that $x \in \{\frac{k}{2^8} : k \in \mathbb{N}, k < 2^8\}$. Therefore the equivalent capacitance of the circuit in terms of the integer value $k \in [0, 256)$ is shown in Equation 2.23.

$$C_{eq} = \frac{x-1}{x}C_{set} \tag{2.22}$$

$$\frac{C_{eq}}{C_{set}} = \frac{k-2^8}{k} \tag{2.23}$$

This capacitance is plotted over the range of $k \in [23, 255]$ and $k \in [85, 255]$ in Figures 2.3 and 2.4 respectively. By choosing $C_{set} \approx C_0$ of the sensor the negative capacitor circuit can stay within the nearly linear region shown in Figure 2.4.

2.2.2.2 Relaxation oscillator

The same relaxation circuit shown in Figure 2.1 is used to convert the change in capacitance into a change in frequency with the one key change: the capacitor is the parallel combination
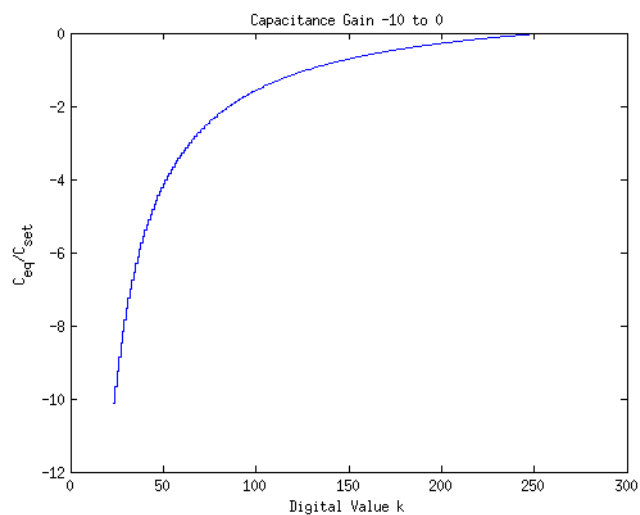
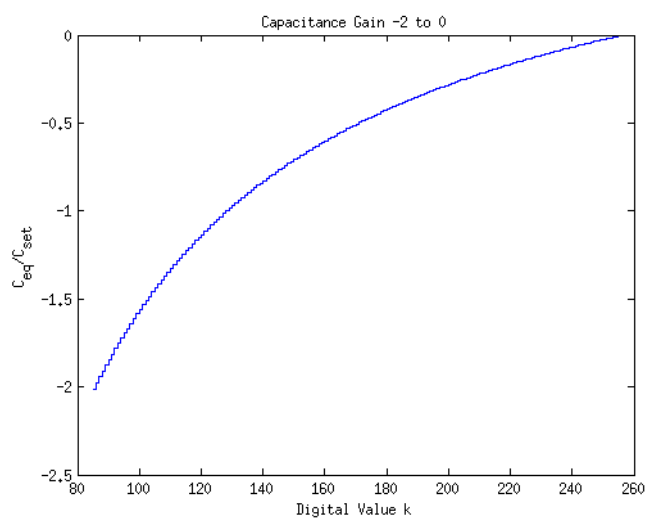Figure 2.3   Negative capacitance gain from -10 to 0

Figure 2.4   Negative capacitance gain from -2 to 0

of the sensor C_{sens} and the negative capacitance C_1. The equivalent capacitance of this parallel combination is the sum of both capacitors, $C = C_{sens} + C_1$ where C_1 is the equivalent capacitance of the negative capacitance circuit calculated in Equation 2.22.

Assuming all resistors R in the relaxation oscillator are equal the frequency output of the oscillator is repeated in Equation 2.24. Using the new value for the capacitance input of the oscillator yields a new frequency equation shown in Equation 2.25. Combining the definition of C_1 from Equation 2.22 the equation shown in Equation 2.26 can be obtained for the frequency output in terms of the C_{sens} and digital value k.

$$f(C) \quad = \quad \frac{1}{2\ln(3)RC} \tag{2.24}$$

$$f(C_{sens}, C_1) \quad = \quad \frac{1}{2\ln(3)R(C_{sens} + C_1)} \tag{2.25}$$

$$f(C_{sens}, k) \quad = \quad \frac{1}{2\ln(3)R(C_{sens} + \frac{k-256}{k}C_{set})} \tag{2.26}$$

From the model of the capacitive skin sensor, the value of C_{sens} has two components: the first component is the unstrained base capacitance and the second is the change in capacitance due to strain. Therefore by writing C_{sens} in the form of Equation 2.27 the frequency equation in Equation 2.26 becomes Equation 2.28.

$$C_{sens} \quad = \quad C_0 + \Delta C \tag{2.27}$$

$$f(\Delta C, k) \quad = \quad \frac{1}{2\ln(3)R(C_0 + \frac{k-256}{k}C_{set} + \Delta C)} \tag{2.28}$$

The sensitivity of this new capacitive measurement system can be calculated using the definition of sensitivity given in Equation 2.6 and the frequency equation given in Equation 2.26. First the derivative of the frequency with respect to the capacitance of the sensor is calculated in Equation 2.30. Form this the sensitivity can be calculated in Equation 2.33. Including the definition of C_{sens} given in Equation 2.27 gives the resulting sensitivity in Equation 2.35.

$$\frac{\partial f(C_{sens})}{\partial C_{sens}} = \frac{\partial}{\partial C_{sens}}\left[\frac{1}{2\ln(3)R(C_{sens}+\frac{k-256}{k}C_{set})}\right] \tag{2.29}$$

$$= \frac{1}{2\ln(3)R(C_{sens}+\frac{k-256}{k}C_{set})^2} \tag{2.30}$$

$$S = \frac{C_{sens}}{f(C_{sens})}\frac{\partial f(C_{sens})}{\partial C_{sens}} \tag{2.31}$$

$$= \frac{C_{sens}(2\ln(3)R(C_sens+\frac{k-256}{k}C_{set})}{2\ln(3)R(C_{sens}+\frac{k-256}{k}C_{set})^2} \tag{2.32}$$

$$= \frac{C_{sens}}{C_{sens}+\frac{k-256}{k}C_{set}} \tag{2.33}$$

$$= \frac{C_0+\Delta C}{C_0+\frac{k-256}{k}C_{set}+\Delta C} \tag{2.34}$$

$$= \frac{C_0}{C_0+\frac{k-256}{k}C_{set}+\Delta C} + \frac{\Delta C}{C_0+\frac{k-256}{k}C_{set}+\Delta C} \tag{2.35}$$

Define a new variable α as given in Equation 2.36. By choosing k and C_{set} such that α is small the sensitivity becomes very large reaching a theoretical maximum of $C_0/\Delta C + 1$ when $\alpha = 0$.

$$\alpha := C_0 + \frac{k-256}{k}C_{set} \tag{2.36}$$

$$S = \frac{C_0}{\alpha+\Delta C} + \frac{\Delta C}{\alpha+\Delta C} \tag{2.37}$$

Parameter selection is based on the design requirements. The design requirements specify the need to measure $1\mu\epsilon$ resolution over a range of $\pm500\mu\epsilon$ from unstrained. Define ΔC_ϵ to be the change in capacitance of the sensor due to a $1\mu\epsilon$ strain, then the maximum and minimum values of ΔC are $\Delta C_{max} = 500\Delta C_\epsilon$ and $\Delta C_{min} = -500\Delta C_\epsilon$ respectively.

To achieve maximum sensitivity α must be chosen as small as possible; however, if the denominator of Equation 2.28 becomes negative the system poles of the system will move into the right half plan causing the circuit to become unstable. Therefore the constrain equation shown in Equation 2.38 must be true for all ΔC in the range.

$$C_0 + \frac{k - 256}{k} C_{set} + \Delta C = \alpha + \Delta C \quad > \quad 0, \quad \forall \quad \Delta C \in [\Delta C_{min}, \Delta C_{max}] \qquad (2.38)$$

$$\alpha \quad > \quad -C_{min} \qquad (2.39)$$

Using this value of α the values of C_{set} and k can be set. Choose $C_{set} \approx 2C_0$ to force k into a sensitive and approximately linear region in Figure 2.4. Since the nominal capacitance of the capacitive skin sensors is approximately 500pF choose C_{set} =1nF. With this value k can be calculated to be 171 giving making $\alpha = 2.9 \times 10^{-12}$. Plugging these numbers into Equation 2.37 gives a theoretical sensitivity of 3.4×10^{11}. It should be noted that this theoretical value could never be achieved in a real system; however, this shows the high sensitivity potential of this circuit.

The capacitive sensor is characterized for oscillation frequencies under one kilohertz.

2.2.3 Output

The output form the data acquisition circuit is a square wave which has a frequency dependent on the strain of the sensor. The frequency of the square wave is depended on the capacitor C_{set}, the set potentiometer value, and the resistor value R of the oscillator. The higher this frequency is for a given value of R the higher the overall sensitivity of the system; however, there is a physical limit of sampling frequency for the capacitive sensor that the frequency must remain under.

2.3 Data Acquisition Circuit

The overall data acquisition includes the capacitive measurement circuit, calibration circuit, and USB interface. This circuit samples the strain of the capacitive sensor using the capacitive measurement circuit and measures the frequency of the of the capacitive measurement circuit. It will do some preprocessing of the data and then send it to the computer over a USB connection. This circuit is also responsible for calibrating the unstrained frequency of the system at start up and when requested by the computer. A microcontroller is used to connect all the subparts of
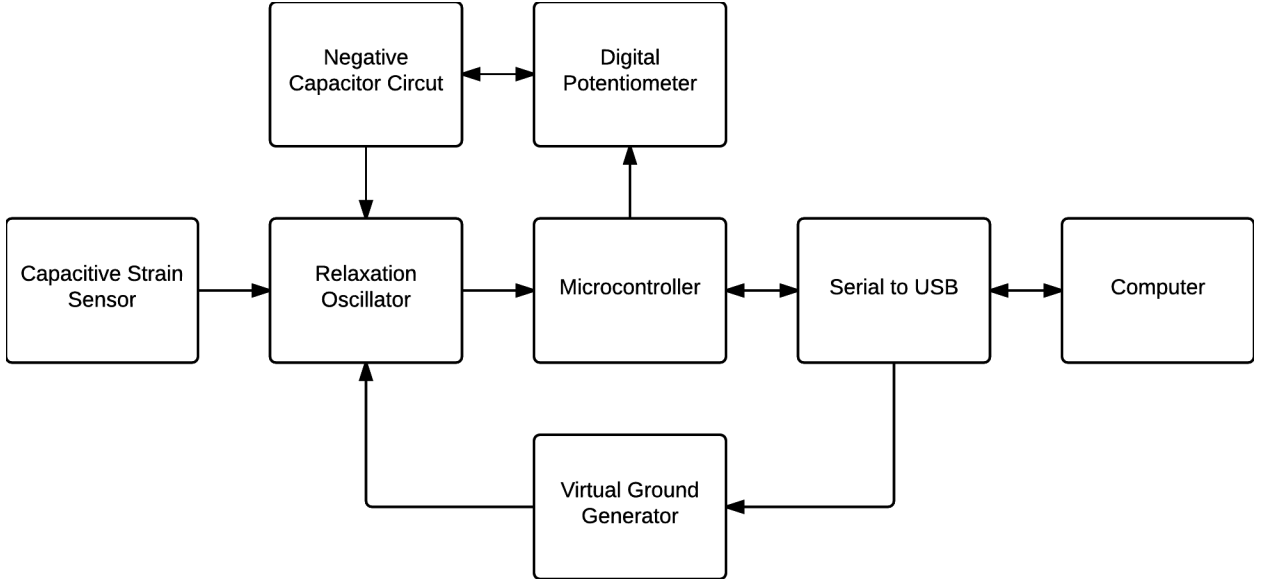
Figure 2.5 Data acquisition system diagram

TODO: Need to change the passive values on the digital schematic and re-export since we
re-tuned for the lower frequency.

Figure 2.6 System schematic

the circuit together and control the entire system. This is shown in the overall system diagram
in Figure 2.5. This system was implemented with the schematic shown in 2.6.

2.3.1 Measurement Method

The measurement method is the aforementioned relaxation oscillator in conjunction with
the negative capacitance circuit. The capacitive strain sensor is connected directly to the
relaxation oscillator circuit along with the negative capacitor circuit. The potentiometer of
negative capacitor circuit is provided by the digital potentiometer which is used for calibration
of the system.

The relaxation oscillator and negative capacitor circuits require positive and negative supply
voltages to operate properly. The system is powered from a single voltage source, and therefore
these operational amplifier bias voltages must be created. The system ground is chosen as the

negative bias voltage and the system V_{DD} is chosen as the positive bias voltage. A virtual ground is created at the midpoint voltage between V_{DD} and ground, and it is this virtual ground that is referenced for the analogue portions of the circuit.

2.3.2 Calibration

The calibration is preformed using the digital potentiometer and the negative capacitor circuit. The output frequency of the relaxation oscillator is selected by the parameters C_{set}, R, and the digital potentiometer set value. The highest sensitivity for a given strain is achieved by selecting R as large as physically possible and C_{set} as close to the value of C_0 of the circuit as possible. Therefore after these two values are set an unstrained frequency is selected to be as high as physically possible without degrading the physics of the capacitor to maximize sampling rate. The value of the potentiometer is changed until the measured frequency matches the selected set point frequency.

2.3.3 Communication protocol

The communication with the computer is over USB and connected as a virtual serial port. The data transmitted from the data acquisition circuit to the computer is an ASCII formated string. The format is "`time, value\n\r`" where `time` is a floating point time stamp in seconds and `value` is the raw measured value at that time. The `\n` is the linefeed character (ASCII 10) and `\r` is the carriage return character (ASCII 13). The value `time` starts at zero when the system is powered on and counts up at a resolution of 100 milliseconds. The time stamp corresponds to the system time when the sampling process finished. The `value` parameter is a raw value and can be converted into frequency using Equation 2.40 where β is a scaling factor and is 32MHz on the current system.

$$f_{sens} = \frac{\beta}{\texttt{value}} \tag{2.40}$$

The strain of the sensor can be calculated from `value` as well. Define the received raw value as V and the set point raw value as V_0. From Equations 2.28 and 2.40 Equation 2.44 can be found.

$$f(\Delta C, k) = \frac{\beta}{V} \quad = \quad \frac{1}{2\ln(3)R(C_0 + \frac{k-256}{k}C_{set} + \Delta C)} \tag{2.41}$$

$$V \quad = \quad 2\beta\ln(3)R\left(C_0 + \frac{k-256}{k}C_{set}\right) + 2\beta\ln(3)R\Delta C \tag{2.42}$$

$$V \quad = \quad V_0 + 2\beta\ln(3)R\Delta C \tag{2.43}$$

$$\Delta C \quad = \quad \frac{V - V_0}{2\beta\ln(3)R} \tag{2.44}$$

2.3.4 Microcontroller

The microcontroller unit (MCU) selected for the data acquisition system was a PIC18LF2520. This MCU was selected for its low power requirements, large amount of RAM, relatively high clock speed, and the well documented programming API. The MCU also offered the peripherals needed for the system including input capture, serial peripheral interface (SPI), and universal asynchronous transceiver (UART).

2.3.4.1 Frequency Measurement

The frequency of the capacitive strain sensor is measured using the input capture module of the MCU. This module measures the time between events on a input pin. The event used in this system was every sixteenth rising edge of the frequency signal. By measuring the time between the first and sixteenth rising edge the average frequency of the signal over that time interval can be measured.

The time measurement of the current system is the system clock of 8MHz divided by four. This gives a timer frequency of 2MHz or a minimum measurement resolution of 500ns. — PLACEHOLDER: add the calculation that this equates to 15fF resolution at 1kHz—. An option is being currently explored to increase the system clock to 32MHz yeilding a time resolution of 125ns which greatly exceeds the 1fF requirement —and this equation too—.

The current system measures the average frequency averaged over sixteen pulses. Assuming a nominal frequency of 1kHz this equates to a minimum of 16ms per sample and more when frequency is higher. This is unable to meet the requirement of a 100Hz sample rate. Options are being explored including changing the window from sixteen pulses down to eight pulses.

This would cut the per measurement sample time in half at the expense of also cutting the sensitivity in half.

2.3.4.2 Calibration Potentiometer

The digital potentiometer used to calibrate the frequency communicates with the MCU over a serial peripheral interface (SPI). The bus is used to serially set the position of the potentiometer. It is also used to program the default potentiometer value into non-volatile memory inside the digital potentiometer.

2.3.4.3 Communication

Communication with the connected computer uses the universal asynchronous receiver and transmitter (UART). The UART signal is connected to an FTDI UART to USB converter. This converter connects to the computer and shows up as a virtual transport which can be written to and read from as if the circuit was plugged directly into the computers RS232 communications port.

2.3.5 Software

The software for the data acquisition system is relatively simple. After a power-on-reset the MCU initializes the oscillator and other peripherals and then runs self calibration. The self calibration sets the digital potentiometer to the midpoint value and measures the frequency. It then increases or decreases as necessary and takes another frequency measurement. This is repeated until the measured frequency matches the setpoint frequency within some tolerance. The system then starts a system tick clock which interrupts every 250ms—Note to self: This will probably change to ever 100ms after dropping the samples down to 8 and getting the PLL to work—. On each interrupt of this system clock a new frequency measurement is taken and that frequency along with the time it was taken is returned to the computer over the UART to USB connection. This process is repeated indefinitely until the user stops it.

The system receives commands over the UART. Commands are delimited by line feed characters (ASCII 10) at which time the command is deciphered asynchronously in an interrupt

routine. Currently three types of commands are supported. The first command is a number between 0 and 255 inclusive. This command is used as a new digital potentiometer value and applied immediately. The second command is an ASCII 'c' character followed by a number. The number following the 'c' is used as a new frequency set point value and the system is immediately recalibrate to that new frequency. The frequency is as a raw value which corresponds to a real frequency using Equation 2.40. The final command currently supported is the ASCII character 'r' which causes the system to do a hard reset. This will recalibrate the frequency to its power on default and restart the time stamp count at zero.

CHAPTER 3.   RESULTS

— This still needs to be filled in. Initial findings are at 2MHz the capacitor no longer behaves as a cap. Hopefully get new results from Toby early next week.
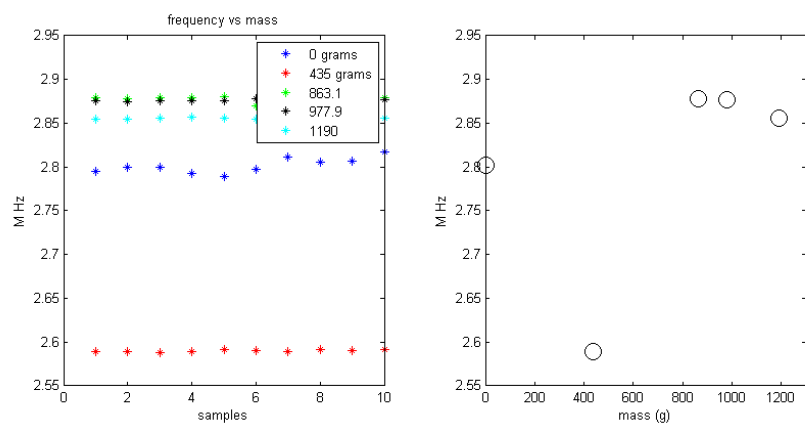
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Figure 3.1   High frequency strain to frequency

CHAPTER 4.   SUMMARY AND DISCUSSION

I will do this after I get the results from Toby.