Electrical Engineering

4-Bit Calculator

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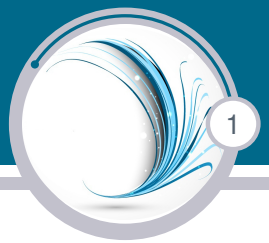
Introduction



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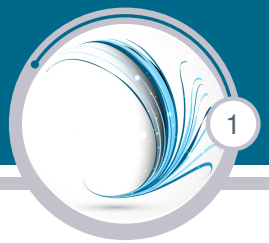

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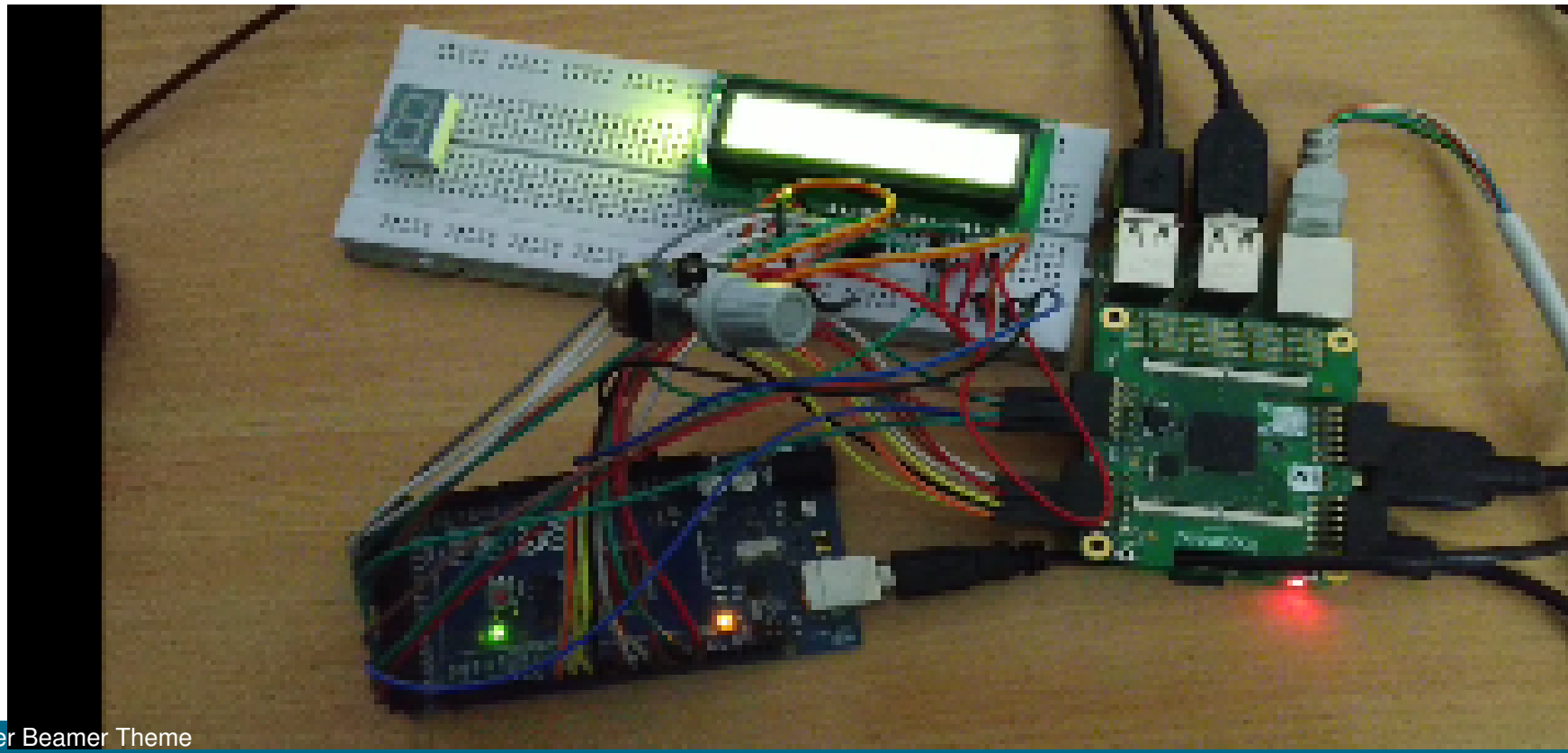
- The icoboard contains a Lattice FPGA with 8k LUT, 100MHz max clock, up to 8 MBit of SRAM and is pro-
  grammable in Verilog by a complete open source FPGA toolchain.



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- And then inputs are given to the icoboard pins from the arduino in the binary format.

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- The decimal number is then displayed on the LCD display.