1.3.1 Programs are Data

1.

|  |  |  |
| --- | --- | --- |
| **Current Instruction in Binary** | **Current Instruction Separated** | **Current Instruction Written with Mnemonic** |
| 01001011 | |  |  | | --- | --- | | **OP Code Bits** | **Data Bits** | | 11 | 010010 | | REA 010101 |

|  |  |
| --- | --- |
| **Register 1** | **Register 2** |
| 01001011 | 01001011 |

|  |  |  |
| --- | --- | --- |
| **Current Instruction in Binary** | **Current Instruction Separated** | **Current Instruction Written with Mnemonic** |
| 00000011 | |  |  | | --- | --- | | **OP Code Bits** | **Data Bits** | | 11 | 000000 | | REA 00000011 |

|  |  |
| --- | --- |
| **Register 1** | **Register 2** |
| 01001011 | 00000011 |

3.

|  |  |
| --- | --- |
| **Register 1** | **Register 2** |
| 00000000 | 00000000 |

**Instruction 1**

|  |  |  |
| --- | --- | --- |
| **Current Instruction in Binary** | **Current Instruction Separated** | **Current Instruction Written with Mnemonic** |
| 00000000 | |  |  | | --- | --- | | **OP Code Bits** | **Data Bits** | | 00 | 000000 | | REA 00000000 |

**Registers after Instruction 1**

|  |  |
| --- | --- |
| **Register 1** | **Register 2** |
| 00000000 | 00000000 |

**Instruction 2**

|  |  |  |
| --- | --- | --- |
| **Current Instruction in Binary** | **Current Instruction Separated** | **Current Instruction Written with Mnemonic** |
| 11111110 | |  |  | | --- | --- | | **OP Code Bits** | **Data Bits** | | 10 | 111111 | | STO 00111111 |