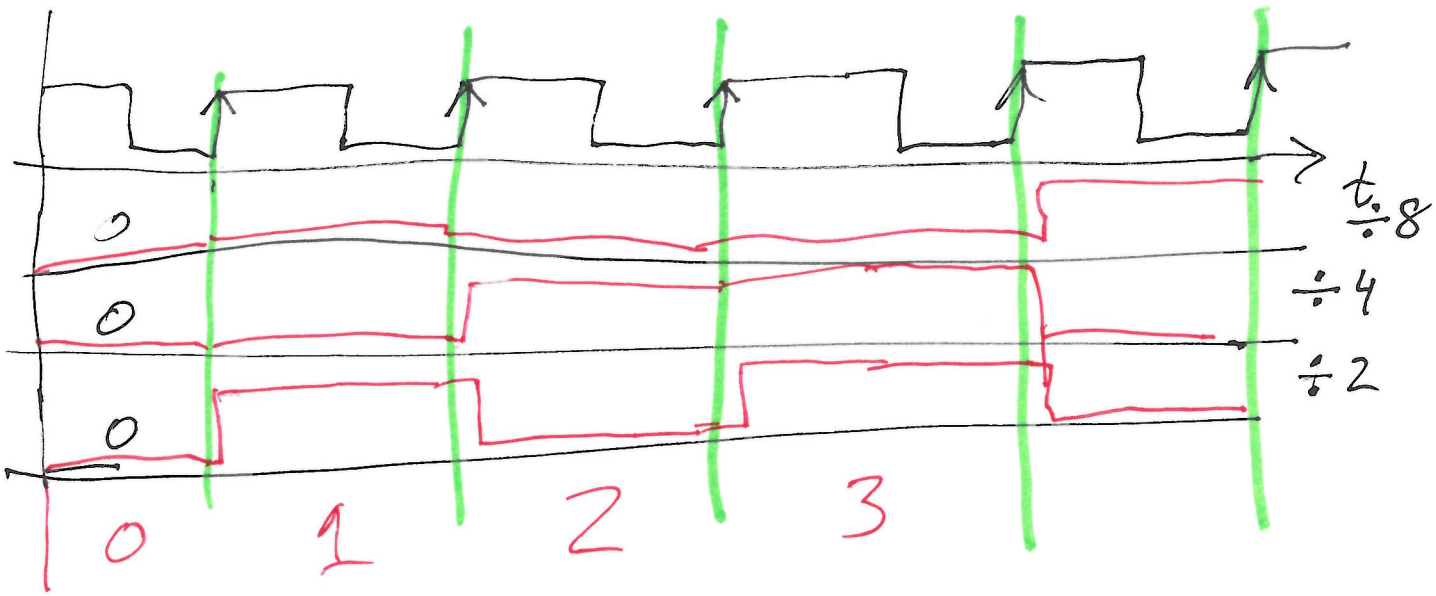
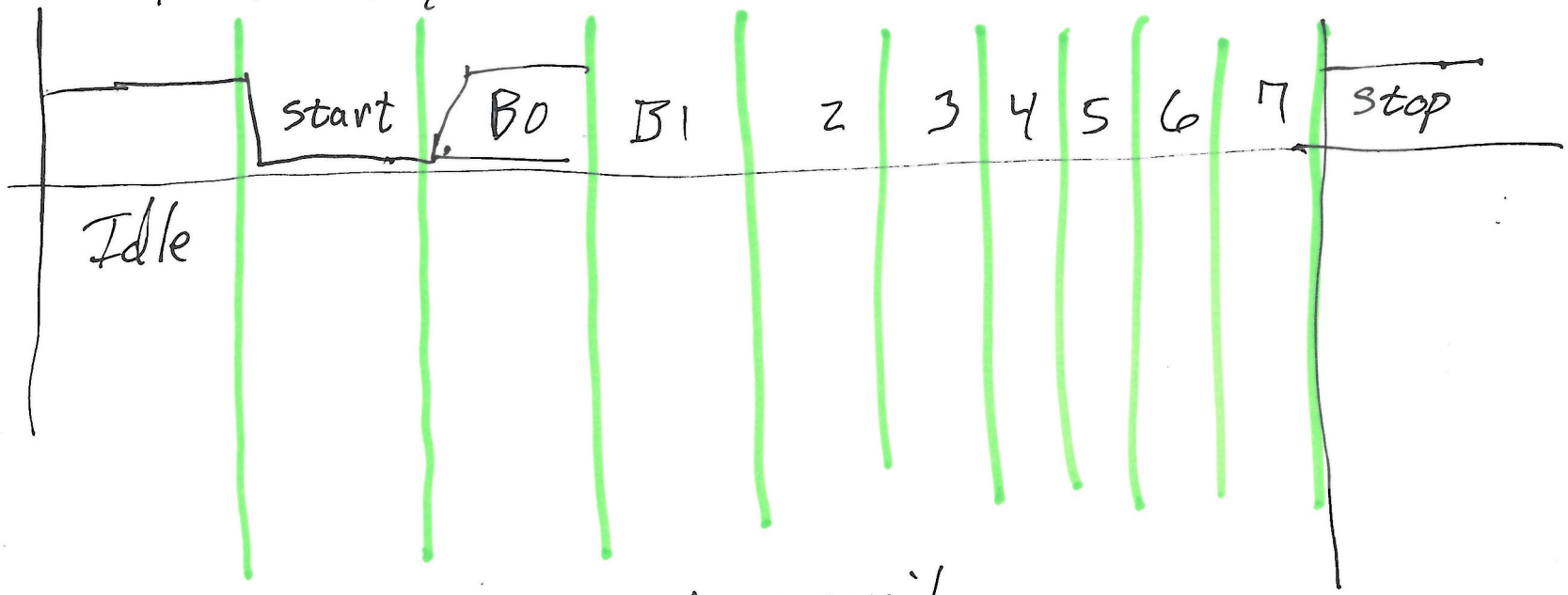
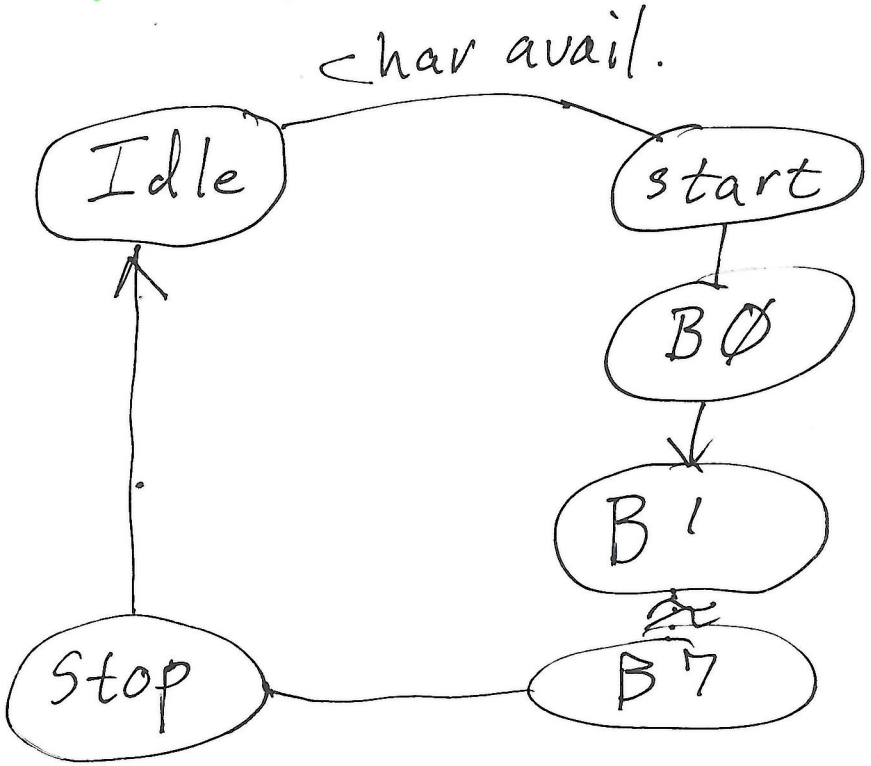
Timer 1 is 16 bits (65536)



$$\frac{65536 \text{ cycle}}{16\text{MHz}\left(\frac{\text{cycle}}{\text{sec}}\right)} = 4.096 \text{ millisec}$$

Interrupt Handler

1) Processor Stops
2) Records location & Vaviable
3) Jump to ISR
4) No arguement/return → void ISR(void)
5) Simple & Fast
6) → Resets to where it was

Transmit & Serial (9600 Baud → 104 μ)