

The diagram illustrates the timing of various signals across three modes: Nominal, Long6, and Long7. The signals are:

- clk**: A continuous clock signal represented by a series of upward arrows.
- d\_plus**: A signal that transitions from low to high and back to low, with a small dip at the end of each cycle.
- d\_edge**: A signal that transitions from low to high and back to low, with a small dip at the end of each cycle.
- d\_orig**: A signal that transitions from high to low and back to high, with a small dip at the end of each cycle.
- shift\_enable**: A signal that transitions from low to high and back to low, with a small dip at the end of each cycle.
- timer**: A sequence of hexagonal cells containing numbers 0-7. The sequence is: 2 (orange), 3, 4, 5, 6, 7, 0 (blue), 1, 2, 3, 4, 5, 6, 7, 0 (blue), 1, 2, 3, 4, 5, 6, 7, 0 (blue), 1, 2, 3, 4, 5, 6, 7, 0 (blue). The first three cells (2, 3, 4) are hatched.

The Long7 mode shows a greyed-out section at the end of the timer sequence, indicating a transition to a new state.