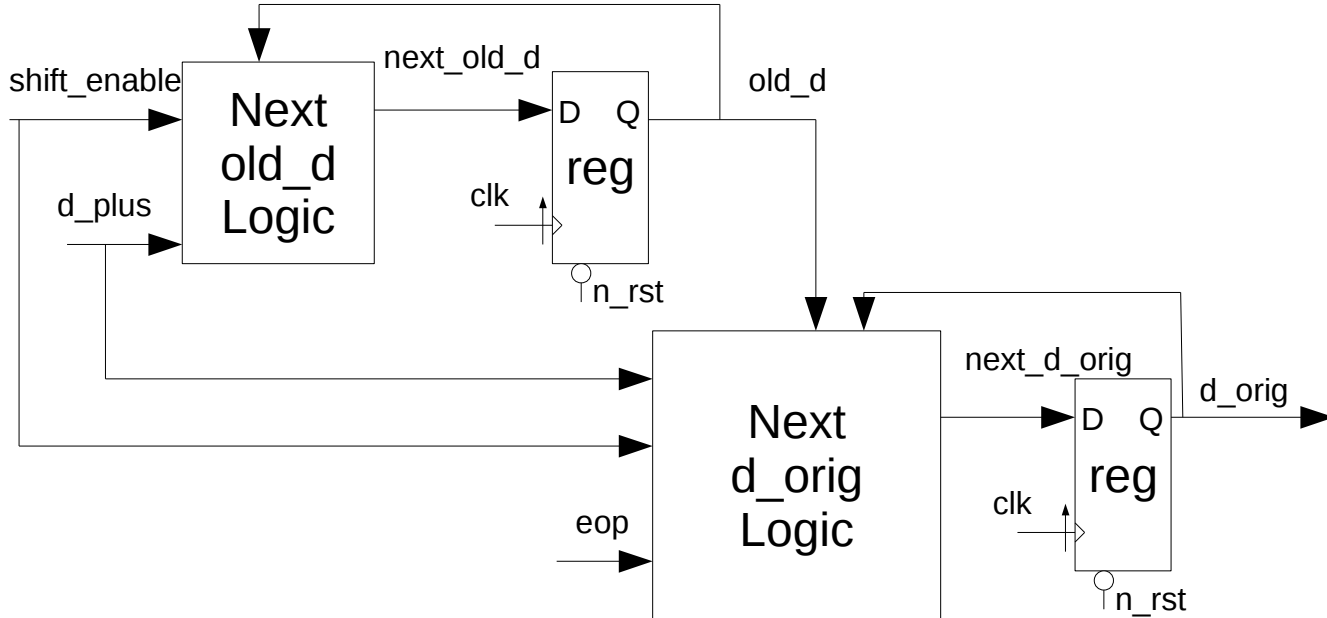


# Schematic Diagram for Decode Block

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ECE 337  
Section 4  
Lab 6



Next old\_d Logic:

Multiplex between `old_d` and `d_plus` using `shift_enable`. If `shift_enable == 1`, `next_old_d = d_plus`, else `next_old_d = old_d`.

Next d\_orig Logic:

If `shift_enable == 1 && eop == 1`, `next_d_orig = 1`, else if `shift_enable == 1`, `next_d_orig = ~(old_d ^ d_plus)`, else `next_d_orig = d_orig`