Project
CS 388

Due date December 3 (Firm Deadline)

Class is expected to form several super-groups. Each super-group is a collection of two existing groups, one acting as the lead group. The lead group is in charge of synchronizing activities among the sub groups and writing the final report. The two sub groups will simulate CDC6600 and CDC7600 in executing target programs and report the performance (in terms of clock pulses). Simulators will be in C or C^{++}.

Subgroup:

Simulators simulate the instruction pipe, scoreboard, delivery of instructions to the functional units, and execution of the instructions in the processor. Each simulator should generate a time table (time chart) (similar to the tables in class notes) in terms of the number of clock pulses showing the time progress of the instructions in the programs.

Group Leader:

Group leader is acting as a moderator between the subgroups and should act as a communication bridge between the subgroups (uniformity of reports, language, and …). He/she will combine the reports generated by each subgroup and add a section comparing and contrasting the results against each other. .

1) The test data will be made available on the course web site on November 19.

2) The due date of the project is firm and will not change under any circumstances.

3) Each super-group is expected to hand in the hard copy of the report along with the print out of simulators, in class. Simulators are expected to be well documented.

Please note that randomly some super-groups may be asked to demo their simulators and run it for some input data.