

1. Description

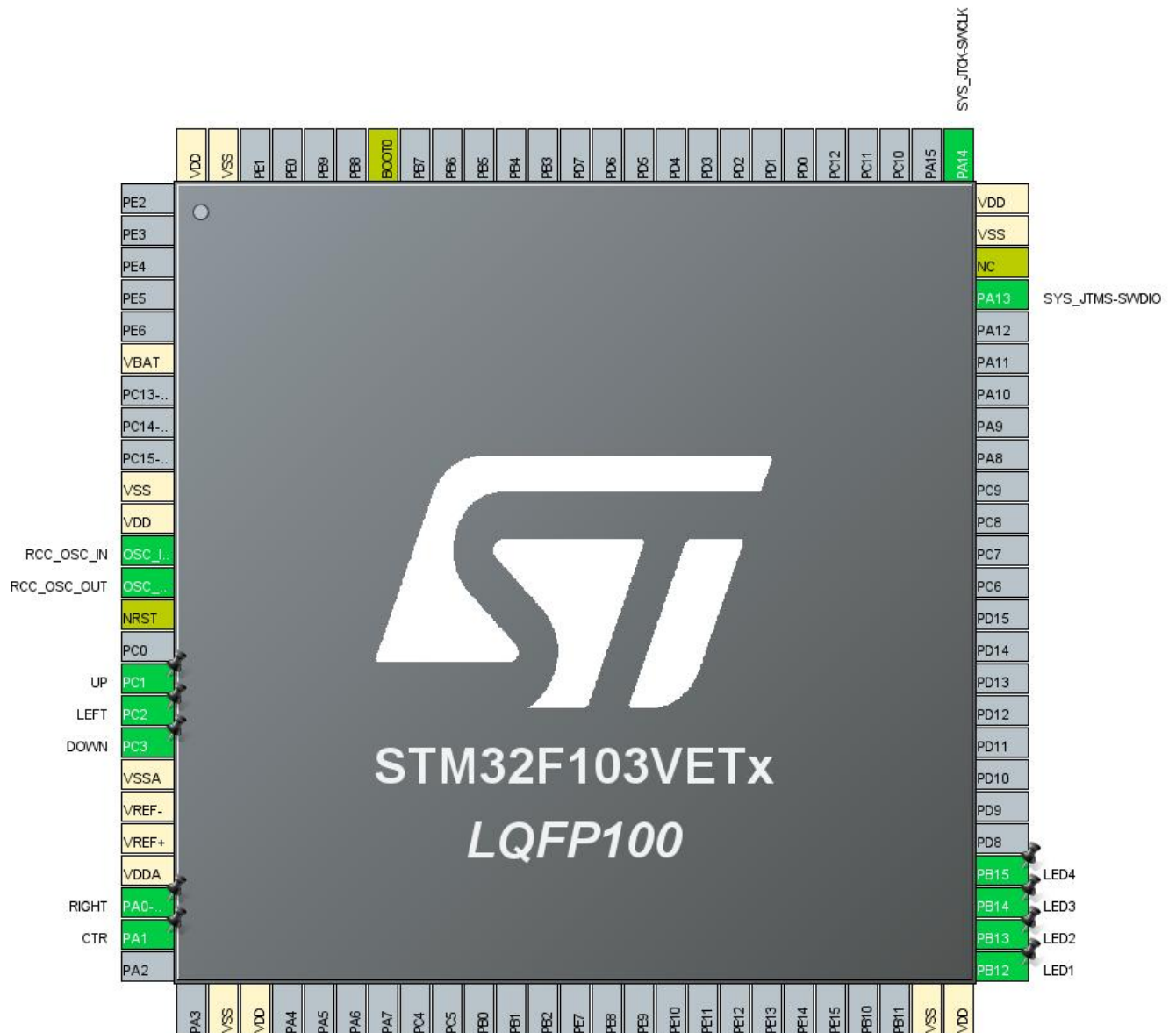
1.1. Project

Project Name	KEY
Board Name	KEY
Generated with:	STM32CubeMX 5.6.1
Date	07/21/2020

1.2. MCU

MCU Series	STM32F1
MCU Line	STM32F103
MCU name	STM32F103VETx
MCU Package	LQFP100
MCU Pin number	100

2. Pinout Configuration

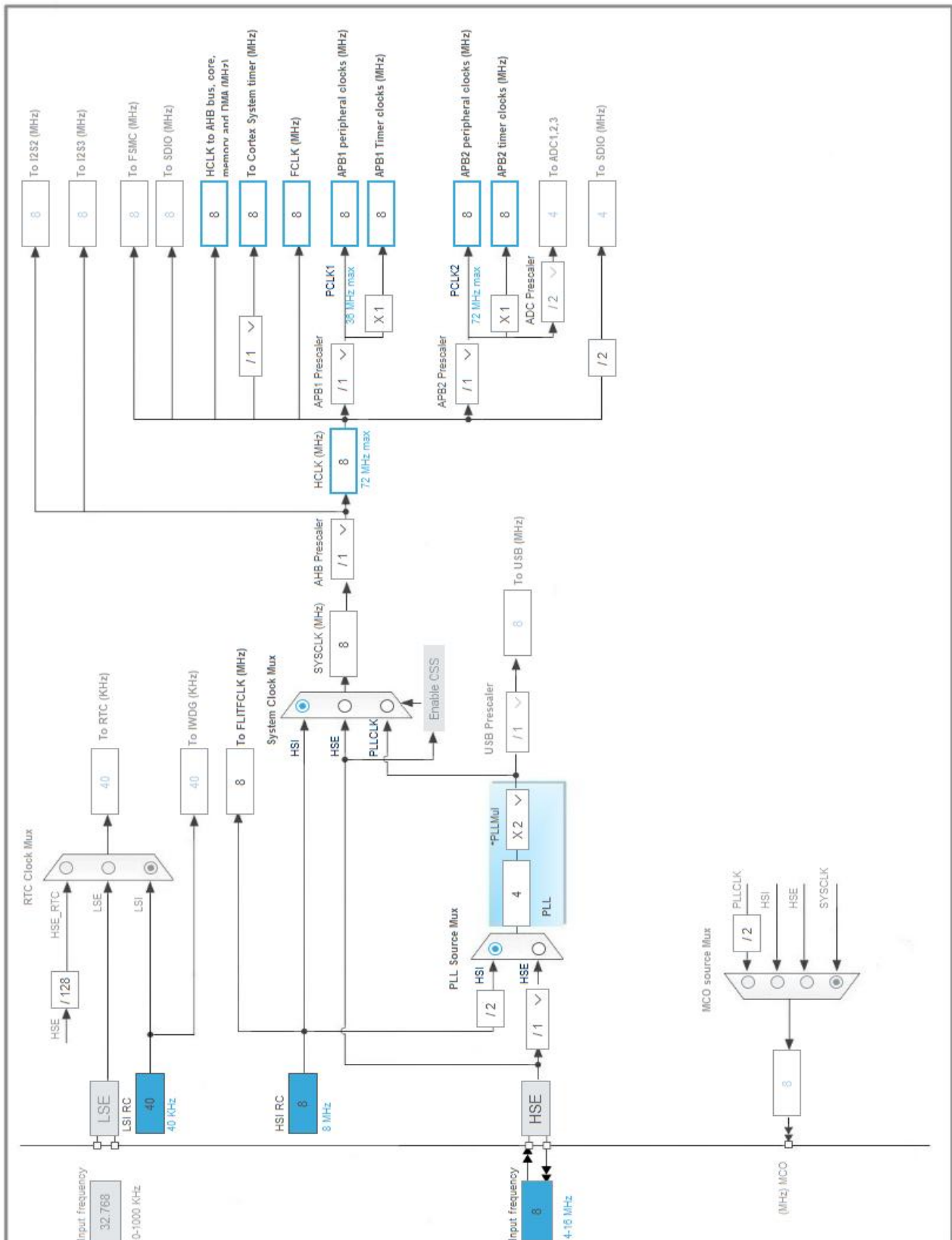


3. Pins Configuration

Pin Number LQFP100	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
6	VBAT	Power		
10	VSS	Power		
11	VDD	Power		
12	OSC_IN	I/O	RCC_OSC_IN	
13	OSC_OUT	I/O	RCC_OSC_OUT	
14	NRST	Reset		
16	PC1 *	I/O	GPIO_Input	UP
17	PC2 *	I/O	GPIO_Input	LEFT
18	PC3 *	I/O	GPIO_Input	DOWN
19	VSSA	Power		
20	VREF-	Power		
21	VREF+	Power		
22	VDDA	Power		
23	PA0-WKUP *	I/O	GPIO_Input	RIGHT
24	PA1 *	I/O	GPIO_Input	CTR
27	VSS	Power		
28	VDD	Power		
49	VSS	Power		
50	VDD	Power		
51	PB12 *	I/O	GPIO_Output	LED1
52	PB13 *	I/O	GPIO_Output	LED2
53	PB14 *	I/O	GPIO_Output	LED3
54	PB15 *	I/O	GPIO_Output	LED4
72	PA13	I/O	SYS_JTMS-SWDIO	
73	NC	NC		
74	VSS	Power		
75	VDD	Power		
76	PA14	I/O	SYS_JTCK-SWCLK	
94	BOOT0	Boot		
99	VSS	Power		
100	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. Software Project

5.1. Project Settings

Name	Value
Project Name	KEY
Project Folder	D:\STM32CubeIDE\workspace\KEY
Toolchain / IDE	STM32CubeIDE
Firmware Package Name and Version	STM32Cube FW_F1 V1.3.1

5.2. Code Generation Settings

Name	Value
STM32Cube MCU packages and embedded software	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	Yes
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No

6. IPs and Middleware Configuration

6.1. GPIO

6.2. RCC

High Speed Clock (HSE): Crystal/Ceramic Resonator

6.2.1. Parameter Settings:

System Parameters:

VDD voltage (V)	3.3
Prefetch Buffer	Enabled
Flash Latency(WS)	0 WS (1 CPU cycle)

RCC Parameters:

HSI Calibration Value	16
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6.3. SYS

Debug: Serial-Wire

Timebase Source: SysTick

* User modified value

7. System Configuration

7.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
RCC	OSC_IN	RCC_OSC_IN	n/a	n/a	n/a	
	OSC_OUT	RCC_OSC_OUT	n/a	n/a	n/a	
SYS	PA13	SYS_JTMS-SWDIO	n/a	n/a	n/a	
	PA14	SYS_JTCK-SWCLK	n/a	n/a	n/a	
GPIO	PC1	GPIO_Input	Input mode	Pull-up *	n/a	UP
	PC2	GPIO_Input	Input mode	Pull-up *	n/a	LEFT
	PC3	GPIO_Input	Input mode	Pull-up *	n/a	DOWN
	PA0-WKUP	GPIO_Input	Input mode	Pull-up *	n/a	RIGHT
	PA1	GPIO_Input	Input mode	Pull-up *	n/a	CTR
	PB12	GPIO_Output	Output Push Pull	n/a	Low	LED1
	PB13	GPIO_Output	Output Push Pull	n/a	Low	LED2
	PB14	GPIO_Output	Output Push Pull	n/a	Low	LED3
	PB15	GPIO_Output	Output Push Pull	n/a	Low	LED4

7.2. DMA configuration

nothing configured in DMA service

7.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
Memory management fault	true	0	0
Prefetch fault, memory access fault	true	0	0
Undefined instruction or illegal state	true	0	0
Debug monitor	true	0	0
System tick timer	true	0	0
PVD interrupt through EXTI line 16	unused		
Flash global interrupt	unused		
RCC global interrupt	unused		

* User modified value

8. Predefined Views - Category view : Current

Middleware

System Core

Analog

Timers

Connectivity


Multimedia

Computing

DMA

GPIO 

NVIC 

RCC 

SYS 

9. Software Pack Report