

Ve203 Discrete Mathematics (Fall 2016)

Assignment 1: Logic and Set Theory

Date Due: 4:00 PM, Thursday, the 22nd of September 2016

This assignment has (25 Marks).



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Exercise 1.1 De Morgan's Rules

- i) Let a, b be statements. Write out the truth tables to prove *de Morgan's rules*:

$$\neg(a \wedge b) \Leftrightarrow \neg a \vee \neg b,$$

$$\neg(a \vee b) \Leftrightarrow \neg a \wedge \neg b.$$

(2 Marks)

- ii) Let M be a set and $A, B \subset M$. Prove the following equalities by writing out the sets in terms of predicates and applying de Morgan's rules.

$$(A \cap B)^c = A^c \cup B^c,$$

$$(A \cup B)^c = A^c \cap B^c.$$

(2 Marks)

Exercise 1.2 Disjunctive Normal Form

Suppose that a truth table in n propositional variables is specified. Show that a compound proposition with this truth table can be formed by taking the disjunction of conjunctions of the variables or their negations, with one conjunction for each combination of values for which the compound proposition is true. The resulting compound proposition is said to be in *disjunctive normal form*.

(2 Marks)

Exercise 1.3 Functional Completeness

A collection of logical operators is called *functionally complete* if every compound proposition is logically equivalent to a compound proposition involving only these logical operators.

- Show that $\{\wedge, \vee, \neg\}$ is a functionally complete collection of logical operators. (Hint: use the disjunctive normal form.)
- Show that $\{\wedge, \neg\}$ is a functionally complete collection of logical operators. (Hint: use a de Morgan law.)
- Show that $\{\vee, \neg\}$ is a functionally complete collection of logical operators.

(3 Marks)

Exercise 1.4 Exclusive Disjunction

In daily language, the phrase " A or B " is generally taken to mean " A or B , but not both A and B ." The corresponding binary operation is called the *exclusive or*, written as \oplus in logic or XOR in logic gate design. It is defined by the truth table

A	B	$A \oplus B$
T	T	F
T	F	T
F	T	T
F	F	F

- i) Express \oplus by logical conjunction, disjunction and negation, i.e., through the operations $\{\wedge, \vee, \neg\}$.

(2 Marks)

- ii) As shown in Exercise 3, any binary operation can be represented through $\{\wedge, \vee, \neg\}$. For technical reasons, it is preferable in computer design to represent logical operations using $\{\wedge, \oplus, \neg\}$ instead. Write \vee using $\{\wedge, \oplus, \neg\}$.

(2 Marks)

- iii) Explain why ii) proves that $\{\wedge, \oplus, \neg\}$ is functionally complete.

(1 Mark)

Exercise 1.5 Functional Completeness with a Single Operator

In computer design, the logical operations NAND and NOR play an important role.¹ In logic, NAND is represented by the *Scheffer stroke* $|$ while NOR is represented by the *Peirce arrow* \downarrow . They are defined as

$$A | B := \neg(A \wedge B),$$

$$A \downarrow B := \neg(A \vee B).$$

- i) Give the truth tables for $A | B$ and $A \downarrow B$.
(1 Mark)
- ii) Prove that $A \downarrow A \equiv \neg A$ and $(A \downarrow B) \downarrow (A \downarrow B) \equiv A \vee B$.
(2 Marks)
- iii) Deduce that $\{\downarrow\}$ is a functionally complete collection of logical operators.
(1 Mark)
- iv) Represent the exclusive or \oplus solely through \downarrow .
(1 Mark)
- v) Prove that $\{| \}$ is a functionally complete collection of logical operators.
(1 Mark)

Exercise 1.6 The Symmetric Difference

Let S be a set and $X, Y, Z \subset S$. We define the *symmetric difference*

$$X \triangle Y := (X \cup Y) \setminus (X \cap Y).$$

- i) If $X = \{x : A(x)\}$ and $Y = \{x : B(x)\}$ show that

$$x \in X \triangle Y \quad \Leftrightarrow \quad A(x) \oplus B(x).$$

In the following proofs, it may be easiest to use truth tables and properties of the exclusive disjunction rather than any laws of set theory.

- (1 Mark)
- ii) Prove that $X \triangle Y = (X \setminus Y) \cup (Y \setminus X)$.
(2 Marks)
- iii) Prove that $X^c \triangle Y^c = X \triangle Y$.
(2 Marks)

¹According to https://en.wikipedia.org/wiki/Logical_NOR, “The computer used in the spacecraft that first carried humans to the moon, the Apollo Guidance Computer, was constructed entirely using NOR gates with three inputs.” A reference for this claim is given in that article. See also https://en.wikipedia.org/wiki/Flash_memory for a discussion of NAND and NOR flash memory.