

Résumé: Jeayoung Jeon

MLOps/DevOps and AI Engineer (Updated at 2024-11-28)

NOTE

My name is Jeayoung Jeon [전제영], and I'm an MLOps engineer in Seoul, South Korea. I also specialize in:

- Developing **MLOps (APIs, Pipelines)** and **AI/LLM Platforms** in cloud-native environments.
- Developing **Hybrid Kubernetes Clusters** for **High Availability** and **GPU Cost Reduction**.
- Contributing decisions for **MLOps/DevOps** using backgrounds in **ML, Computer Vision, Automotive**.

I'm always open to new challenges and opportunities for various fields including **Automotives** and **AI**. Please feel free to contact me. If you're looking for my professional experience and details, please see my [project portfolio](https://jyje.live/profile/portfolio).

✉: jyjeon@outlook.com

🏠: <https://jyje.live>

🌐: [LinkedIn: jyje](https://www.linkedin.com/in/jyje)

🐙: [Github](http://github.com/jyje)

🔗: [StackShare](https://stackshare.io/jyje/jyje-pro-stack)

Work

Mar 2024 – Nov 2024 (9 Months)

👤 **Intermediate Software Engineer [책임연구원] at MAXST** (<https://maxst.com/ENG/main>)

Roles: Lead MLOps/DevOps Engineer at Technology Division, MAXST

- MLOps** Developing ML APIs, data pipelines, and AI Platforms for research center using open sources.
- LLMOps** Building chatbots using self-hosted RAG+LLM systems for internal documents.
- SRE** Site reliability engineering for web services. Service reliability engineering for ML workloads.

Jan 2021 – Feb 2024 (3 Years and 2 Months)

👤 **Software Engineer [선임연구원] at MAXST** (<https://maxst.com/ENG/main>)

Roles: Associate Researcher and DevOps Engineer at Technology Division, MAXST

- Algorithm Research** Reviewing computer vision algorithms in state-of-art papers and implementing prototypes.
- Hybrid Clusters** Building hybrid clusters with AWS EKS and on-premise Kubernetes for digital twin project.
- DevOps** Building on-premise clusters and providing data pipelines for digital twins.

Mar 2012 – Aug 2020 (8 Years and 6 Months)

👤 **Graduate Student Researcher in Computer Vision at POSTECH** (<https://eee.postech.ac.kr/>)

Roles: Ph.D Integrated Student at Department of Electrical Engineering, POSTECH

- Computer Vision** Research on hyperparameters for accurate and efficient computer vision algorithms.
- Automotives** Principal computer vision technologies for autonomous driving including ADAS and SLAM. Participated in the development of the Korean government's incubation projects with various ADAS researches.
- FPGA** Efficiently implemented computer vision and machine learning algorithms with real-time parallel matrix processing. This is very early type of GPU/NPU accelerator.

Education

Mar 2012 – Aug 2020

🎓 **Master's Degree (Integrated Program) in Department of Electrical Engineering, Signal Processing & Computer Vision from Pohang University of Science and Technology (POSTECH) with GPA of 4.3/4.5**

- Thesis: Virtual Visual-SLAM for Real-World Environments, 2020

Mar 2008 – Feb 2012

🎓 **Bachelor's Degree in School of Electronic Engineering, Electronic Communication from Kumoh National Institute of Technology (Kit) with GPA of 4.3/4.5**

- Thesis: A Study on a Visible Light Communication using LED in Under-water Environment, 2011

Skills

NOTE

Highlighted items are specialized in industry-ready.

MLOps & LLMOps :

Ollama OpenAI API RAG AutoRAG
Kubeflow AutoML Katib Training Operator
JupyterHub Data Pipelines

DevOps & SRE :

Kubernetes On-Premise AWS EKS
GCP GKE Hybrid Clusters ARM64 IaC
Kubespray Terraform Ansible Istio
Grafana Stack Karpenter

CI/CD/CT/CT :

Argo Projects Bitbucket Pipelines
GitHub Actions Self-Hosted Runner Kaniko
Buildah Locust Litmus

ML Backend :

FastAPI Ollama PostgreSQL Milvus Redis

Computer Vision :

Automotives SLAM PyTorch OpenCV
FPGA

UI/UX :

Slackbot Python/FastUI .NET/MAUI
.NET/WPF Unity

FinOps & BizOps :

Kubecost Continuous BI

Programming languages :

Python .NET/C# C/C++ MATLAB

Awards



May 2014

Altera Design Contest 2014, Excellence Prize from Intel-Altera Korea

[System] *FPGA, Vision-Based Driver Support Navigation System*

May 2014

Best Poster Session in Workshop from KYUTECH-POSTECH Joint Workshop

[Poster] *Iterative Polygon Detection using Harris Corner Space Method for Finding Traffic Signs*

May 2013

Altera Design Contest 2013, 2nd Prize from Intel-Altera Korea

[System] *FPGA, Vision-Based Traffic Sign Recognition System*

Feb 2012

Highest Honors in Undergraduate School from Kumoh National Institute of Technology

[Summa Cum Laude] *Highest Honors in Undergraduate Electronic Engineering School*

Jan 2012

NAVER Power KiN 2011 (<https://m.site.naver.com/1y6qP>) from NAVER

[Activity] *Knowledge Export in `Electronics Engineering, Mathematics and Programming fields`. Active 2009-2011, Selected as a MVP in 2012 / Total number of answers 723, Selection ratio 98.1%*

Publications



NOTE

The full list of my publications are available on [Google Scholar](https://scholar.google.com/citations?user=gwCPQM8AAAAJ&hl=ko) (<https://scholar.google.com/citations?user=gwCPQM8AAAAJ&hl=ko>) .

Jul 2020, POSTECH, Thesis (1st)

Virtual Visual-SLAM for Real-World Environments (<http://postech.dcollection.net/common/orgView/200000341295>)

by **Jeayoung Jeon**

Nov 2014, ISVC, Advances in Visual Computing, 10th International Symposium (2nd)

Cost Aggregation Table: Cost Aggregation Method Using Summed Area Table Scheme for Dense Stereo Correspondence (https://doi.org/10.1007/978-3-319-14249-4_78) by **JeongMok Ha, Jeayoung Jeon, GiYeong Bae, SungYong Jo & Hong Jeong**

Oct 2014, ICCAS, 14th International Conference on Control, Automation and Systems (1st)

Polygonal symmetry transform for detecting rectangular traffic signs (<https://doi.org/10.1109/ICCAS.2014.6987934>) by **Jea Young Jeon, JeongMok Ha, Sung Yong Jo, Gi Yeong Bae, Hong Jeong**

Apr 2011, ICS-KIEE (1st, equivalent)

A Study on a Visible Light Communication using LED in Under-water Environment

(<https://www.dbpia.co.kr/Journal/articleDetail?nodeId=NODE01951197>) by **Daehee Lee, Ki-Sung Park, Jea-Young Jeon, Yeon-Mo Yang**

Certifications



Nov 2024 (Expired in Nov 2027)

GitHub Foundations (<https://www.credly.com/badges/876fa6b3-0b27-4ddf-bbb3-a9d853918566>) from **GitHub**

Sep 2024 (Expired in Sep 2026)

CAPA: Certified Argo Project Associate (<https://www.credly.com/badges/ee42c2c7-2ac3-411f-8713-cc26cbec8022>) from **The Linux Foundation**

Jun 2024 (Expired in Jun 2026)

CKAD: Certified Kubernetes Application Developer (<https://www.credly.com/badges/9e072a3a-57d0-403e-8bef-5831d618675c>) from **The Linux Foundation**

Mar 2024 (Expired in Mar 2027)

CKA: Certified Kubernetes Administrator (<https://www.credly.com/badges/d944bde7-222a-4ce5-b4e6-4e6c84df0ef8>) from **The Linux Foundation**

Interests



DevOps Culture :

Coop First, Tech Next

Automate as Possible

Internal Development Platform

Cost Efficiency :

AMD-to-ARM Transition

Hybrid Clusters

Home Clusters :

Raspberry Pies

Personal RAG

Live Demo

Languages



Korean :

Native

English :

Working Proficiency