Anizamariah Bt Hj Daud

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50480 Kuala Lumpur

Malaysia

Career Objectives

* To seek challenging assignment and responsibility, with an opportunity for growth and career advancement as successful achievements.
* To seek experience and learning opportunities from senior colleagues.
* Willing to learn and develop new skills from different fields.

Employment

Western Digital Engineer (Product) March 2014 – Present

* Perform Failure Analysis for new and sustaining products to know root causes in order to further improve the performance of product.
* Management of product by planning and monitoring each of the build to project more revenue and lessen the loss for the company.
* Help other functional team for development of new products.

MIMOS Berhad Research Assistant December 2011-February 2014

* Completed research project on Cognitive Radio and Interference Management under escience fund for Ministry of Science and Technology Information (MOSTI).
* Wrote and presented 2 international conference technical papers;
  + A. Daud, N. Ramli, H. Mohamad, M. Ismail, “Single Carrier Adaptive Antenna Array for Cognitive Radio Network”, Asia Pacific Conference on Communications (APCC) 2012, Jeju Island, South Korea
  + A. Daud, N. Ramli, H. Mohamad, M. Ismail, “Reweighted Nuclear Norm Minimization for Cognitive Radio Network”,( WiMob) 2013, Lyon, France
* Co-authored 2 international technical papers;

Juris Technologies Software Engineer October 2011-December 2011

* Worked on Juris Collection and Loan project for Bank Simpanan Nasional (BSN) Malaysia.

PHN Industry IT Management Trainee June 2011-October2011

* Maintain IT services for all PHN plants.
* Resolve IT issues for PHN and DRB-Hicom sub-companies.
* Audit IT services for all PHN.

Faculty of Engineering, IIUM Tutor, Lab demonstrato r 2007-2011

* Tutored for courses: C language and Object Oriented Programming.

Education

MSc in Communication Engineering, March 2012 – Present

University Kebangsaan Malaysia Full Research

Research topic : Cognitive Radio and Interference Mangement

Skills developed: Matlab, Latex, Mathematica

B.Eng in Communication Engineering, June 2007 – May 2011

International Islamic University Malaysia CGPA: 3.26

Final Year Project:

8-bit adder with FPGA board.

Responsible for writing the source code and implementation on FPGA board.

Skills developed: Verilog, C, C++

Additional Experience and Awards

* Strong command in English and Bahasa Malaysia.
* Dean’s list
* Assistant Head for Education Bureau Engintex 2009
* Represented Selangor for karate-do event (bronze medalist).
* Represented IIUM karate team (bronze medalist)

Languages and Technologies

* Fluent in English and Bahasa Melayu
* Speaks and write basic French
* C, C++, Verilog, Linux
* Visual Studio, Matlab, Mathematica, FPGA
* Microsoft Office