

The CDC in its efforts to help students prepare better for the placement process would like to hear your views on different relevant aspects. Would you be willing to spare 2 minutes of your time to fill this form.

This survey is completely optional. That being said, the results of the survey will have an impact on how CDC helps students prepare in future years. If you do choose to fill it, we would urge you to be honest in your answers, after reading, weighing and analysing all options. We promise it won't take more than 2 minutes of your time. Your identity will not be revealed publicly.

☐ Yes

☐ No

[View applied Resume](#)

Company : Analog Devices India Pvt. Ltd.

Job Profile

Form Type	Stipend per month	Additional Criteria	CGPA Cut-off
INTERNSHIP	40000 INR [40000+1200 (food Coupon) per month - UG 50000+1200 (food Coupon) per month- PG One time relocation of INR 25,000.]		0.0

Job Description

Position : Digital Design / Verification Intern Analog Devices is a global leader in the design and manufacturing of integrated circuits to help solve the toughest engineering challenges. Analog Devices enables our customers to interpret the world around us by intelligently bridging the physical and digital with unmatched technologies that sense, measure and connect. We create innovative solutions to solve design challenges in instrumentation, automation, communications, healthcare, automotive and numerous other industries. Analog Devices Inc (ADI) is looking for Digital IC Design and Verification Engineers for its Chip Design and development team in Bangalore, India. The selected students will work on chip development based on ultra-deep submicron semiconductor process technologies. They will be guided and trained by ADI's experienced design and verification engineers. This will include • Development of key digital blocks and SoCs. • Development of optimal micro-architecture and design by analyzing power, performance and area tradeoffs. • Verification of key digital blocks and SoCs in High Speed signal converter /Communication products. • Definition of testplan, tests and verification methodology for block and chip-level verification. • They will use industry leading CAD tools. Essential: 1. Knowledge of Verilog 2. Knowledge of digital circuit design – combination and sequential 3. Hands on any scripting languages such as perl/phyton 4. Good Analytical skills 5. Should be able to work independently and proactive Additional Skills being looked for: 1. System Verilog 2. Any projects on FPGA/matlab would benefit 3. Understanding of signal processing/filtering background 4. Good understanding of ADCs/DACs 5. Basic understanding of pipe lining, 8085/any-other-assembly language Eligibility: BTech / Mtech / Dual Degree in Electronics Engineering with 8+ CGPA academic records and successfully completing Degree in June 2022

Selection Process

Resume,Online/Offline test (Max 30 mins)

Allowed Departments and degrees

ELECTRICAL ENGINEERING

B.TECH --- ELECTRICAL ENGG. (B.TECH 4Y)

B.TECH --- INSTRUMENTATION ENGG. (B.TECH 4Y)

DUAL DEGREE --- ELECT.ENGG. CONTROL SYSTEM ENGG.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. DUAL DEGREE IN ANY SPL.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. INSTRUMENTATION AND SIGNAL PROCESSING ENGG.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. MACH. DRIVES & POWER ELECT.(M.TECH DUAL 5Y)

DUAL DEGREE --- INSTRUMENTATION AND SIGNAL PROCESSING ENGINEERING(M.TECH DUAL 5Y)

DUAL DEGREE --- INSTRUMENTATION ENGINEERING/CONTROL SYSTEM ENGINEERING(M.TECH DUAL 5Y)

ELECTRONICS AND ELECTRICAL COMMUNICATION ENGG.

B.TECH --- ELECTRONICS & ELEC. COMM.ENGG. (B.TECH 4Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.DUAL DEGREE IN ANY SPL.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.MICROELECTRONICS & VLSI DES.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.TELECOMM SYSTEM ENGG.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.VISUAL INFORMN. & EMBEDDED SYS.(M.TECH DUAL5Y)