

The CDC in its efforts to help students prepare better for the placement process would like to hear your views on different relevant aspects. Would you be willing to spare 2 minutes of your time to fill this form. This survey is completely optional. That being said, the results of the survey will have an impact on how CDC helps students prepare in future years. If you do choose to fill it, we would urge you to be honest in your answers, after reading, weighing and analysing all options. We promise it won't take more than 2 minutes of your time. Your identity will not be revealed publicly.

- ☐ Yes  
☐ No

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### Company : Texas Instruments

#### Job Profile

Form Type	Stipend per month	Additional Criteria	CGPA Cut-off
INTERNSHIP	45000 INR [one time additional 25,000 for travel & accommodation]		0.0

#### Job Description

Analog Intern TI India was set up in 1985 and has R&D presence for all the major business units of TI including Analog - (Data Converters, Amplifiers, Clocks & Synthesizers, Motor Drives, Power Management ) and Embedded Processors (Connected Microcontrollers, Radar, ADAS- Advanced Driver Assistance and Infotainment Processors etc.) and caters to products for different market segments - Industrial, automotive, personal Electronics, Communication and Enterprise. The internship will give you a flavor of the real work at TI. You would be assigned to a real time project where in your deliverables will be feed in to the deliverables of your team. You would be assigned a mentor who would work very closely with you and guide you through the entire process. As an Analog intern you will have the opportunity to work in one of the many exciting areas that TI works on like Wireless Infrastructure, audio, Motor drives, industrial automation, Medical Imaging, high speed interface, clocks & Synthesizers, high volume linear, power supply, battery management solutions, linear power etc. Some of the projects that students have done in the past: Analog: 1) Design of a low voltage,high gain amplifier 2) Oscilloscope front end design 3) Analysis and design of N-path filter 4) EMI reduction in PWM Class-D Amp 5) Investigations on Hybrid PLL

#### Selection Process

Online/Offline test (Max 30 mins)

#### Allowed Departments and degrees

ELECTRICAL ENGINEERING

B.TECH --- ELECTRICAL ENGG. (B.TECH 4Y)

B.TECH --- INSTRUMENTATION ENGG. (B.TECH 4Y)

DUAL DEGREE --- ELECT.ENGG. CONTROL SYSTEM ENGG.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. DUAL DEGREE IN ANY SPL.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. INSTRUMENTATION AND SIGNAL PROCESSING ENGG.(M.TECH DUAL 5Y)

DUAL DEGREE --- ELECT.ENGG. MACH. DRIVES & POWER ELECT.(M.TECH DUAL 5Y)

DUAL DEGREE --- INSTRUMENTATION AND SIGNAL PROCESSING ENGINEERING(M.TECH DUAL 5Y)

DUAL DEGREE --- INSTRUMENTATION ENGINEERING/CONTROL SYSTEM ENGINEERING(M.TECH DUAL 5Y)

ELECTRONICS AND ELECTRICAL COMMUNICATION ENGG.

B.TECH --- ELECTRONICS & ELEC. COMM.ENGG. (B.TECH 4Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.DUAL DEGREE IN ANY SPL.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.MICROELECTRONICS & VLSI DES.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.TELECOMM SYSTEM ENGG.(M.TECH DUAL5Y)

DUAL DEGREE --- ELECT.&ELEC.COM.ENGG.VISUAL INFORMN. & EMBEDDED SYS.(M.TECH DUAL5Y)