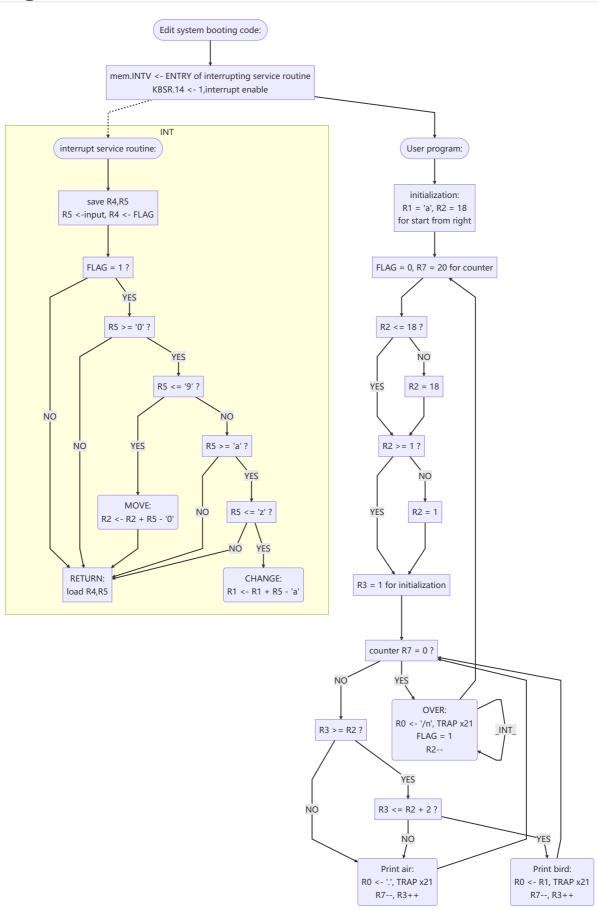
repo-lab4(ddl 7.27)

Algorithm



Essential parts of codes

```
1
    ; interrupt service routine
           .ORIG x2000
2
           STR
3
                  R4, R6, #-1
4
           STR
                  R5,R6,#-2
5
           ;
6
           LDI
                  R5,KBDR; input -> R5
7
                  R4,JFLAG ; []
           LDI
8
           BRZ
                  R ; FLAG = 0
9
           LD
                  R4, NUMS
10
           ADD
                  R4,R5,R4
                  R ; R5 < '0'
11
           BRn
12
           LD
                  R4, NUML
13
           ADD
                  R4,R5,R4
14
                  MOVE ; ('0' <=) R5 <= '9'
           BRnz
                  R4,CHAS
15
           LD
16
           ADD
                  R4,R5,R4
17
                  R ; R5 < 'a'
           BRn
                  R4,CHAL
18
           LD
19
                  R4,R5,R4
           ADD
20
                  CHANGE ; ('a' \ll) R5 \ll 'z'
           BRnz
21
           BR
22
23
   MOVE
                  R4, NUMS
           LD
24
           ADD
                  R5,R5,R4
25
                  R2,R2,R5
           ADD
26
           BR
   CHANGE LD
27
                  R4,CHAS
28
           ADD
                  R1,R5,R4
29
                  R1,R1,#1; offset (from 1)
           ADD
           STI
                  R1, JBIRD
30
31
   R
           LDR
                  R5,R6,#2
32
                  R4,R6,#1
          LDR
33
           RTI
34
          ; data base
35
   KBSR .FILL xFE00
36 KBDR
          .FILL xFE02
37
   JBIRD .FILL BIRD
38 JFLAG .FILL FLAG
39
   NUMS .FILL xFFD0 ; -48 neg'0'
40 NUML
          .FILL xFFC7; -57 neg'9'
   CHAS .FILL xFF9F ; -97 neg'a'
41
   CHAL
42
          .FILL xFF86 ; -122 neg'z'
43
           . END
```

The essential part of this lab is the 'interrupt service routine' as I have posted above, whose entry is $\times 2000$ and the address is stored at $\times 0180$ (the address from INTV).

And at first, the processor saves the value of R4 and R5 in the stack(line 3, line 4), which will be restored before the processor returns to the user program. In this way, the interrupt service routine can't destroy the value in GPR.

Then, load input into R5, and judge whether FLAG is one(line 8). The FLAG is set by user program, and 'one' means that everything in this line has already been printed and can be interrupted. While 'zero' means it can't be interrupted, so the processor just returns. (By 'return', I mean restore

the GPS and then return, R part from line 31 to line 33. The same is true for the following part.) For case FLAG is one, the interrupt service routine runs. If the input character is a digit('0' <= input <= '9', judged by codes from line 9 to line 14), add this digit to R2(MOVE part from line 23) and then return. If the input character is a lowercase-letter('a' <= input <= 'z', judged by codes from line 15 to line 20), load the offset into R1(CHANGE part from line 27) and then return. (Adding: R1 represents the offset of the character to express 'bird', and R2 represents the first location where the 'bird' occurs.)

And in the user program, I set R7 as a counter with initialization value 20(in the process of printing a line, which doesn't impact the RET instruction after that), and compare R2(the first location of the 'bird') with R3(the current location to print) to determine print air('.') or bird(R1 determine the letter to print).

Q&A

I talked about the idea of writing code, and introduced the the meaning of the registers used in the program and the algorithms used in the code to TA.

Besides, there is a bug in my codes that it can't prevent interrupting when some contents of current line haven't been printed. And to prevent this, I set a FLAG to check whether the INTV should be set. Here are some according codes posted below:

```
1
            .ORIG
                   x2000
                            ; interrupt service routine
2
            ; . . .
 3
            LDI
                    R5, KBDR; input -> R5
4
            LDI
                    R4,JFLAG
5
                    R ; FLAG = 0
            BRZ
6
            ; . . .
7
    KBDR
            .FILL
                   xFE02
8
    JFLAG .FILL
                   FLAG
9
            ; . . .
10
           .ORIG
                   x3000 ; user program
11
            . . . .
12
    PRINT AND
                   R4,R4,#0
                     R4,FLAG ; FLAG = 0
13
            ST
14
             . . . .
15
    OVER
                     RO, NEWLINE
            LD
16
             TRAP
                     x21
17
             AND
                     R4,R4,#0
18
             ADD
                     R4,R4,#1
19
                     R4,FLAG; FLAG = 1
             ST
20
             JSR
                     DELAY
21
             ADD
                     R2,R2,#-1
22
             BRnzp
                     PRINT
23
             ; . . .
24
    FLAG
             .BLKW
                     #1
```

In the user program, when the processor start printing a new line, the program first sets FLAG to zero(PRINT part from line 12, which means non-interruptable). Having printed this line, the program sets FLAG to one(OVER part from line 15, which means interruptable). And in the interrupt service routine, check whether it is interruptable(line 5). Last but not least, when modifying this codes, I didn't do a good job of saving the value of GPR used in interrupt service routine, which is totally a big trouble.