# Jonathan E. Zarger

jzarger@umich.edu jzarger.me

Objective

Obtain an Electrical Engineering position working in embedded systems and control systems

**Education** 

### M.S.E. in Electrical and Computer Engineering: Controls

**Expected Graduation - May 2018** 

University of Michigan [Ann Arbor]

 Courses (tentative): Self-Driving Cars: Perception and Control, Matrix Methods for Digital Signal Processing, Advanced Embedded Systems, VLSI for Digital Signal Processing

### **B.S.E.** in Electrical Engineering

**September 2013 - May 2017** 

University of Michigan [Ann Arbor] - Graduated Magna Cum Laude, GPA: 3.621/4.000

Courses: Linear System Theory, Embedded Control Systems, Control System Design & Analysis,
 Microprocessor Based System Design, Navigation & Guidance, Flight Software, Computer Organization

Work Experience

# Northrop Grumman [Rolling Meadows, IL] - Engineering Intern

May - August 2017

- Worked on team to develop standard internal test automation software in Python and Robot
  Framework for remotely controlling test equipment like signal generators, spectrum analyzers, etc.
- Validated software functionality with test cases and demonstrated to product owner

### Delphi Electronics and Safety [Kokomo, IN] - Engineering Intern

May - August 2016

 Designed, fabricated and tested equipment for frequency based signals in radiated immunity validation testing, including analog circuit design and RF immunity design

### BWI Group [Brighton, MI] - Engineering Intern

May - August 2015

Led functional and failsafe benchmarking on brake systems to aid new project development

### Honda R&D Americas [Southfield, MI] - Engineering Intern

May - August 2014

Led project to design and fabricate remotely controlled obstacles for demonstrating collision detection

Team and Project Experience

### Michigan Aeronautical Science Association - Avionics Team

September 2014 - Present

- Avionics Team Lead (2015-2016)
- Led team of ten students to design, implement, and test electrical systems
- Managed instrumentation and data acquisition systems during hybrid engine testing
- Led project to design, fabricate, and program a recovery control and telemetry device

### **MHacks Coordinator Team - Hardware**

November 2015 - October 2016

- Designed microcontroller development boards to distribute to event participants
- Planned and ran Introduction to Hardware and Arduino Workshops
- Provided mentoring and assistance at event to participants working on hardware-based projects

#### Digital Signal Processing Lab Senior Capstone Project - DOGBOT

Fall 2016

- Worked on team to design robot that uses image tracking to follow a laser pointer
- Designed printed circuit board for chassis, containing motor drivers and microcontroller
- Designed and implemented observer and closed-loop controller embedded software for laser following

### Microprocessor Based System Design Final Project - Motion Based Game Controller Winter 2017

- Worked on team to design device to convert foot motion and orientation to wireless game control
- Designed printed circuit board with microcontroller, FPGA, Bluetooth module, and inertial sensor
- Implemented data interpretation and communication algorithms in C and Verilog

#### Other Control Systems and Embedded Systems Projects

2015 - 2017

- Implemented simulated adaptive cruise control and lane-keep system with Simulink and Stateflow
- Wrote control and navigation software to fly a quadcopter autonomously through a 3D path
- Developed real-time embedded software to control rotation of tabletop satellite simulator

### Technical Skills

# **Electrical Hardware Proficiencies**

- Printed circuit board and schematic design with Altium CircuitMaker and EAGLE
- Through-hole and surface-mount soldering (soldering iron and hot-air reflow)
- Reading electrical schematics and component datasheets
- Using standard EE tools: oscilloscopes, bench power supplies, waveform generators, logic analyzers

### **Software Proficiencies**

- C, MATLAB, Python, Verilog, ARM, C++, Simulink, Stateflow
- Proficient with NI Multisim (SPICE), LTSpice, and Synopsys Saber for circuit modeling