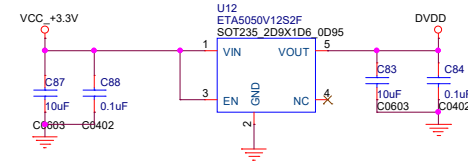
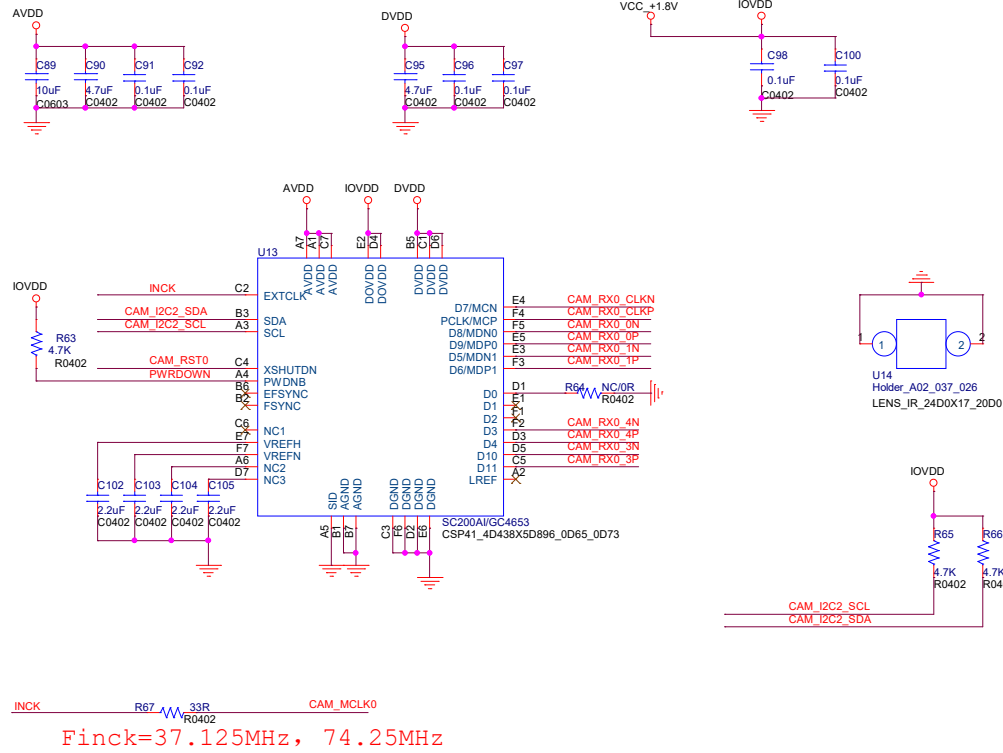


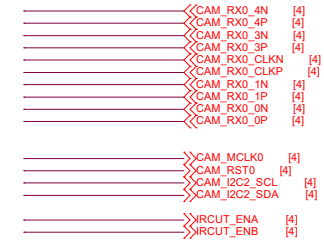
$$V_{out} = 0.8V * (R1/R2 + 1)$$



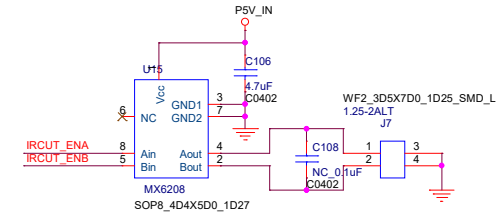
SENSOR



NET

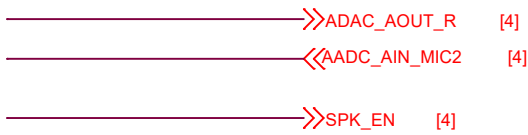


IR CUT

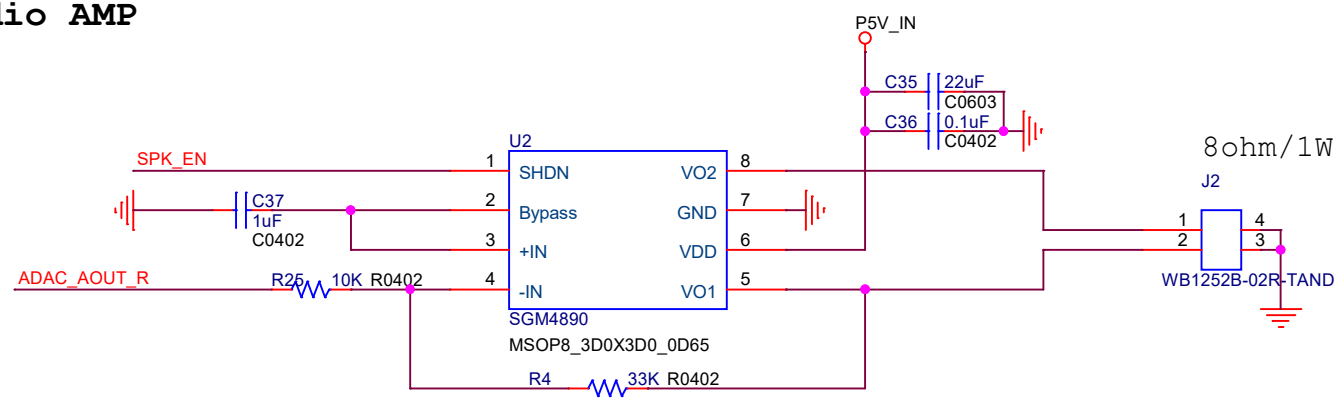


PAD		SC200A1	SC3335	SC500A1	GC2063	GC4653
A6	C104	NC	NC	NC	2.2uF	NC
D1	R64	NC	NC	0R	NC	NC
D7	C105	NC	NC	2.2uF	2.2uF	2.2uF
MIPI		2Lan	2Lan	4Lan	2Lan	2Lan

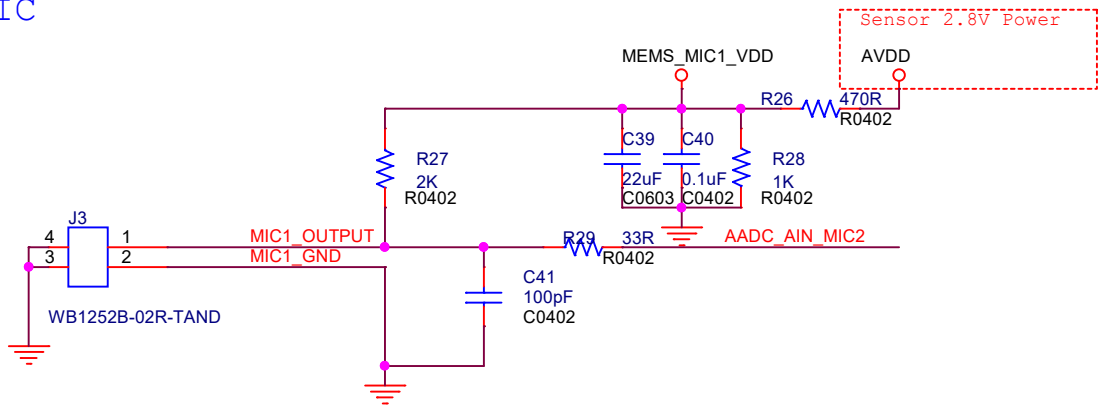
NET

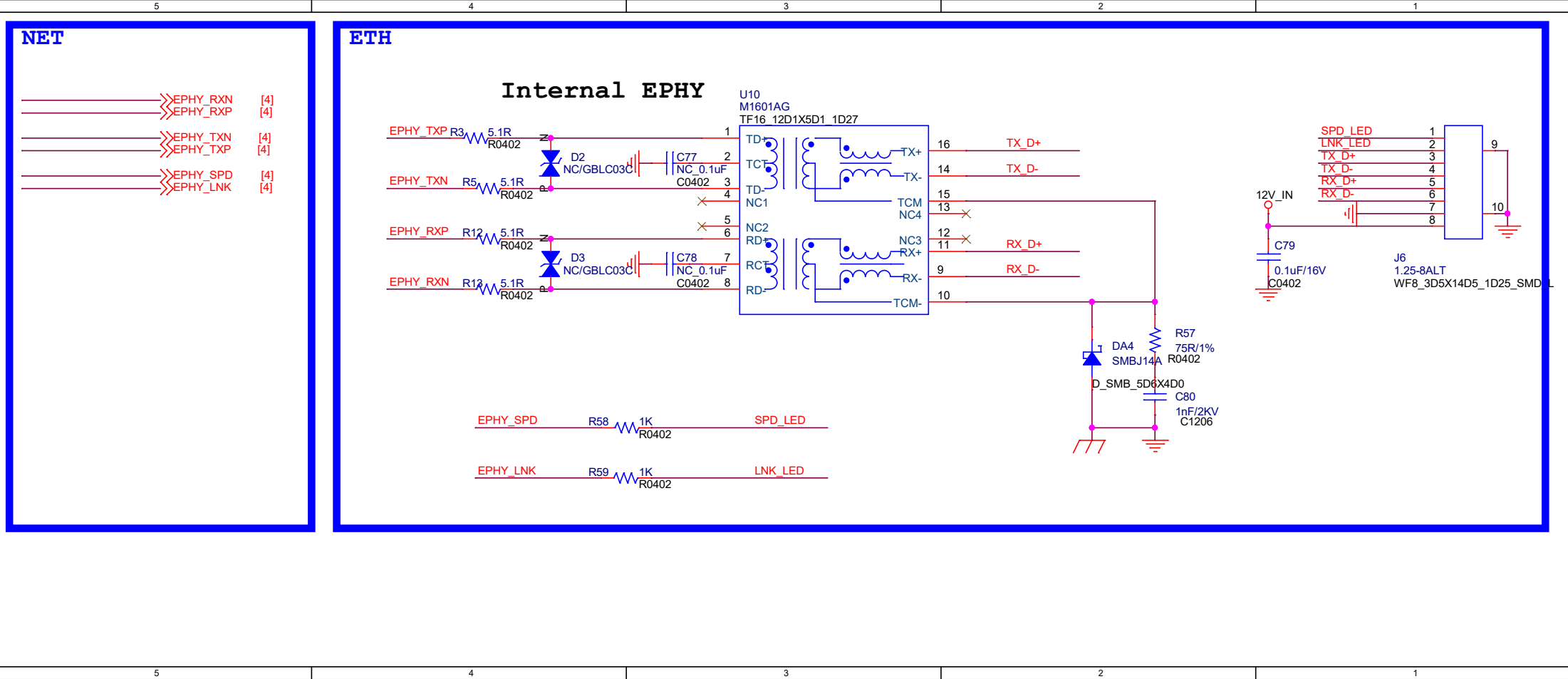


Audio AMP



MIC

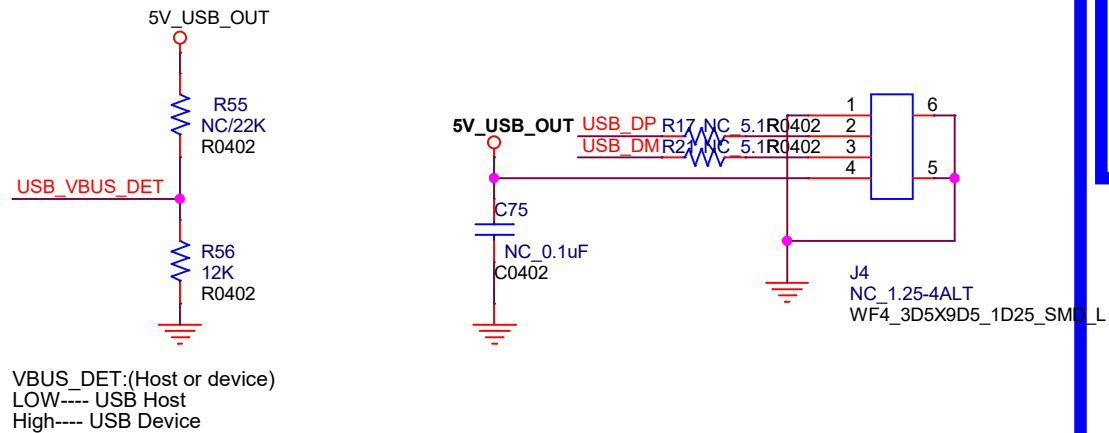




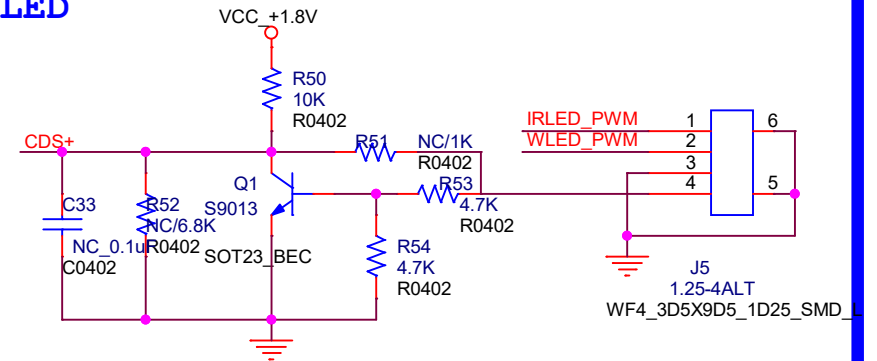
NET



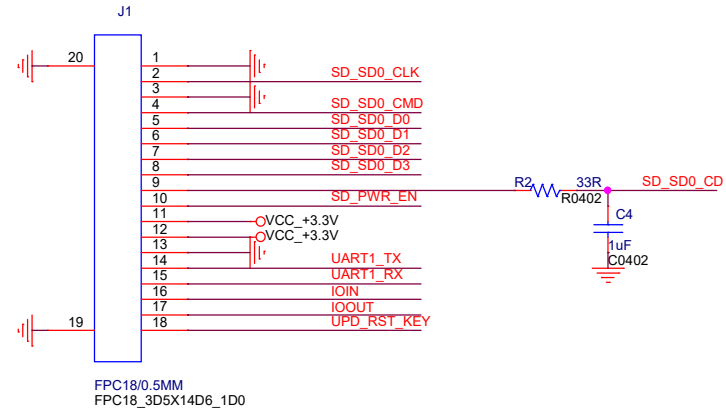
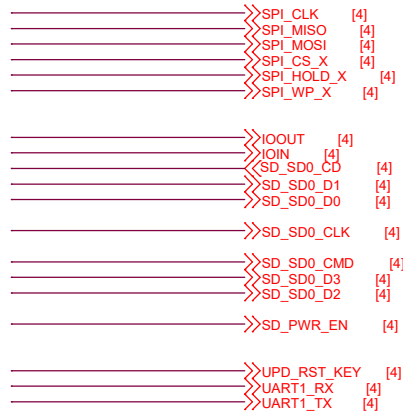
USB



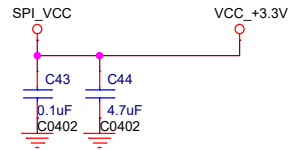
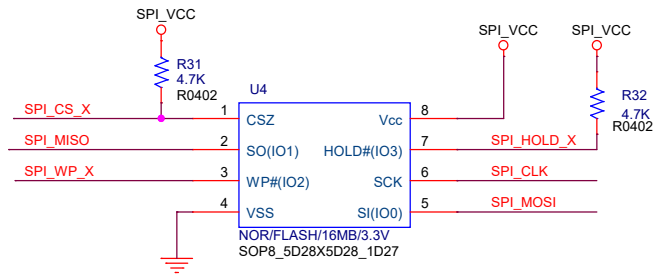
LED

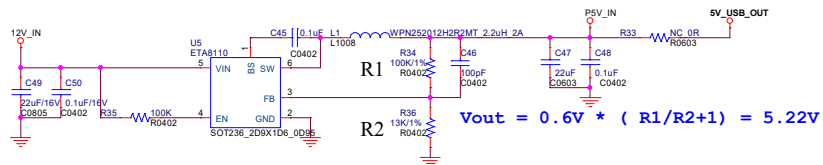


NET

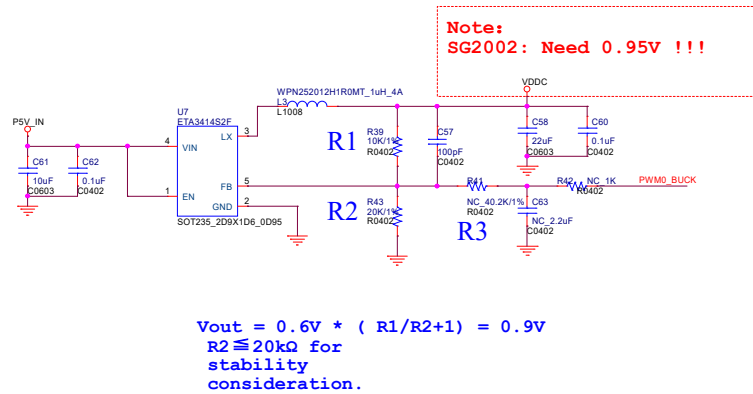
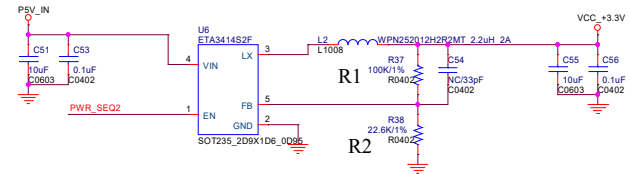


SPINOR

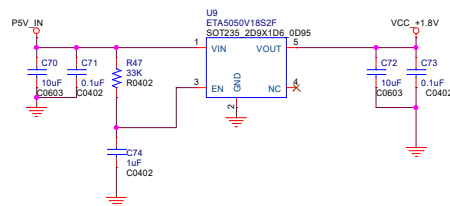
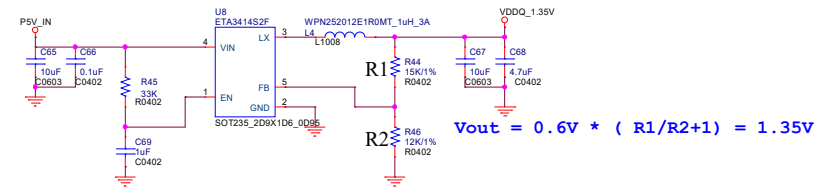




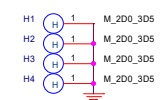
PWR_SEQ02

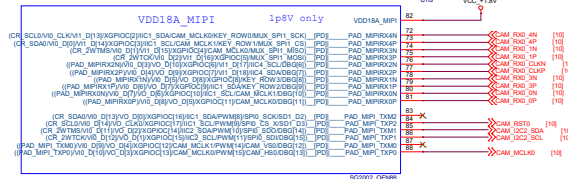


Vin-->DDR31P35

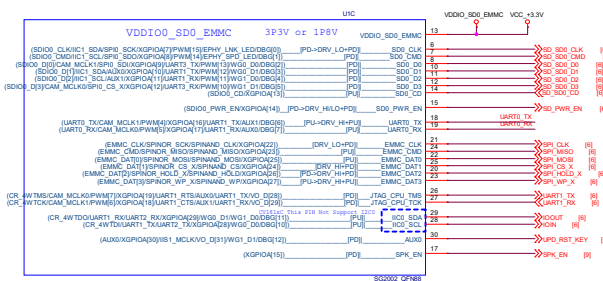


--->PWR_SEQ2 [4]
 --->PWM0_BUCK [4]

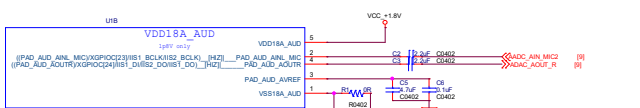




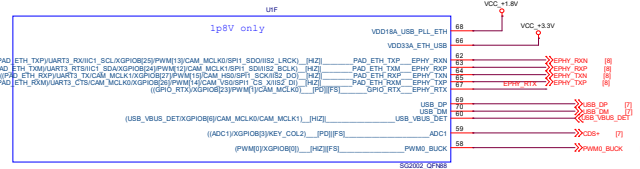
VDDIO SD0 EMMC



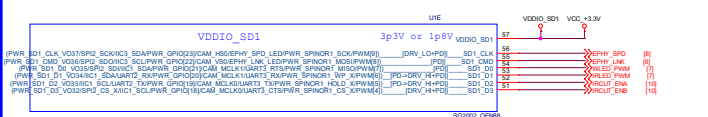
AUDIO



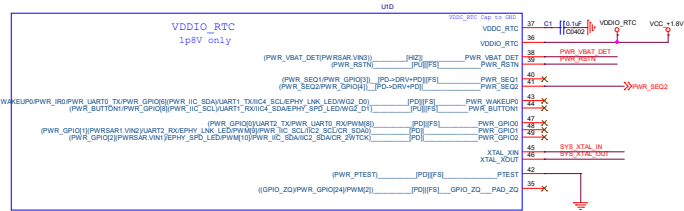
EPHY&USB



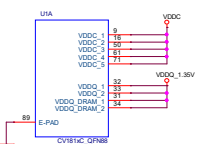
SD1



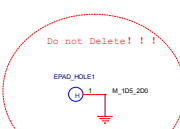
RTC



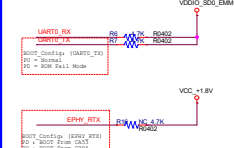
POWER&GND



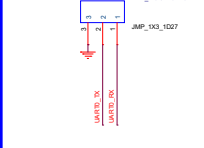
EPAD Hole



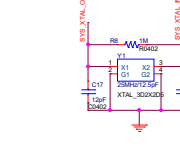
Pull Up



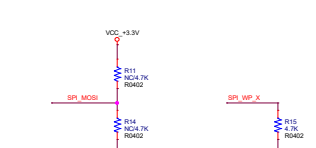
Debug



SYS XTAL

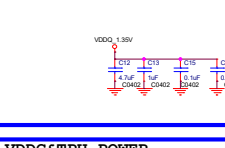


BOOT CONFIG

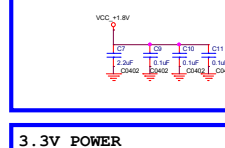


SPI_MOSI	SPI_WP_X	boot device selection
1	1	eMMC
1	0	Serial NOR
0	0	Serial NAND

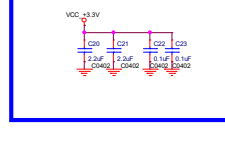
DDR3 POWER



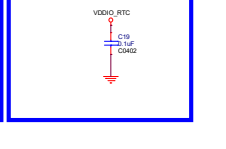
1.8V POWER



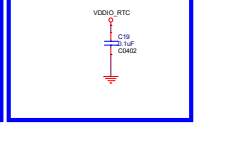
3.3V POWER



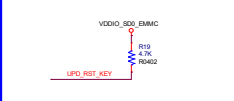
VDDIO_RTC



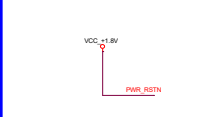
VDDIO_RTC



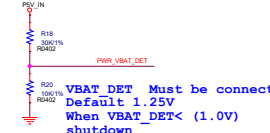
Update&Reset Key



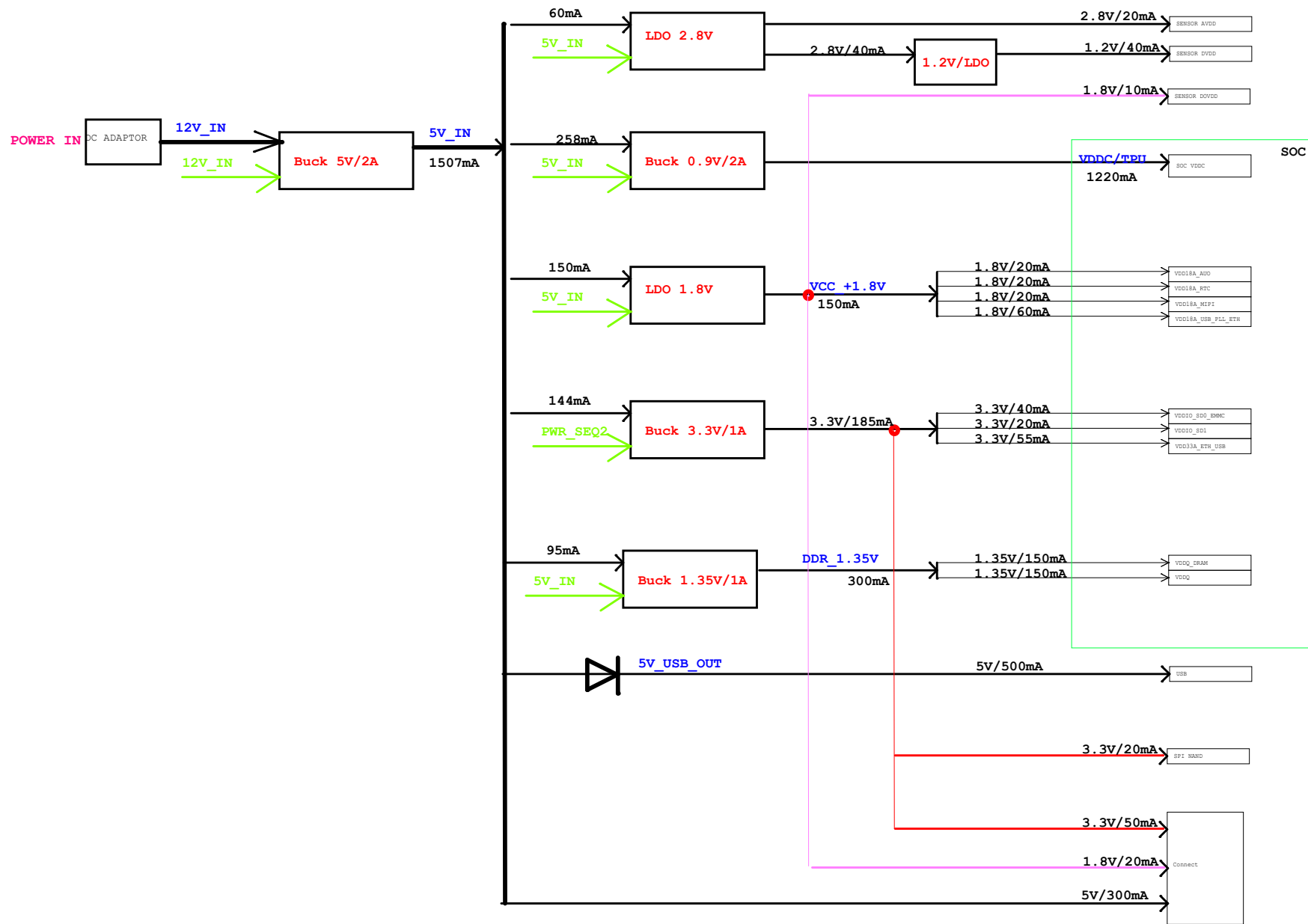
SYS Reset



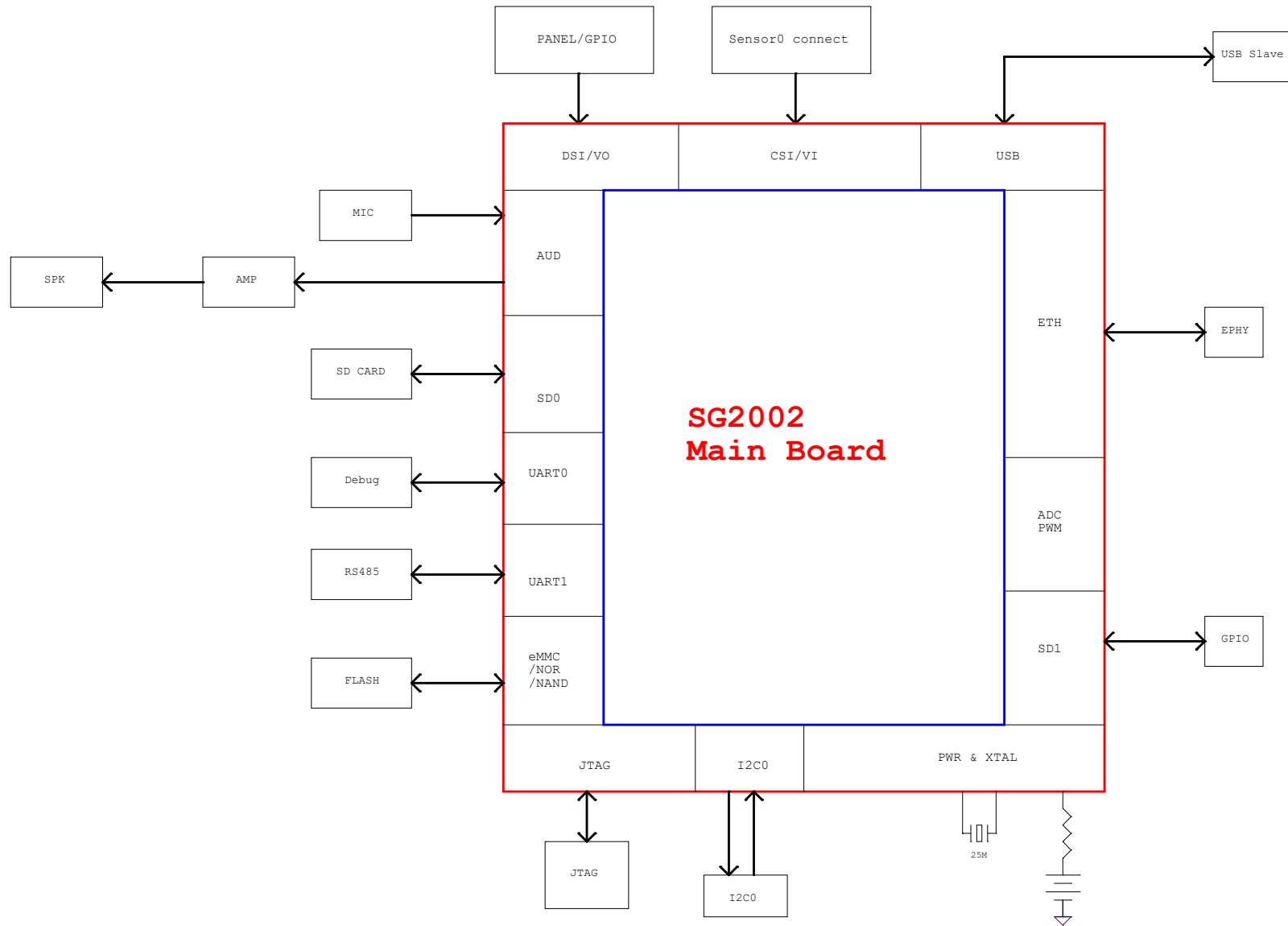
PWR DET



VBAT_DET Must be connect
Default 1.25V
When VBAT_DET < (1.0V)
shutdown



Block Diagram for SG2002



5 4 3 2 1

D

D

Version List			
Version	Change List	Date	Author
1.0.0	First Release	2021-11-11	Miller.Zhang
1.1.0	1、C77,C78 100nF电容NC 2、增加R16	2022-6-10	Miller.Zhang
1.3.0	1、增加CV1812C-P VDDC电压0.95V说明 2、C39由10uF改为22uF	2022-12-28	Miller.Zhang

C

C

B

B

A

A

5 4 3 2 1