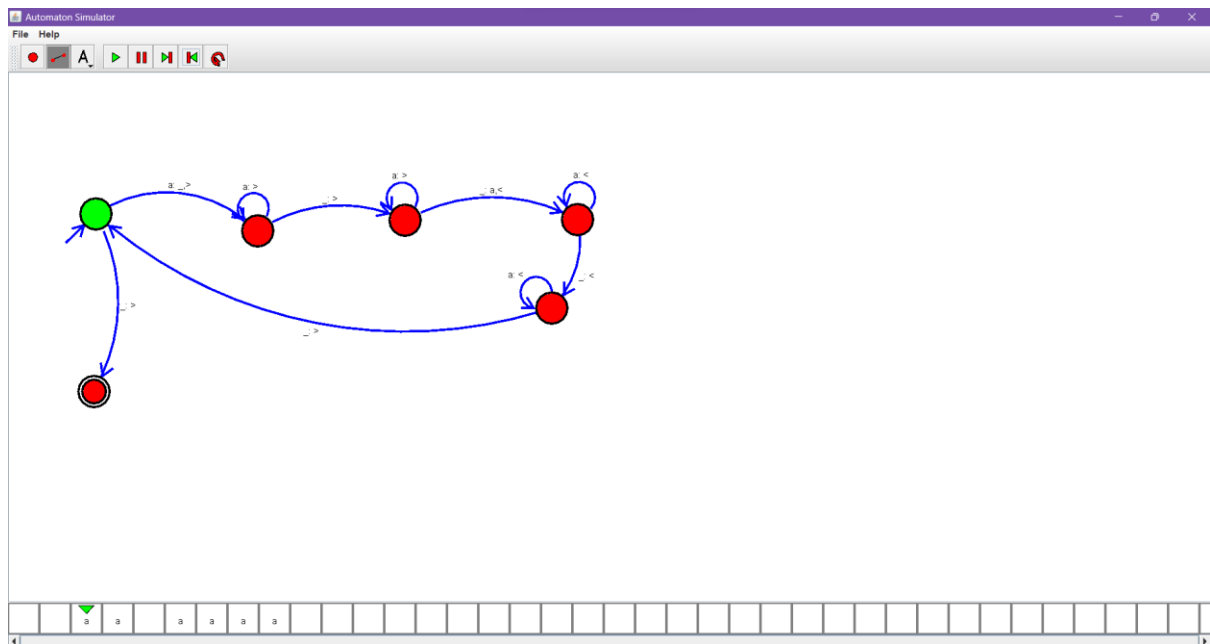
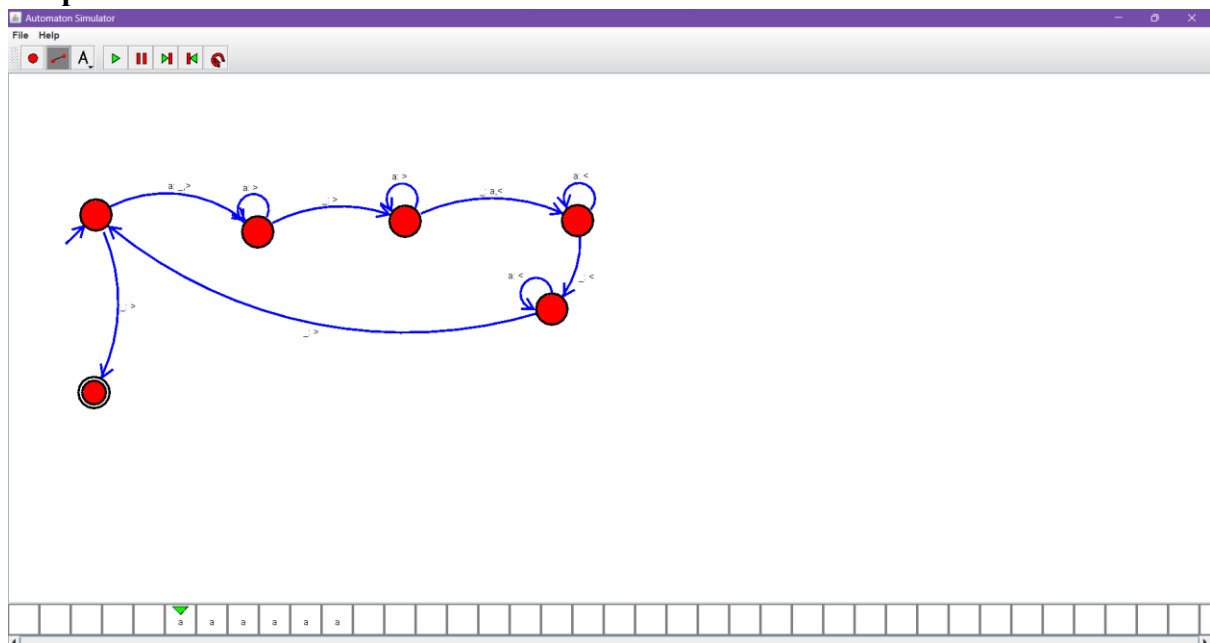


1. Design TM using simulator to perform addition of 'aa' and 'aaa'
2. **AIM:** TO Design TM using simulator to perform addition of 'aa' and 'aaa'

INPUT:



Output:



RESULT: Output is successfully obtained.