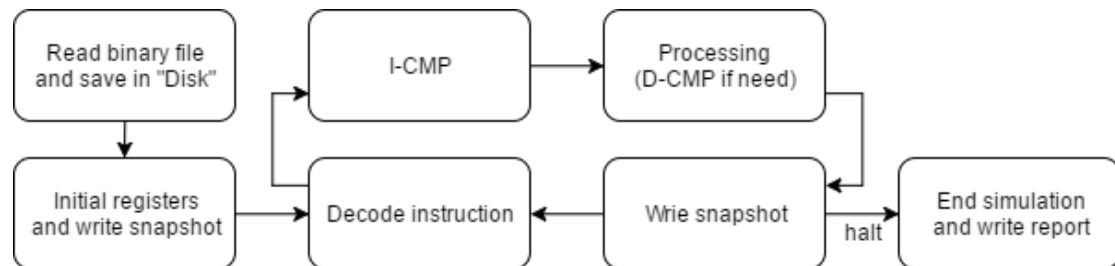


## Computer Architecture Project 3 Report

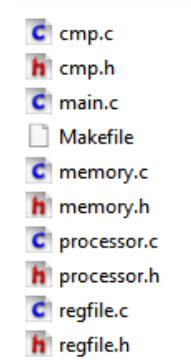
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### 1) Project Description

#### 1-1) Program Flow chart:



#### 1-2) Detailed Description:



The file I used in this project are copy from project 1 except two "cmp" file.

#### I. Project 1 part:

I changed some function that can help me to write the trace file, and add the I-CMP check function before running the process, if the process need to handle the data, then call the D-CMP function.

However, when I changing the project 1 file, I deleted some important things that may make my program output the incorrect snapshot file. And because I have not noticed that, so my 1st submission program cannot pass many test case.

## II. CMP part:

```
12 //Config value
13 typedef struct _variable {
14     int memSize;
15     int pageSize;
16     int cacheSize;
17     int blockSize;
18     int set;
19 } variable;
20
21 typedef struct _PTE {
22     int ValidBit;
23     unsigned int PhysicalPageNumber;
24 } PTE;
25
26 typedef struct _TLB {
27     int ValidBit;
28     unsigned int VirtualPageNumber;
29     unsigned int PhysicalPageNumber;
30     int Cycle;
31 } TLB;
32
33 typedef struct _Cache {
34     int ValidBit;
35     unsigned int Tag;
36     int MRU;
37 } Cache;
38
39 typedef struct _Memory {
40     int ValidBit;
41     int Cycle;
42 } Memory;
43
44 variable I_var, D_var;
45 PTE *iPTE, *dPTE;
46 TLB *iTLB, *dTLB;
47 Cache **iCache, **dCache;
48 Memory *iMem, *dMem;
```

<- the structure created in cmp.h

```
49 unsigned int iPTE_entries, dPTE_entries;
50 int iPTE_HIT, iPTE_MISS, dPTE_HIT, dPTE_MISS;
51 unsigned int iTLB_entries, dTLB_entries;
52 int iTLB_HIT, iTLB_MISS, dTLB_HIT, dTLB_MISS;
53 unsigned int iCache_entries, dCache_entries;
54 int iCache_HIT, iCache_MISS, dCache_HIT, dCache_MISS;
55 unsigned int iMem_entries, dMem_entries;
56
57 void initCMP();
58 void initPTE();
59 void initTLB();
60 void initCache();
61 void initMem();
62 void checkITLB(unsigned int Vaddr); //I
63 void updateITLB(unsigned int VPN);
64 int checkIPTE(unsigned int VPN);
65 void updateIPTE(unsigned int VPN);
66 void checkICache(unsigned int PhysicalAddr);
67 void checkDTLB(unsigned int Vaddr); //D
68 void checkDTLB(unsigned int Vaddr);
69 void updateDLTB(unsigned int VPN);
70 int checkDPTE(unsigned int VPN);
71 void updateDPTE(unsigned int VPN);
72 void checkDCache(unsigned int PhysicalAddr);
73 void writeReport();
```

For the “cmp” file function, to check the i-cmp, we will call checkITLB function, and the other function to help the judge or update value if need, and for the d-cmp is also similar to the i-cmp.

I copy the i-cmp function and change some value, then it became d-cmp function. But I missed some value in D-Cache and unfortunately the open test case cannot test out the problem.

For each initial function, I set the entries value of each, and follow the project description said to calculate out, and we can use the value to create a cache array.

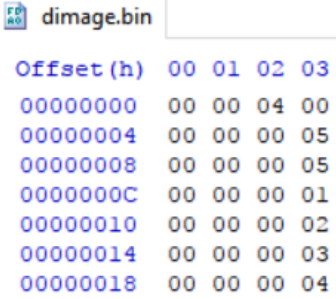
### III. Makefile part:

```
1 CC = gcc
2
3 CMP: main.o cmp.o memory.o regfile.o processor.o
4 $(CC) -o $@ main.o cmp.o memory.o regfile.o processor.o
5 main.o: main.c cmp.c cmp.h memory.c memory.h regfile.c regfile.h processor.c processor.h
6 $(CC) -c -g main.c
7 cmp.o: cmp.c cmp.h memory.c memory.h processor.c processor.h
8 $(CC) -c -g cmp.c
9 memory.o: memory.c memory.h regfile.c regfile.h
10 $(CC) -c -g memory.c
11 regfile.o: regfile.c regfile.h memory.c memory.h
12 $(CC) -c -g regfile.c
13 processor.o: processor.c processor.h cmp.c cmp.h regfile.c regfile.h memory.c memory.h
14 $(CC) -c -g processor.c
15 clean:
16 rm -f main.o cmp.o memory.o regfile.o processor.o
```

Just using a simple way to include all the file that I need to compile the target “\*.o”. And also the clean up function. Test case Design

#### 2-1) Detail Description of Test case:

1	lw	\$1, 0(\$0)	
2	addi	\$2, \$0, 1	
3	addi	\$3, \$0, 1	
4	A:add	\$2, \$2, \$3	
5	bne	\$1, \$2, A	
6	lw	\$5, 11(\$1)	
7	add	\$1, \$1, \$5	
8	B:addi	\$2, \$2, 2	
9	bne	\$1, \$2, B	
10	sub	\$6, \$1, \$2	
11	sub	\$6, \$1, \$5	
12	halt		
13	halt		
14	halt		
15	halt		



Offset (h)	00	01	02	03
00000000	00	00	04	00
00000004	00	00	00	05
00000008	00	00	00	05
0000000C	00	00	00	01
00000010	00	00	00	02
00000014	00	00	00	03
00000018	00	00	00	04

A simple test case to do test try some loop, but because my program in 1<sup>st</sup> submission having some problem, so it cannot challenge other program.