

R23 SENS_SUPPLYD 56k @ 75∀ +12V +12V +12V US1M US1M US1M D8 D2 D4 GND PWR_FLAG 🚫 U6 U7 7 OH02 10HD HO 7 (1HO3 C24 C18 H3D 2 HIN 2.2u L3D 3 LIN 2.2u L1D 3 LIN H2D-2 HIN 2.2u L2D-3 LIN C16 10u LO 5 CILO2 IRS218675 IRS21867S IRS21867S GND GND GND TX4130L 典型应用电路图: Vout: 5V/2A TX4130L: VFB: min=369 typ=380 max=391 mV VCS: min=145 typ=150 max=155 mV ON/OFF Voltage Filters R10 56 <u>@</u> 55√ □ H53 SENS_3D-R13 R15 R1 = R3 R2 = R4 Vout= VFB * (R2+R1)/R1 lout=VC5/R7 (R7=0.06R -> lo=2.6A) Frequency: Fixed 140kHz Inductance: 33UH-1000H 2k _____2k___ GND GND SWD SWD SW9WD
P5 P6 P7P8 D9 SWDSWD 6 4 4 +12V CYA0650-68UH2 R21 68uH 47u 100V C25 GND 0.1R C30 C32 TX4130L/PW2902 ∞ 10u 9 VIN 35 VSEN 7
1 VIN > VSS 5
2 VDD LDO 4 22 u R16 2 R22 5.1k 3 FB1 FB2 4 GND 1k8 R20 D5 1N4148WS 39g 819 GND 1N5819 D3 BOARD REGULATOR +5V https://datasheet.lcsc.com/lcsc/1912231403_XDS-TX4130L_C448635.pdf https://www.pwchip.com/en/product/PW2902-174.html AMS1117-3.3 U5 PWR_FLAG +12V XL1509-5.0E1 +5٧ C8 C12 C14 8 GND Vin 1 7 GND Vout 2 6 GND FB 3 CYA0650-68UH1 C10 100n C22 100u@6.3V Jens Overby GND 0/0 4 100u 100u@6.3V SWD 4 Sheet: /driver/ SW2A File: driver.kicad_sch GND SW_DPDT_x2 Title: FOC KING Size: A4 Date: Rev: GND KiCad E.D.A. kicad 6.0.2+dfsg-1 ld: 6/4

r to stance, or (5



