Laboratory 9 RL and RC Circuits

Objectives

- Observe the transient and steady-state responses of RL and RC circuits.
- Learn how to measure time constant of first order circuits.
- To learn how to simulate RL/RLC circuit with SPICE

Equipment and components

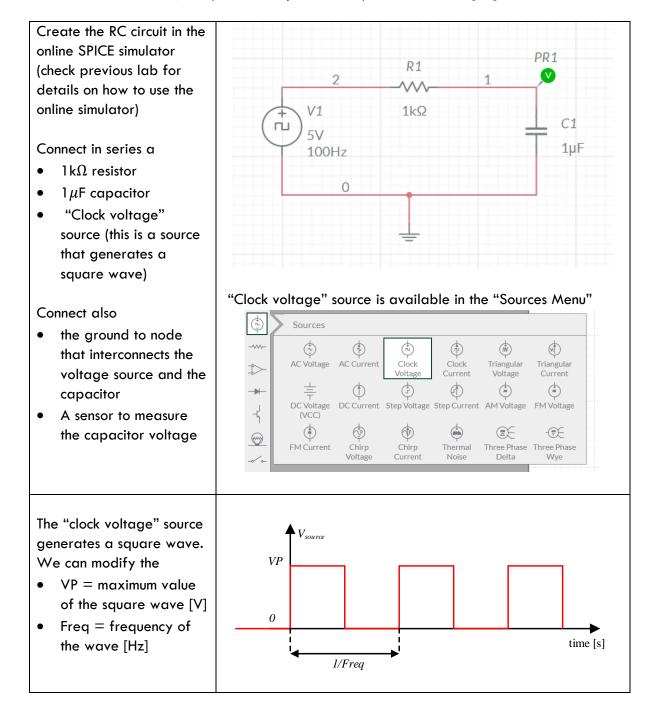
- A desk computer
- SPICE software

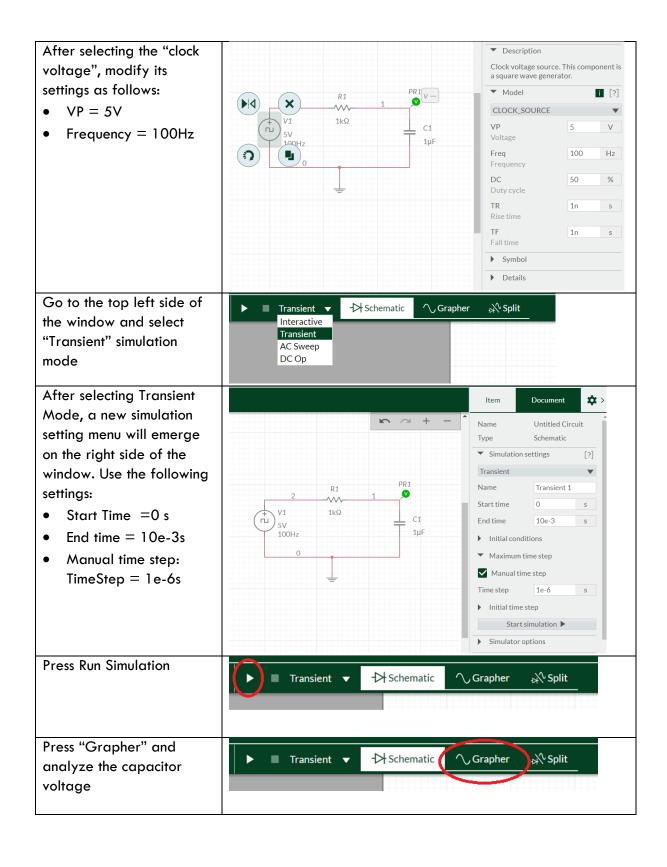
Preliminary Work

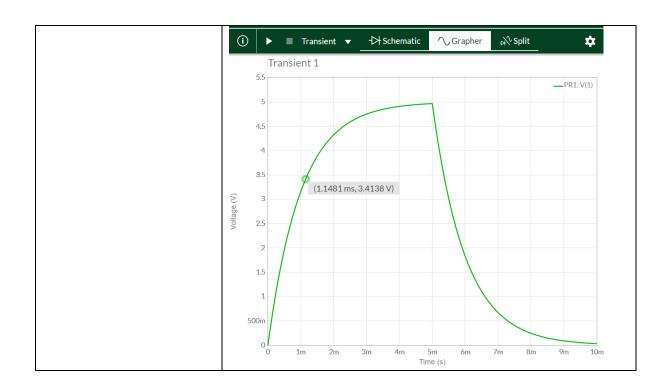
- 1. Review the lecture slides about RL/RC Circuits and Appendix 1 (included at the end of this document)
- 2. Review what you have learned in the previous labs about online SPICE simulator. We will use this tool to analyze the behavior of circuits with resistors, capacitors and inductors
- 3. Read the Lab Procedure (below) and complete the tables.

Procedure

1. The first objective of this assignment is to simulate the behavior of the RC circuit shown below with a resistor 1 k Ω , a capacitor of 1 μ F and a square wave voltage generator.







2. Measure the time constant of the RC circuit by analyzing the voltage signal available in the "Grapher Window" (see also Appendix) . Fill out the table below.

Capacitance	Resistance	Calculated Time Constant*	Measured Time Constant
1μF	1 kΩ		

3. Place two 1 μF capacitors in series. Repeat 1 and 2.

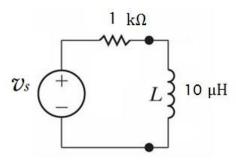
Capacitance	Resistance	Calculated Time Constant*	Measured Time Constant
	1 kΩ		

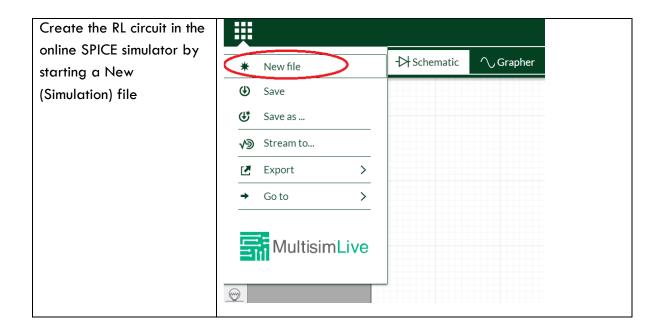
4. Place two 1 μF capacitors in parallel. Repeat 1 and 2. Note: to facilitate the measurement of the time constant you may decrease the function generator frequency to 50Hz.

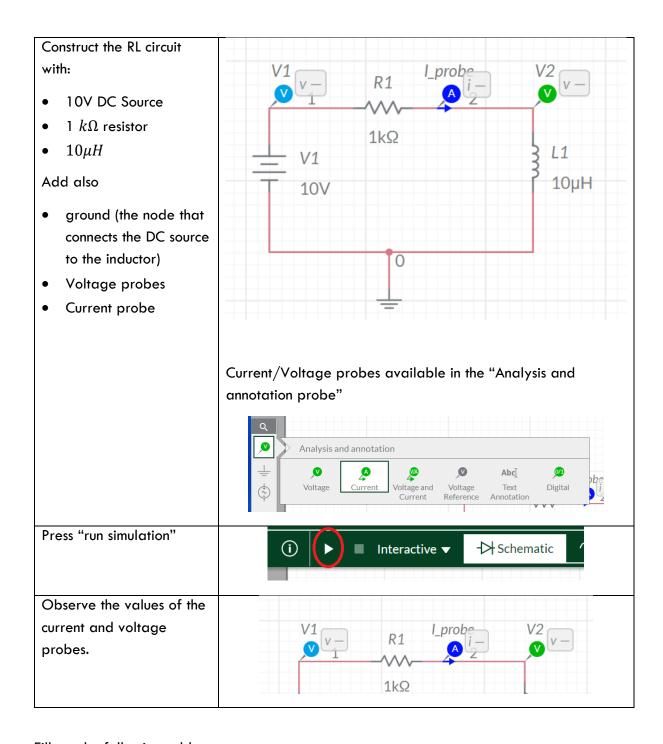
Capacitance	Resistance	Calculated Time Constant*	Measured Time Constant
	1 kΩ		

Does the time constant change with different capacitor combinations? Why?

5. Construct the RL circuit shown below with a resistor of 1 k Ω and an inductor of 10 μ H. Setup the voltage source with a constant voltage, $v_s = 10 \ V$. Measure the current in the circuit and voltages across the resistor and the inductor.





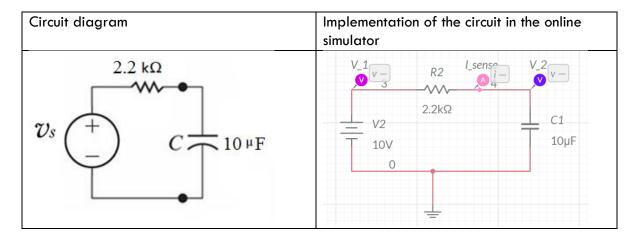


Fill out the following table:

	Calculated Value*	Measured Value
Current		
Voltage across the inductor		
Voltage across the resistor		

What did you find from the above table? Explain your results.

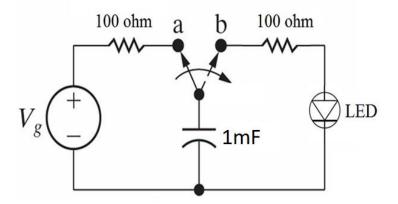
6. Construct a circuit shown below with a resistor of 2.2 k Ω and a capacitor of 10 μ F. Setup the voltage source with a constant value $v_s = 10 \ V$. Measure the current in the circuit and voltages across the resistor and the capacitor.

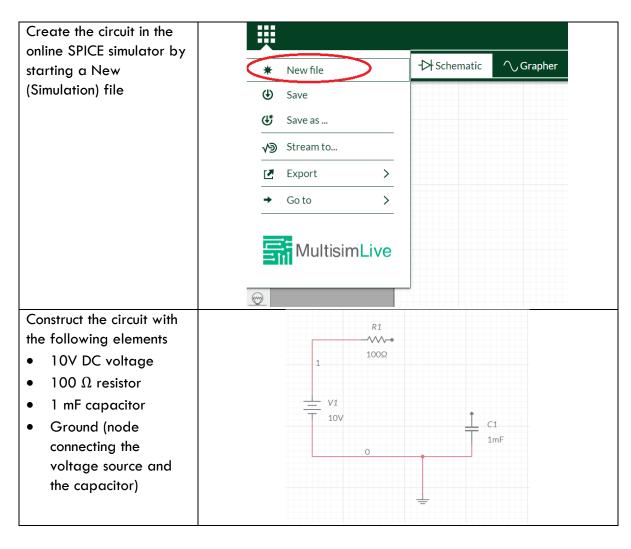


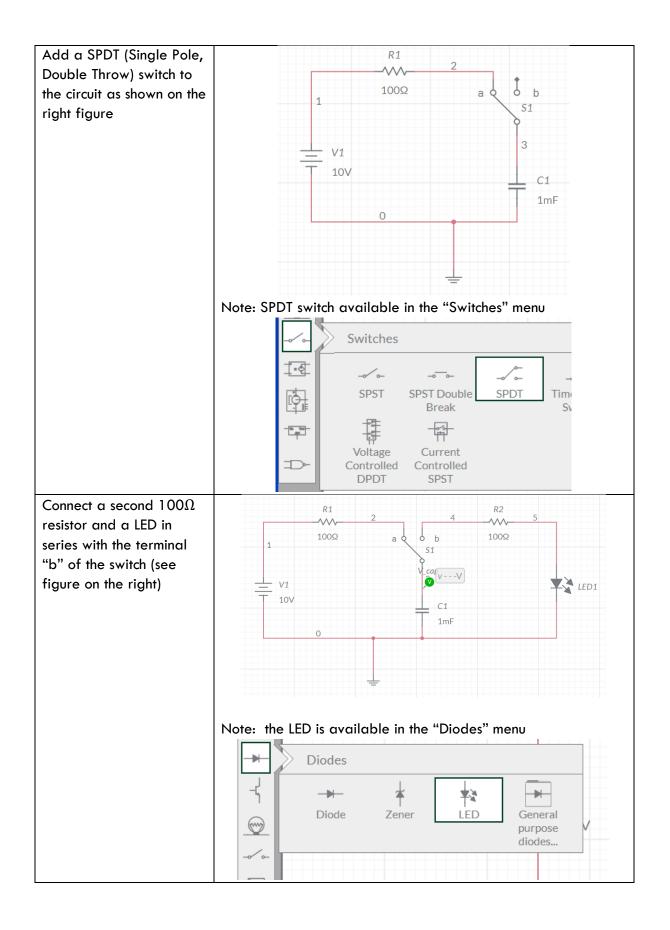
	Calculated Value*	Measured Value
Current		
Voltage across the capacitor		
Voltage across the resistor		

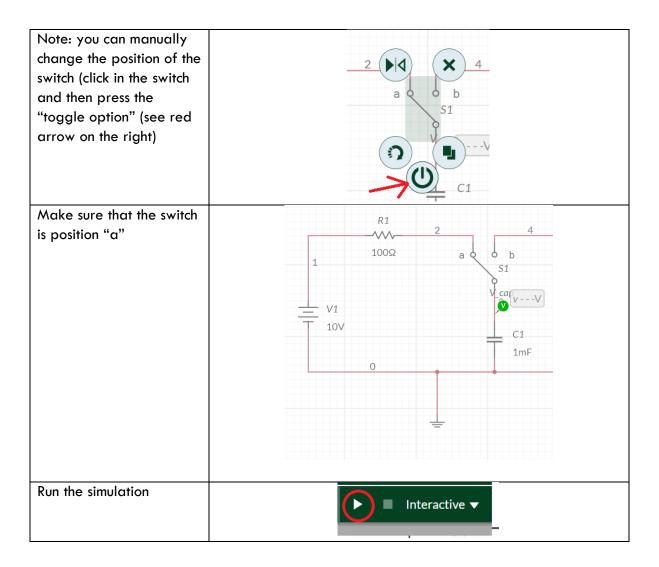
What did you find from the above table? Explain your results.

7. Construct a circuit shown below with 2.2 k Ω resistor and an LED. Setup the voltage source with $v_{\rm g}$ = 10 V.









- 8. At the beginning, the switch is placed in the position "a". What happens to the capacitor voltage?
 - 9. Then move the switcher to the position "**b**" (you can click on the switch during simulation mode to change its position). What happens to the capacitor voltage? Check also the color of the LED. Does the LED become red? Explain why.

Questions and conclusions

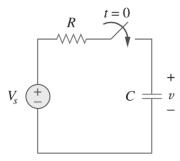
• Summarize your findings and explanations in response to the questions posed in this lab.

Appendix 1 – Measuring the Time Constant of a RC Circuit

As discussed during the ENGR065 lectures, the step response of a RC circuit can be represented by a first-order differential equation:

$$\tau \frac{dv}{dt} + v = V_s, \quad v(0) = V_0, \qquad t \ge 0$$

Where v is the capacitor voltage [V], $\tau=RC$ is the time constant [s], R the resistance value, C the capacitance [F], V_s is the source voltage [V] and V_0 the initial capacitor voltage [V].



If we assume $V_0=0\ V$, the solution of this equation is given by

$$v(t) = -V_S e^{-t/\tau} + V_S = V_S (1 - e^{-\frac{t}{\tau}})$$

The resulting voltage waveform is depicted in the following figure.

After $t=\tau$ seconds, the capacitor voltage reaches $v(t=\tau)=V_s(1-e^{-1})\approx 0.63V_s$. By measuring the time it takes to reach $0.63V_s$ you will be able to estimate the time constant τ (see figure below).

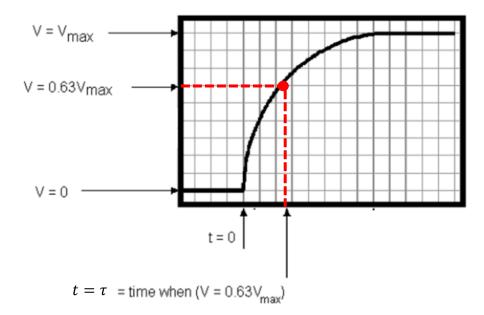


Figure – Capacitor voltage waveform; note that $V_{max} = V_s$ (Source: https://www.webassign.net/question_assets/unccolphyseml1/lab_3/manual.html)