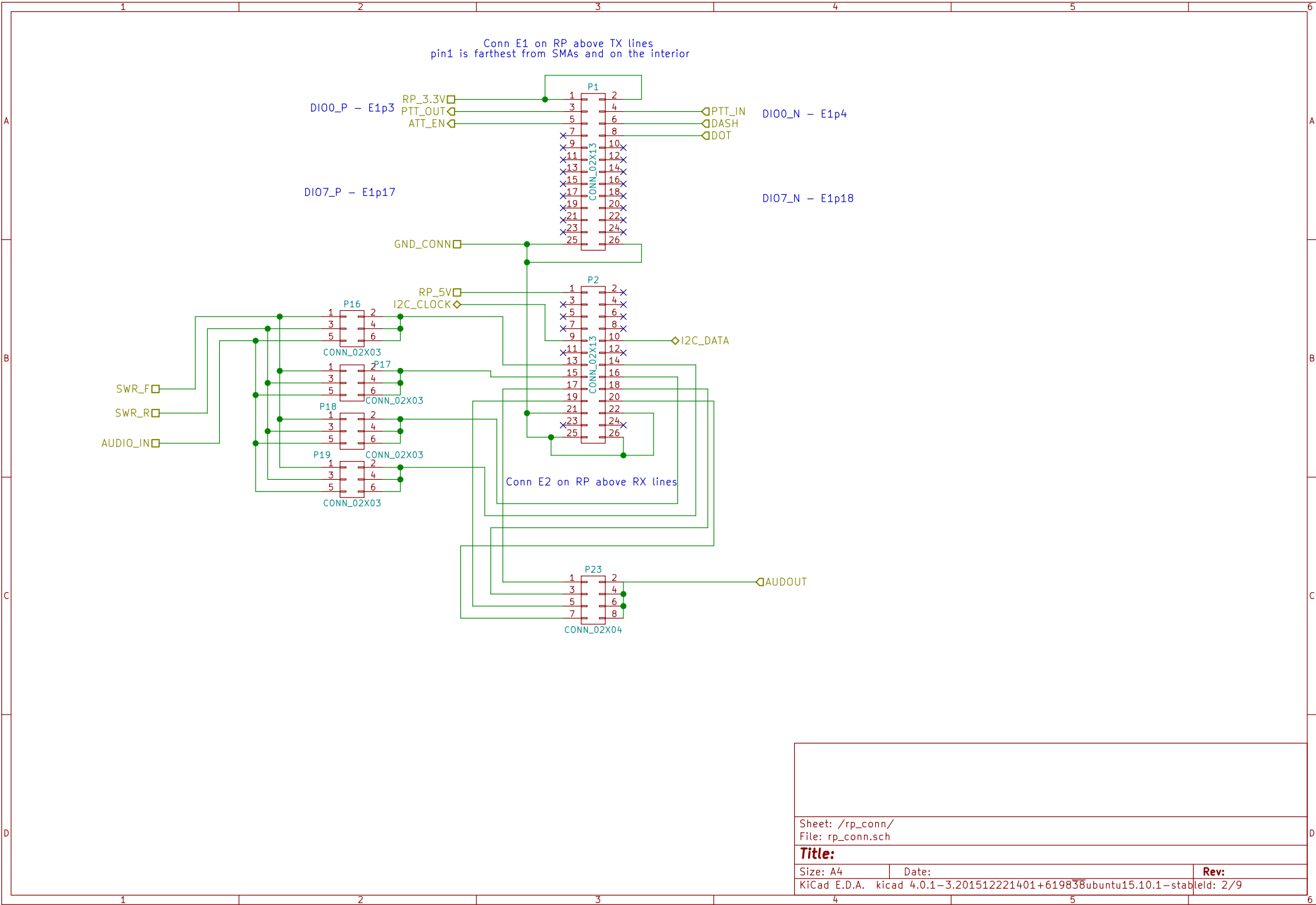


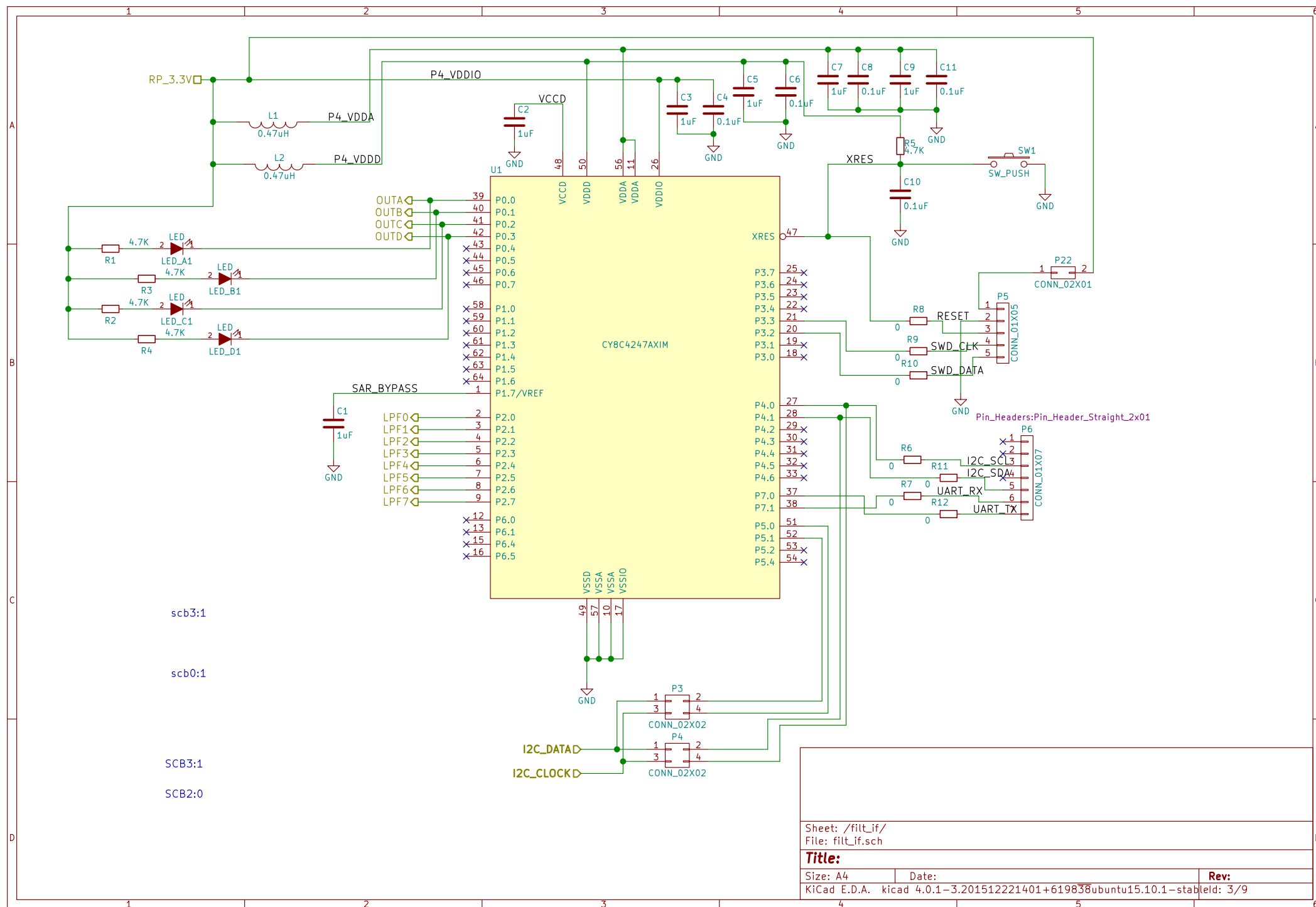
Red Pitaya SDR Interface
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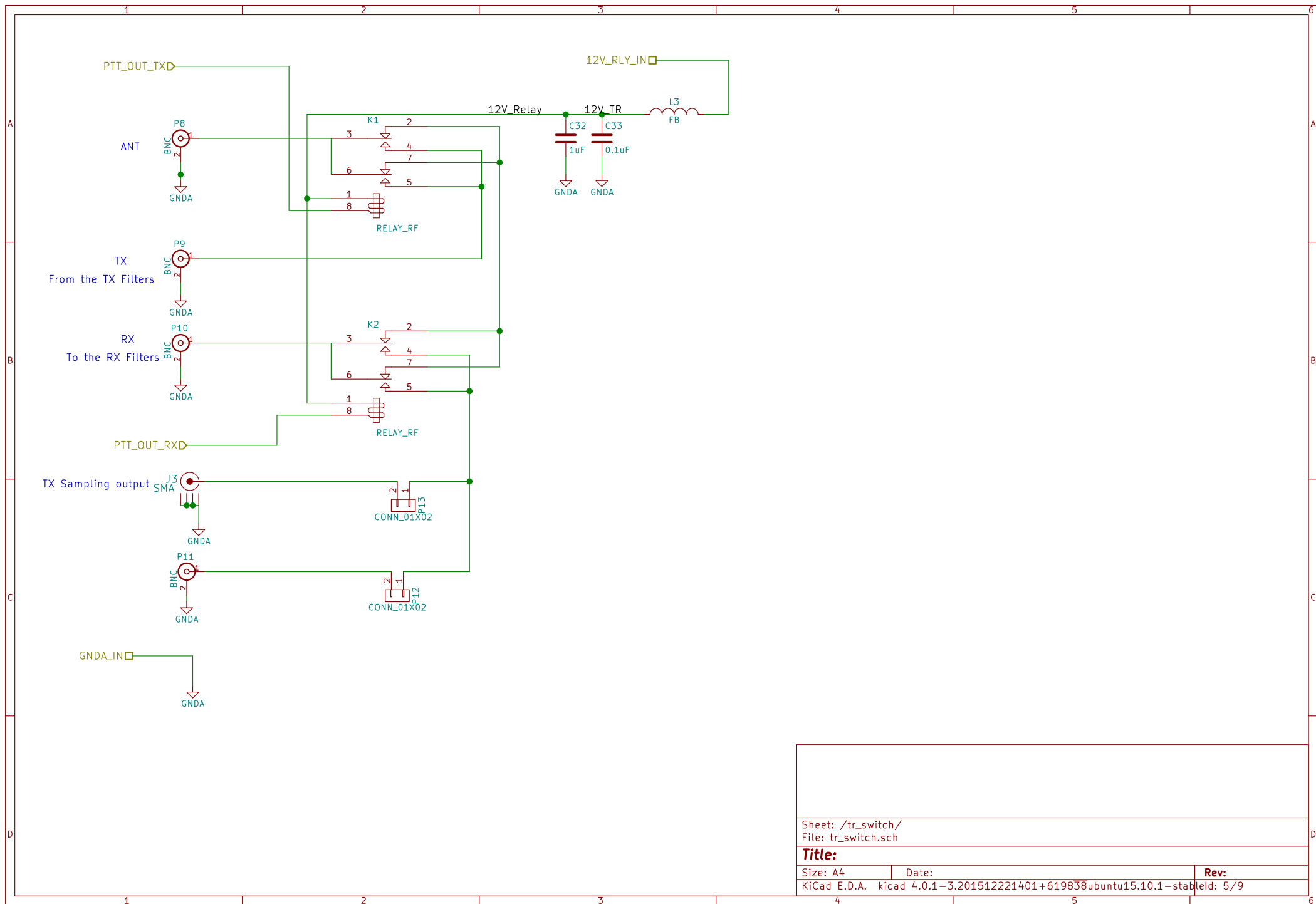
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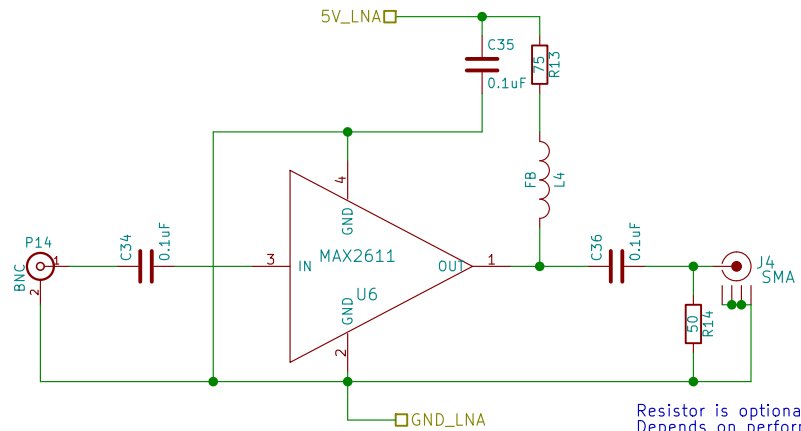
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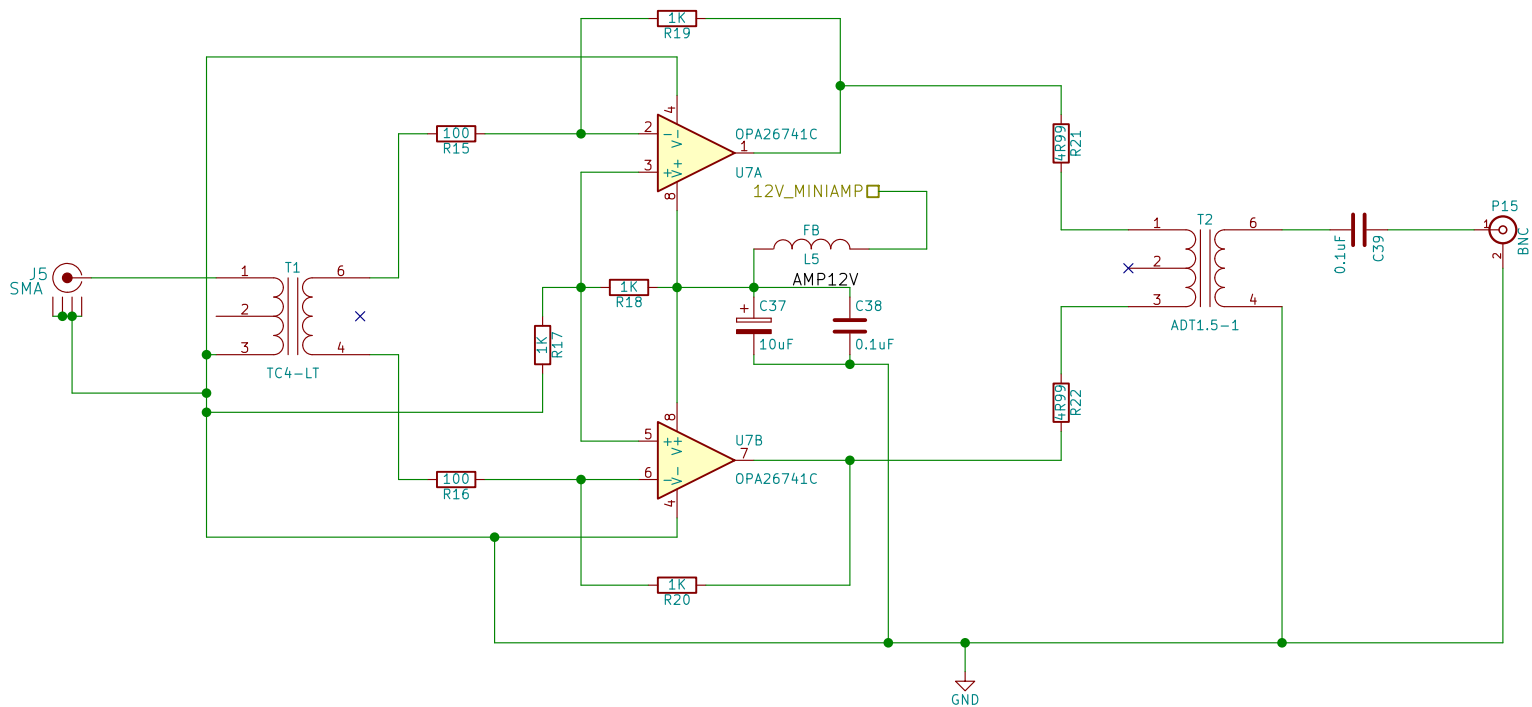




$C_{block} = 53000 / F$ where F is the minimum pass frequency
 $I_d = V_{cc} - V_{dd} / R_{bias}$ with $V_{dd} = 3.8V$ nominal (16ma nominal)
 Blocking Cap should have less than 30hms reactance at 0.5KHz

Resistor is optional
 Depends on performance into
 UnUn

Sheet: /lna/		
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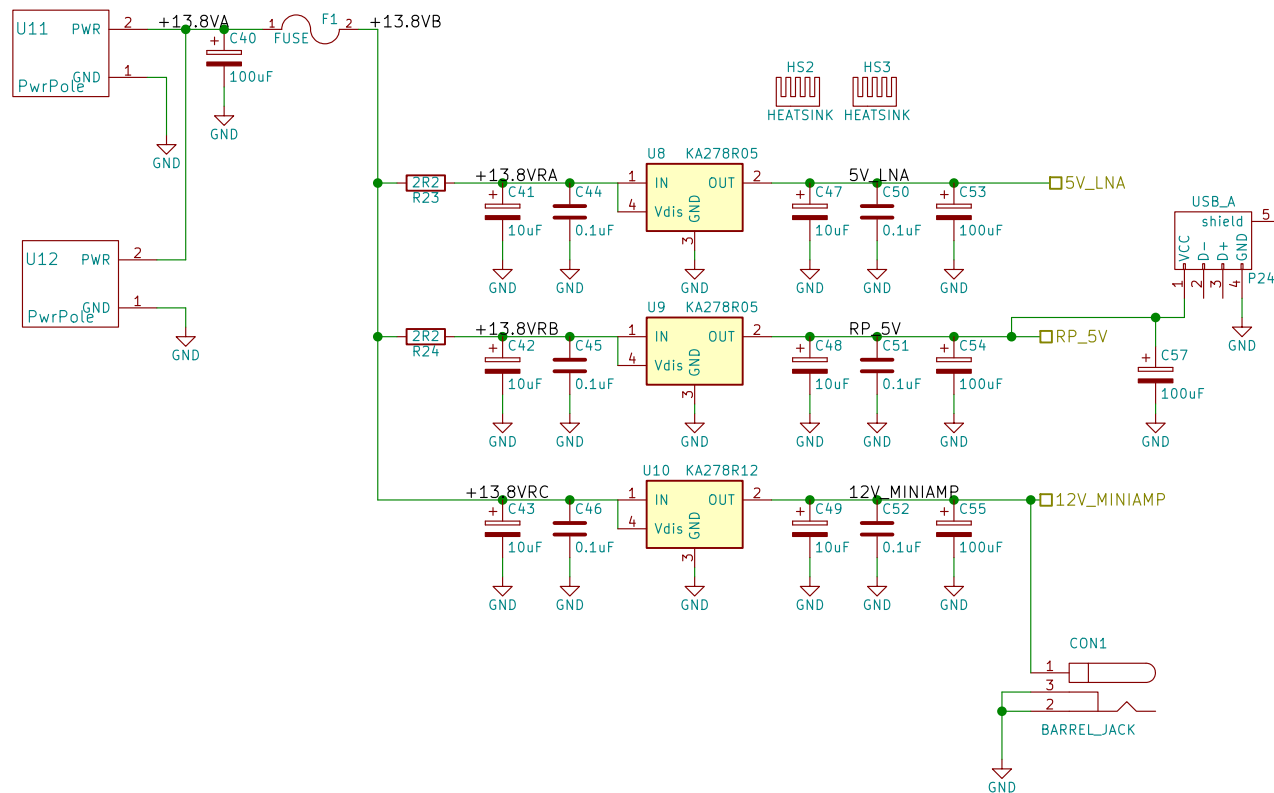
0.5W RF Amplifier – From HPSDR Pennylane

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Title:

Size: A4 Date: Rev:
KiCad E.D.A. kicad 4.0.1-3.201512221401+619838ubuntu15.10.1-stableId: 7/9

13.8V In - 5V DC, 5V Ana, and 12V Digital



RP Adapter Power Supply
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Sheet: /ps/
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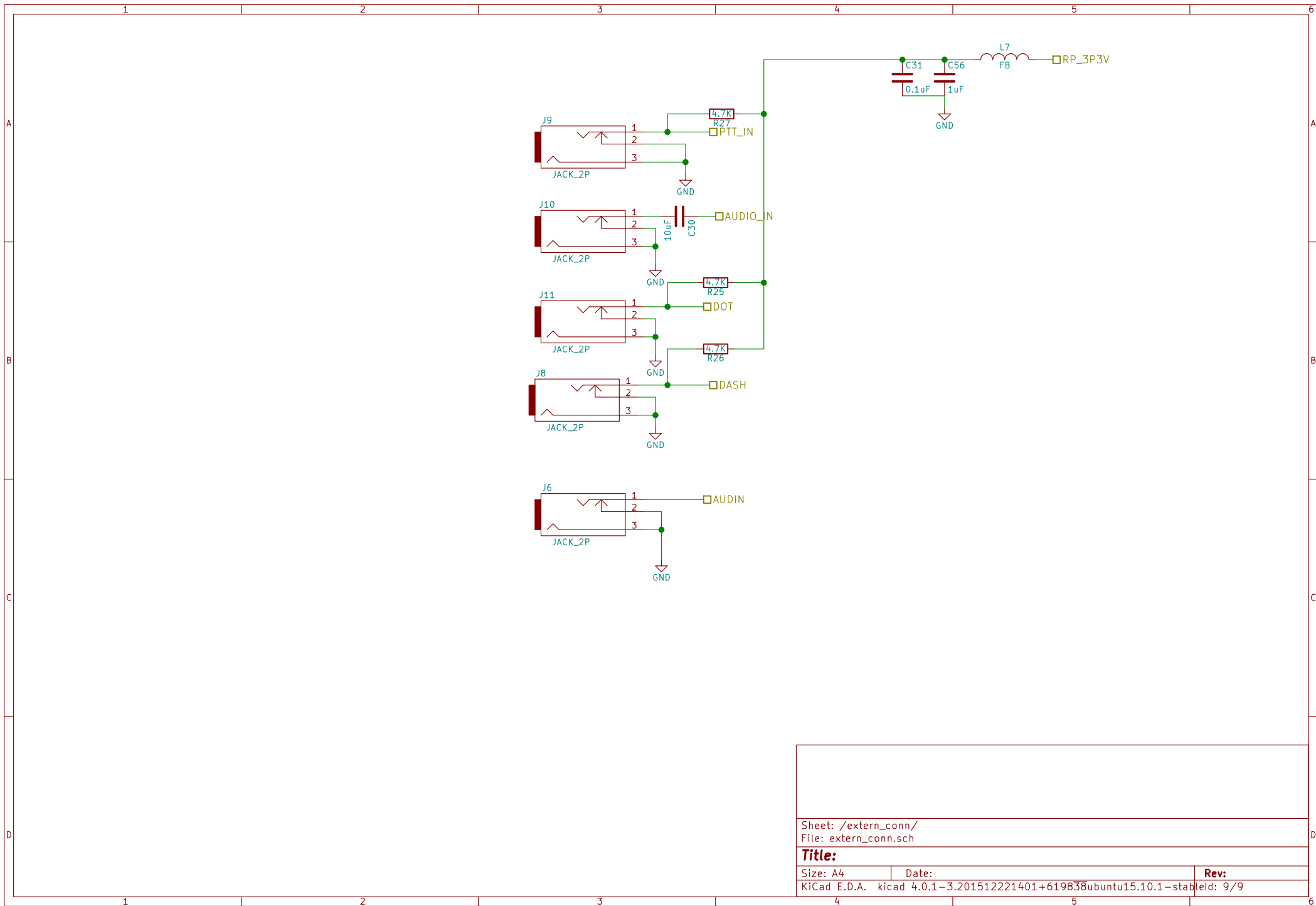
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