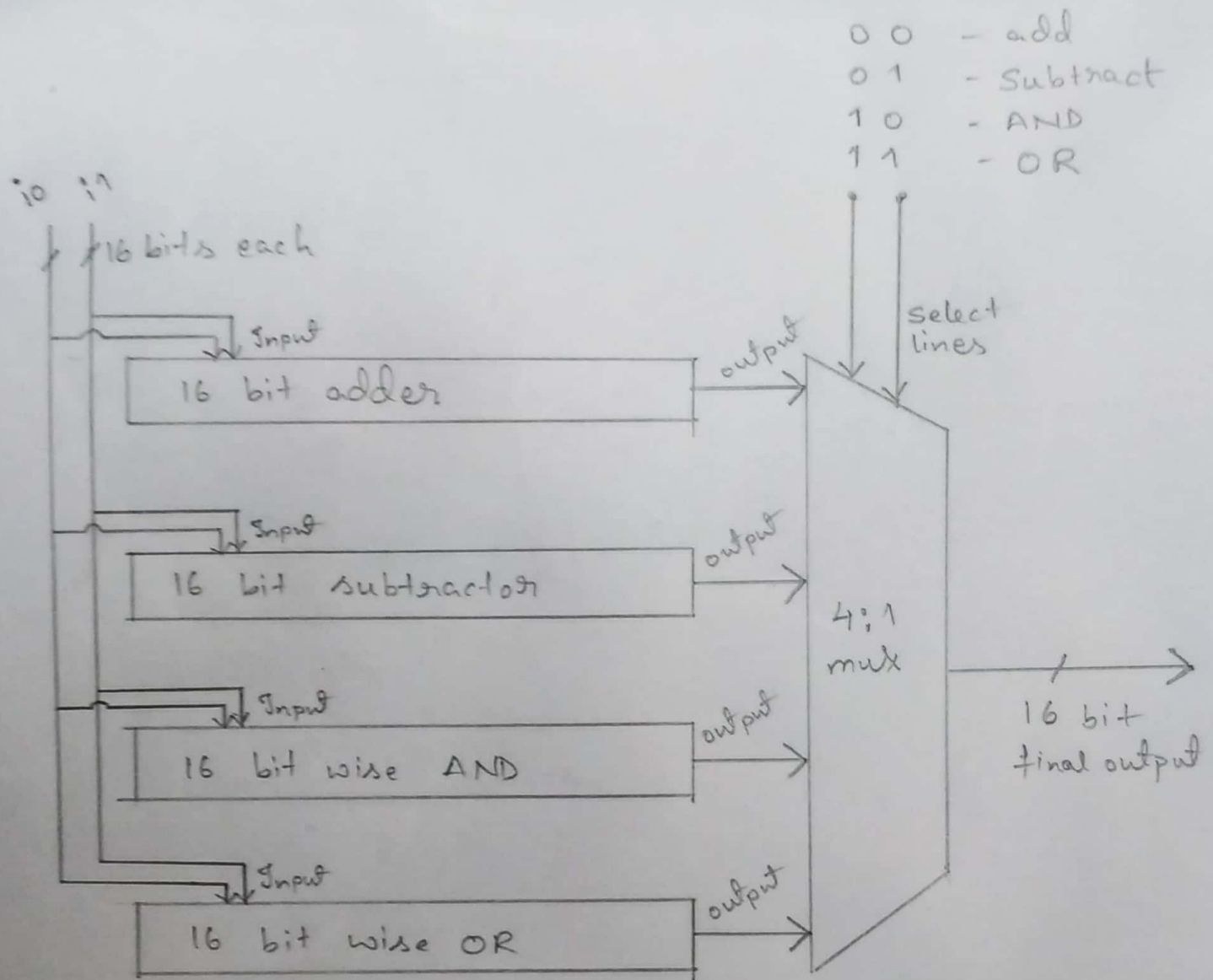


Slices 2 to 16 are all identical, only Slice 1 will have its "cin" wire same as  $op[0]$ .



This mux is 4:1,  
each input is 16 bits  
and output is also 16 bits.