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2559-2971

Function Generator Serial D2A Micro-Controller Applications

Introduction

The processor used for this module is the Atmel ATmega324P. This is an 8-bit processor that is programmed through the usage of the Atmel Ice, using the in-system programming method (functionally an SPI system). This microcontroller has a 1 MHz clock, 1k bytes of EEPROM, and 2K bytes of internal SRAM, in addition to 4 I/O ports, multiple timer/counters, and more. The goal of the design with this microcontroller is to output the digital data that represents a waveform to an external DAC, which generates the analog waveform. The microcontroller can output multiple different waveform types, and can also modulate the frequency and amplitude, all based upon user inputs.

Design

For the hardware, the Atmel Ice is used as the programmer for the ATmega device. This is connected to the ATmega as shown in Figure 1 in the appendix. A 4.7k pull-up resistor is used to connect the reset in place of a wire. In Atmel Studio 7, the interface mode is set to ISP for serial communication. A 5V supply is used to supply power to the circuit, which also represents the voltage level for logical high values. A layout of the design is provided in Figure 2, with the actual board shown in Figure 3. A potentiometer is used to adjust the frequency of the output waveform between 10 Hz and 100 Hz, and another is used to adjust the output voltage peak between 1V max and 5V max. Two switches are used to alternate between the four waveforms: sawtooth, sine, triangle, and square wave.

For the software, a flowchart of the code is shown in Figures 4 through 5. It works as follows: the four lookup tables for the waveforms are stored in memory. Easily accessible global variables are initialized for various parameters, such as the amplitude and period offsets. Port D is set as an input for the switches, as is port A. The SPI and the ADC are initialized. A frequency shift value is set based upon one ADC, and the amplitude shift value is set based upon the other. Math is done to calculate the necessary delay time and amplitude scaling factor, and the waveform to use is determined based upon the switch inputs. The data to write to the DAC is calculated based upon the amplitude scaling factor and the next value in the lookup table, then the data is written to the DAC, and a delay occurs to account for the frequency. The full code is provided in Figures 6 – 14. The bill of materials is shown in Table 1, with the total cost coming to \$107.48.

Conclusion

The amplitude range was measured to be about 1.004V to 5.12V, both being within the 5% error range. Similarly, the frequency ranged from 9.96 Hz to 102.01 Hz, also within the error range, as shown in Figures 15-20. These values held up for all four waveform options. The difficulty creating the function generator lay in generating the proper frequency and amplitude for the output waveform: the CKDIV8 programmable fuse was turned off, in addition to varying the values of F_CPU, the amplitude scaling factor, and the frequency scaling factor. Once these values were determined through trial and error, the rest was simple to execute. The SPI with the external DAC is straightforward as is receiving inputs from the switches and the potentiometers.

Appendix

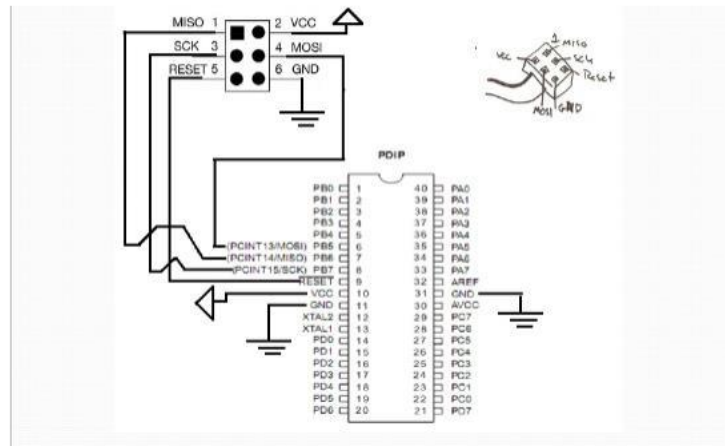


Figure 1: Pin Layout for Connecting Atmel Ice to ATmega324P, Red Wire is SCK

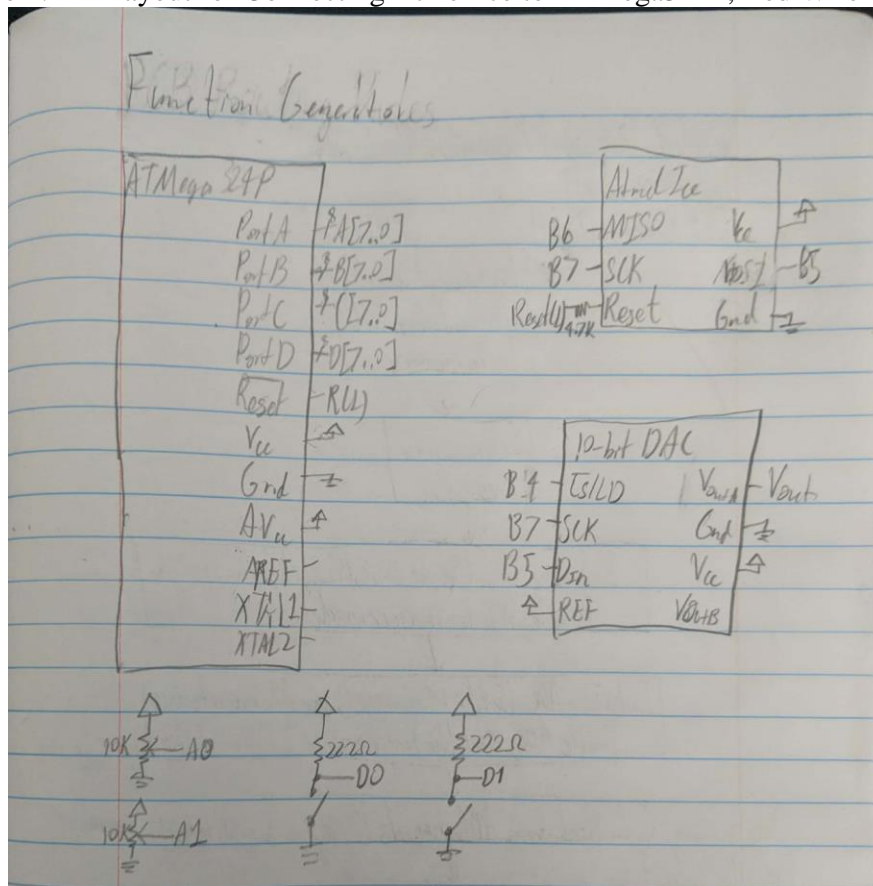


Figure 2: Hardware Connection Block Diagram of the Complete Circuit

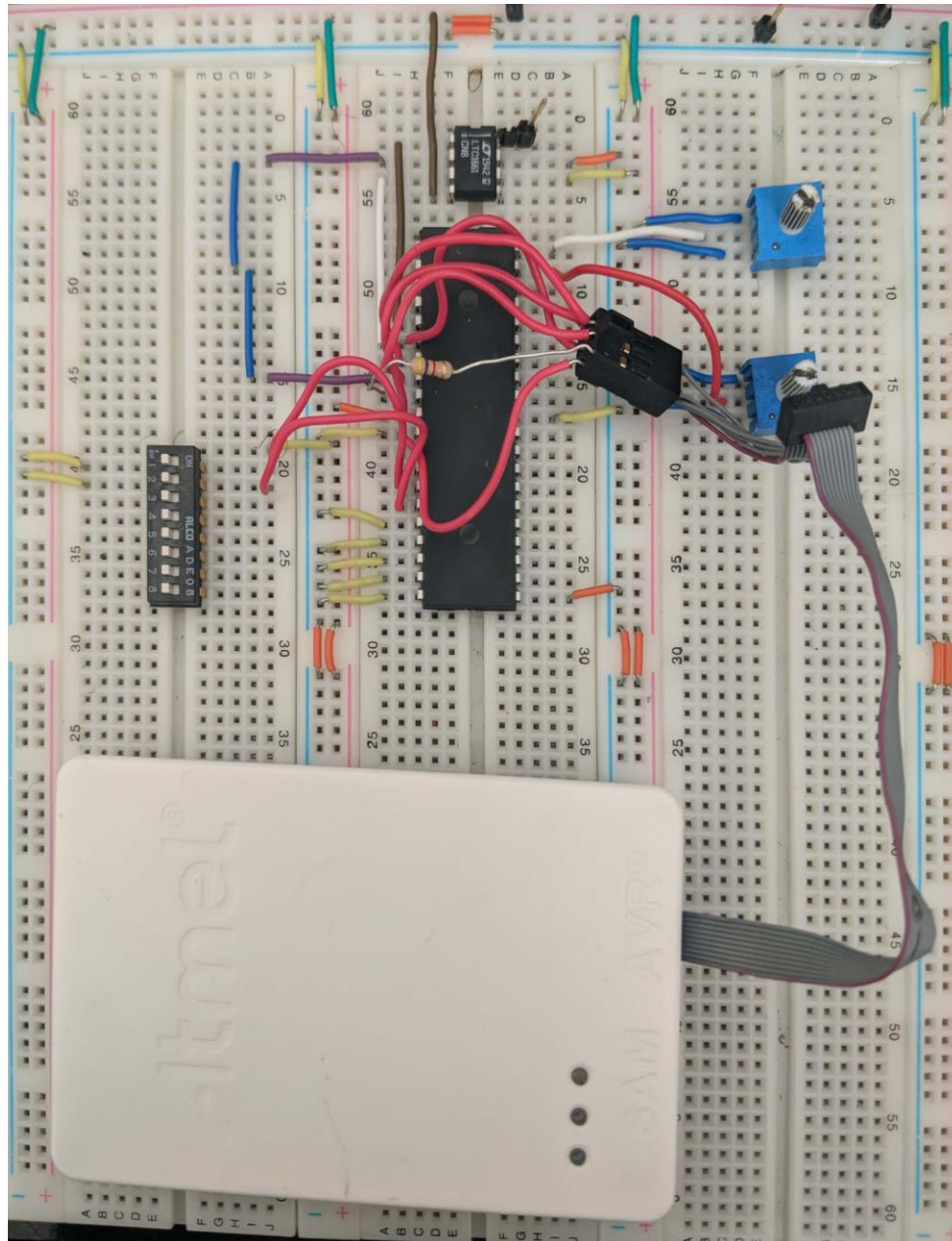


Figure 3: Actual Circuit Showing Atmel Ice (Bottom), ATmega324P (Center), and DAC (Top)

Selected Debugger/Programmer: Atmel-ICE
Interface: ISP

ISP Clock: 125 kHz CKDIV8 off

Software Flowchart

Store lookup tables
in memory

Set necessary
variables

Initialize SPI (Set SCK, MOSI, MISO pins, make SS high,
Enable SPI in master mode)

Initialize ADC (Set Port A as an input, use AVcc as a reference,
Enable ADC, start the first conversion, prescaler of 128)

Once in main
Every 150 cycles
in infinite loop { Read from ADC channels (frequency & amplitude)
Scale frequency & amplitude factors as needed

Determine which lookup table value to use
from switch inputs

Enable DAC, write using SPI, disable DAC

Figure 4: Software Flowchart, Page 1

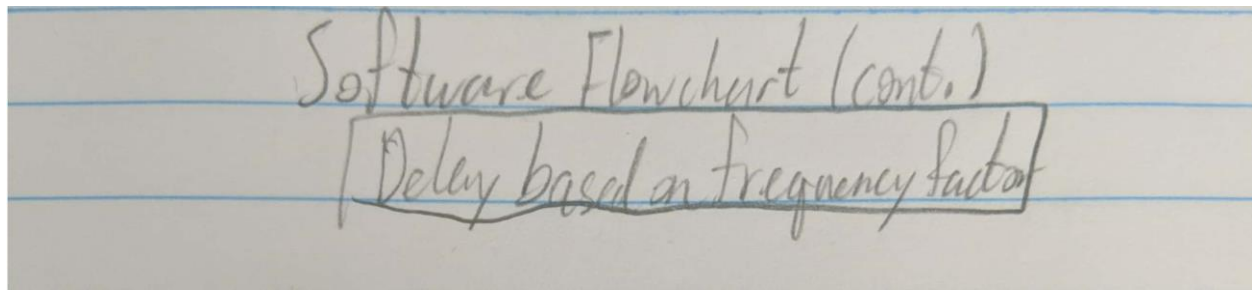


Figure 5: Software Flowchart, Page 2

```

1  /*
2   * FunctionGenerator.c
3   *
4   * Created: 10/3/2018 8:25:26 AM
5   * Author : Kadeem
6   */
7
8  #define F_CPU 98000 //1 MHz system clock
9
10 #include <avr/io.h>
11 #include <util/delay.h>
12
13 //Table for sine values
14 const uint16_t sineTable[] = {
15     0x200,0x232,0x264,0x295,0x2c4,0x2f1,0x31c,0x345,
16     0x36a,0x38c,0x3aa,0x3c4,0x3d9,0x3ea,0x3f6,0x3fe,
17     0x3ff,0x3fe,0x3f6,0x3ea,0x3d9,0x3c4,0x3aa,0x38c,
18     0x36a,0x345,0x31c,0x2f1,0x2c4,0x295,0x264,0x232,
19     0x200,0x1ce,0x19c,0x16b,0x13c,0x10f,0xe4,0xbb,
20     0x96,0x74,0x56,0x3c,0x27,0x16,0xa,0x2,
21     0x0,0x2,0xa,0x16,0x27,0x3c,0x56,0x74,
22     0x96,0xbb,0xe4,0x10f,0x13c,0x16b,0x19c,0x1ce
23 };
24

```

Figure 6: Program Code, Page 1


```

25  const uint16_t squareTable[] = {
26      0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,
27      0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,
28      0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,
29      0x0,0x0,0x0,0x0,0x0,0x0,0x0,0x0,
30      0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,
31      0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,
32      0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,
33      0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,0x3ff,
34      0x3ff,0x3ff,0x3ff,0x3ff
35  };
36  const uint16_t triangleTable[] = {
37      0x20,0x40,0x60,0x80,0xa0,0xc0,0xe0,0x100,
38      0x120,0x140,0x160,0x180,0x1a0,0x1c0,0x1e0,0x200,
39      0x220,0x240,0x260,0x280,0x2a0,0x2c0,0x2e0,0x300,
40      0x320,0x340,0x360,0x380,0x3a0,0x3c0,0x3e0,0x3ff,
41      0x3e0,0x3c0,0x3a0,0x380,0x360,0x340,0x320,0x300,
42      0x2e0,0x2c0,0x2a0,0x280,0x260,0x240,0x220,0x200,
43      0x1e0,0x1c0,0x1a0,0x180,0x160,0x140,0x120,0x100,
44      0xe0,0xc0,0xa0,0x80,0x60,0x40,0x20,0x0
45  };
46  const uint16_t sawtoothTable[] = {
47      0x0,0x10,0x20,0x30,0x40,0x50,0x60,0x70,0x80,
48      0x90,0xa0,0xb0,0xc0,0xd0,0xe0,0xf0,0x100,

```

Figure 7: Program Code, Page 2

```

49      0x110,0x120,0x130,0x140,0x150,0x160,0x170,0x180,
50      0x190,0x1a0,0x1b0,0x1c0,0x1d0,0x1e0,0x1f0,0x200,
51      0x210,0x220,0x230,0x240,0x250,0x260,0x270,0x280,
52      0x290,0x2a0,0x2b0,0x2c0,0x2d0,0x2e0,0x2f0,0x300,
53      0x310,0x320,0x330,0x340,0x350,0x360,0x370,0x380,
54      0x390,0x3a0,0x3b0,0x3c0,0x3d0,0x3e0,0x3f0
55  };
56
57  //Using ADC0 for voltage reference, and ADC1 for frequency
58  void ADC_init(void);
59  uint16_t ADC0_read(void);
60  uint16_t ADC1_read(void);
61  void spi_init(void);
62  void spi_write(uint16_t finalData);
63  void delays(int delayTime);
64
65  //Global variables
66  /*Use two switches to determine the waveform
67      0b00: Sine wave
68      0b01: Square wave
69      0b10: Triangle wave
70      0b11: Sawtooth wave
71  */
72  //Amplitude is between 1 and 5

```

Figure 8: Program Code, Page 3

```

73 //Vary value of ampShift between 0 and 4 based upon ADC1
74 //Add 1, and divide the lookup value by this
75 double volatile amplitude = 2;
76 double volatile ampShift = 0;
77 //Frequency varies from 10Hz to 100Hz
78 //Vary value of freqShift between 0 and 90 based upon ADC0
79 //Add 10, and divide the lookup value by this
80 double volatile period = 58;
81 double volatile freqShift = 0;
82 int volatile peak = 64;
83 int main(void)
84 {
85
86     //Variable to know when interrupt occurred
87     uint8_t volatile counter = 0;
88     uint8_t volatile counter2 = 0;
89     double volatile fShift = 0;
90     double volatile aShift = 0;
91     DDRD = 0; //Use PortD as an input for the switches, on 0 and 1
92     spi_init(); //Initialize SPI
93     ADC_init(); //Initialize ADC
94     double volatile data = 0;
95     int volatile delayTime = 0;
96     fShift = ADC0_read();

```

Figure 9: Program Code, Page 4

```

97     freqShift = (period + period*(fShift/1024));
98     delayTime = (int)(freqShift);
99     aShift = ADC1_read();
100     ampShift = (uint16_t)(amplitude + 4*(aShift/1024));
101     uint16_t volatile finalData = (uint16_t)(data / ampShift);
102     uint8_t volatile switches = 0;
103     while (1)
104     {
105         if(counter2 == 150)
106         {
107             counter2 = 0;
108             fShift = ADC0_read();
109             freqShift = (period*(1 + 15*((fShift-25)/1024)));
110             delayTime = (int)(freqShift);
111             aShift = ADC1_read();
112             ampShift = (1 + 5*(aShift/1024));
113         }
114         switches = PIND;
115         switch (switches)
116         {
117             case 0x00:
118                 data = sineTable[counter];
119                 break;
120             case 0x01:

```

Figure 10: Program Code, Page 5

```

121         data = squareTable[counter];
122         break;
123     case 0x02:
124         data = triangleTable[counter];
125         break;
126     case 0x03:
127         data = sawtoothTable[counter];
128         break;
129     default:
130         break;
131     }
132     finalData = (uint16_t)(data / ampShift);
133     ++counter;
134     ++counter2;
135     spi_write(finalData);
136     if (counter == peak)
137     {
138         counter = 0;
139     }
140     if (switches == 0x01)
141     {
142         delays(delayTime+14);
143     } else {
144         delays(delayTime);
145     }

```

Figure 11: Program Code, Page 6

```

145     }
146 }
147 }
148
149 void delays(int delayTime)
150 {
151     while (0 < delayTime)
152     {
153         _delay_us(82);
154         --delayTime;
155     }
156 }
157
158 void spi_init(void)
159 {
160     DDRB = 0x80; ///SCK, MOSI, and SS as outputs
161     PORTB = 0x10; //Make SS high
162     SPCR0 = (1<<SPE0)|(1<<MSTR0); //Enable the SPI, master mode, no interrupts
163 }
164
165 void spi_write(uint16_t finalData)
166 {
167     uint8_t volatile data = 0;
168     PORTB = 0x00; //Drive SS low

```

Figure 12: Program Code, Page 7


```

169 //Load control code and 4 bits of input data
170 SPDR0 = (0x90)|(uint8_t)(finalData>>6);
171 while((SPSR0 & 0x80) != 0x80);
172 //Load latter 6 bits of data
173 data = SPDR0;
174 SPDR0 = (uint8_t)(finalData<<2);
175 while((SPSR0 & 0x80) != 0x80);
176 data = SPDR0;
177 //Drive SS pin high again
178 PORTB = 0x10;
179 }
180
181 void ADC_init(void)
182 {
183     DDRA = 0x80; //PA is all inputs
184     DIDR0 = (1<<ADC0D); //Disable digital input buffer on PA0 to reduce power consumption
185     ADMUX = (1<<REFS0)|(0x0); //AVcc pin as reference, right adjusted format, gain of 1, using ADC0
186     ADCSRA |= (1<<ADEN)|(1<<ADPS2)|(1<<ADPS1)|(ADPS0); //Enable the ADC, and start the first conversion, prescaler of 128
187 }
188
189 //For frequency
190 uint16_t ADC0_read(void)
191 {
192     ADMUX = (1<<REFS0)|(0x0); //Using ADC0

```

Figure 13: Program Code, Page 8

```

193     ADCSRA |= (1<<ADSC);
194     while(ADCSRA & (1<<ADSC));
195     //Continuously poll the flag
196     return (ADC);
197 }
198
199 //For amplitude
200 uint16_t ADC1_read(void)
201 {
202     ADMUX = (1<<REFS0)|(0x1); //Using ADC1
203     ADCSRA |= (1<<ADSC);
204     while(ADCSRA & (1<<ADSC));
205     //Continuously poll the flag
206     return (ADC);
207 }

```

Figure 14: Program Code, Page 9

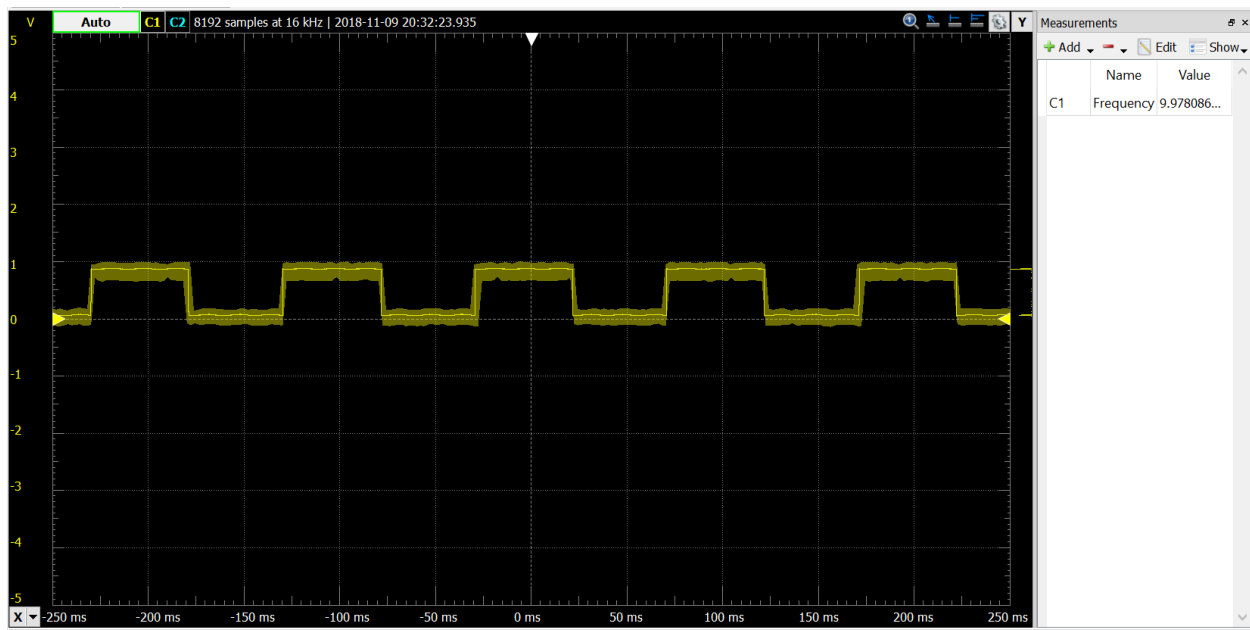


Figure 15: 10 Hz Square Wave Output, 1Vpp

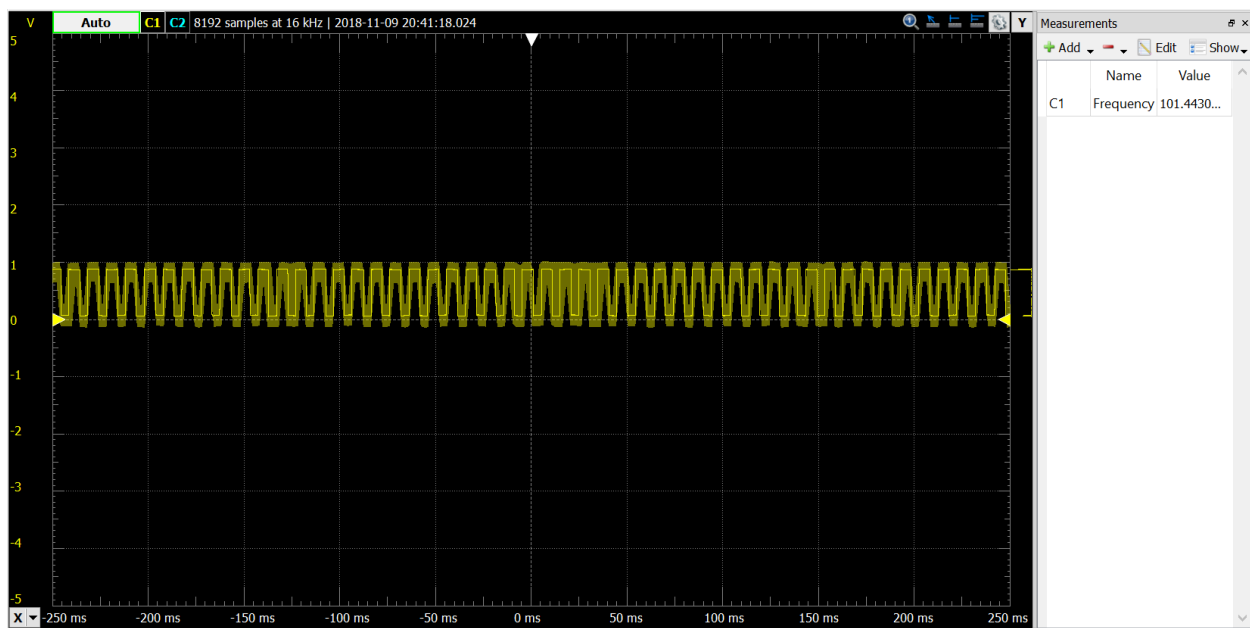


Figure 16: 100Hz Square Wave Output, 1Vpp

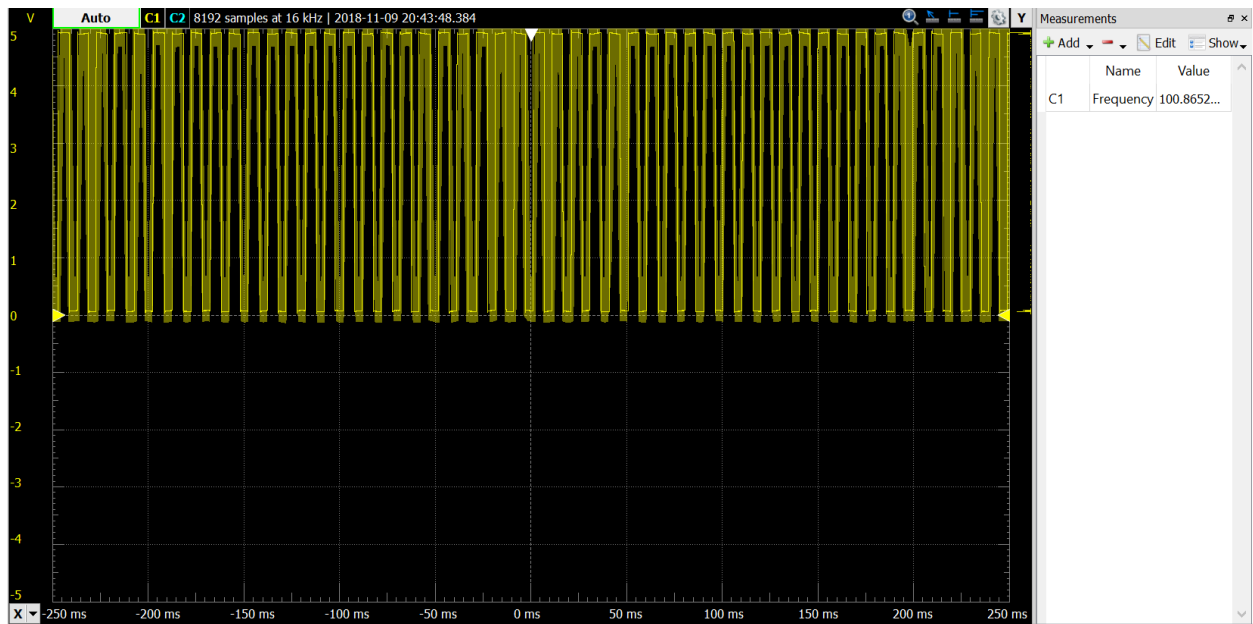


Figure 17: 100Hz Square Wave Output, 5Vpp

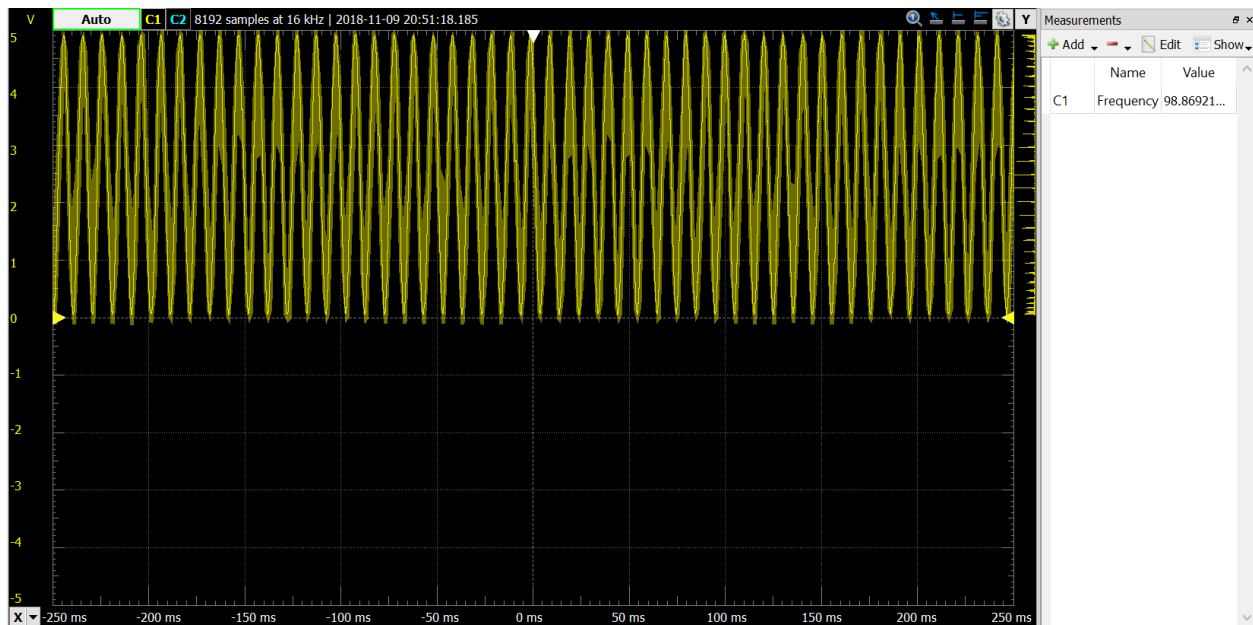


Figure 18: 100Hz Sine Wave Output, 5Vpp

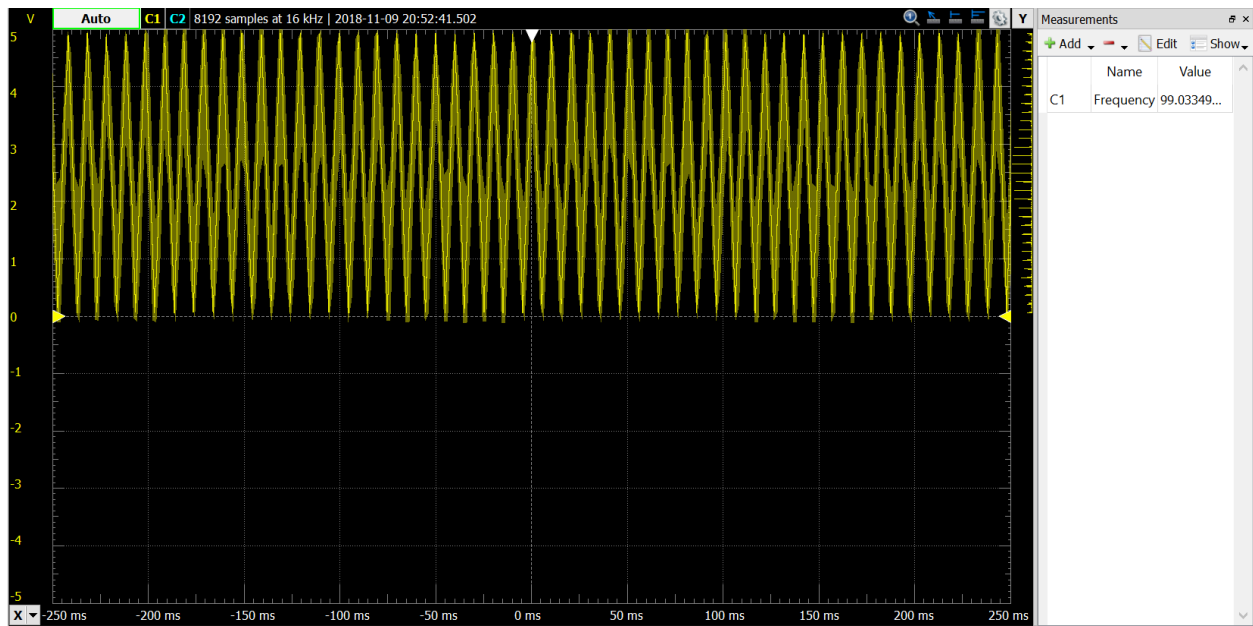


Figure 19: 100Hz Triangle Wave Output, 5Vpp

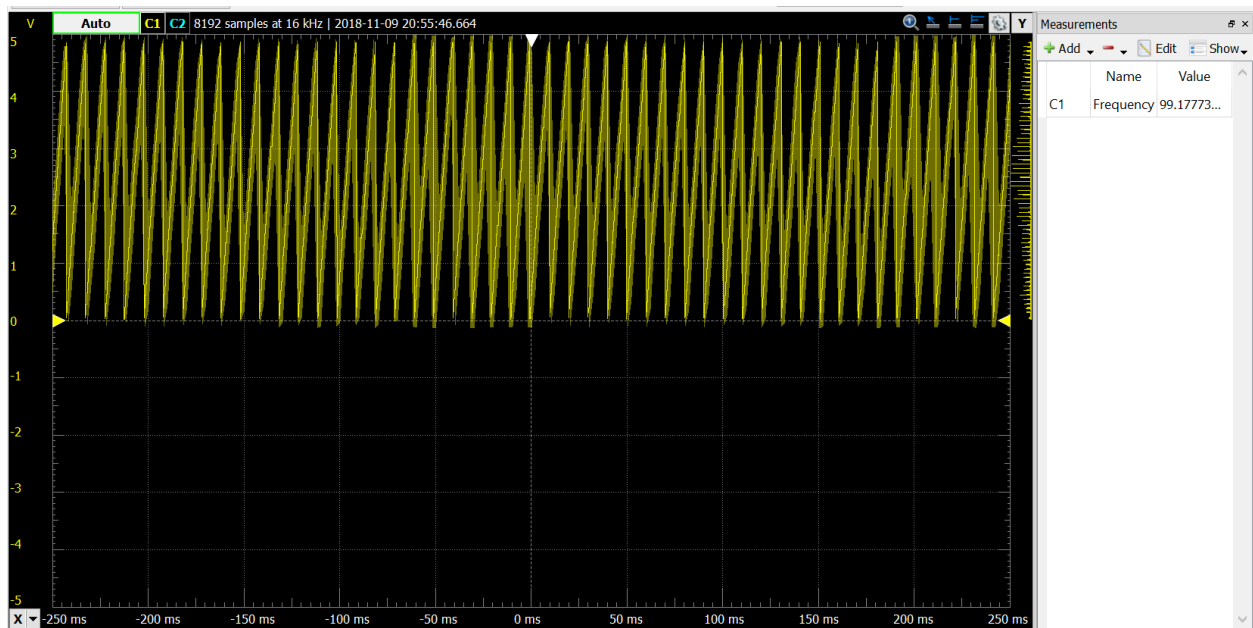


Figure 20: 100Hz Sawtooth Wave Output, 5Vpp

Part Number	Part Name	Cost per Part	Volume Discount (Price per unit for 100 units)	Source
1	ATMega324P	\$4.87	\$4.04	Mouser Electronics
2	Atmel Ice with Connector	\$93.61	N/A	Digi-Key Electronics
3	4.7K Resistor	\$0.10	\$0.04	Jameco Electronics
4	Breadboard with Wires	\$3.59	N/A	Amazon
5	LTC1661 DAC	\$3.46	\$1.79	DigiKey
6	10K Potentiometer (x2)	\$0.57	\$0.45	Mouser Electronics
7	10K Resistor	\$0.25	\$0.22	Jameco Electronics
8	222 Ohm Resistor	\$0.12	N/A	Galco Industrial Electronics
9	CTS Switch Array	\$0.91	\$0.76	DigiKey
Total		\$107.48		

Table 1: Bill of Materials