Bibliography `ka.bib`

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January 12, 2016

Blue denotes clickable links.

References

[1] K. Akbudak and C. Aykanat. Simultaneous input and output matrix partitioning for outer-product–parallel sparse matrix-matrix multiplication. *SIAM Journal on Scientific Computing*, 36(5):C568–C590, 2014.

[2] K. Akbudak, E. Kayaaslan, and C. Aykanat. Hypergraph partitioning based models and methods for exploiting cache locality in sparse matrix-vector multiplication. *SIAM Journal on Scientific Computing*, 35(3): C237–C262, 2013.

[3] M. Karsavuran, K. Akbudak, and C. Aykanat. Locality-aware parallel sparse matrix-vector and matrix-transpose-vector multiplication on many-core processors. *Parallel and Distributed Systems, IEEE Transactions on*, PP(99):1–1, 2015.

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