

- Stop watchdog timer.
- Set DCO 1 Mhz, and enable VLO
- Init P1.1 to generate interrupt with rising edge (but not enable interrupt now)
- Init, clear, write name and number
- Init unused pins as input via internal pull up resistor

Init TimerA_1:

- Load TA1CCR0 the value 12000 to generate interrupt every seconds
- Enable CCIE bit to generate interrupt
- ACLK, no prescale, clear current TAR value
- Start timer with continous mode

Enter LPM3 and enable Global Interrupt Enable bit









